	SPECIFICATIONS					
CUSTOMER		:				
SAMPLE CODE		:	SH800480T037-ZAA			
MASS PRODUCTION CODE		:	PH800480T037-ZAA			
SAMPLE VERSION		:	01			
	SPECIFICATIONS EDITION	:	002			
	DRAWING NO. (Ver.)	:	LMD-PH800480T037-ZAA (Ver.001)			
	PACKAGING NO. (Ver.)		PKG-PH800480T037-ZAA (Ver.001)			

Customer Approved

ApprovedCheckedDesigner廖志豪張慶源陳宗淇Rex LiaoYuan ChangHoward Chen

☐ Preliminary specification for design input

Specification for sample approval

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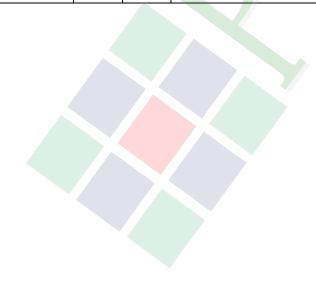
Http://www.powertip.com.tw

NO.PT-A-005-8



History of Version

Date (mm / dd / yyyy)	<u>Ver.</u>	Edi.	<u>Description</u>	<u>Page</u>	Design by
12/13/2023	01	001	Preliminary.	-	Howard
03/15/2024	01	002	Frist Sample.	1	Howard





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Appendix:

1.LCM Drawing

2.LCM Packaging Specifications



1. SPECIFICATIONS

1.1 Features

<u>Item</u>	Standard Value
Display Resolution	800 * 3 (RGB) * 480 Dots
LCD Type	Full Viewing Angle, Normally Black , Transmissive type
Screen size(inch)	7.0 inch
Color configuration	RGB Vertical Strip
Backlight Type	White LED B/L
Weight	106g
Interface	RGB
Other(controller/driver IC)	ST72568 (Or Compatible IC)
ROHS	THIS PRODUCT CONFORMS THE ROHS OF PTC Detail information please refer website: http://www.powertip.com.tw/news_detail.php?Key=1&cID=1

1.2 Mechanical Specifications

<u>ltem</u>	<u>Standard Value</u>	<u>Unit</u>
Outline Dimension	164.9(W) x 100.0(L) x 3.4(H)	mm

LCD panel

<u>Item</u>	Standard Value	<u>Unit</u>
Active Area	153.84 (W) x 85.632(L)	mm
Pixel Size	0.1923(W) * 0.1784(H)	mm

Note: For detailed information please refer to LCM drawing



1.3 Absolute Maximum Ratings

Module

<u>ltem</u>	<u>Symbol</u>	Condition	Min.	Max.	Unit	Remark
Power Supply for TFT Panel	DVDD	GND=0	-0.3	4.0	V	
Operating Temperature	Top (Ts)	Note 1	-20	+70	ů	-
Storage Temperature	T _{ST} (Ta)	Note 2	-30	+80	°C	

The absolute maximum rating values of this product are not allowed to be exceeded at any time. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

Note 1: Ts is the temperature of panel's surface

Note 2: Ta is the ambient temperature of samples

1.4 DC Electrical Characteristics

Module GND = 0V, Ta = $25^{\circ}C$

<u>ltem</u>	Symbol	<u>Condition</u>	Min.	Typ.	Max.	<u>Unit</u>
Power Supply for TFT Panel	DVDD	GND=0V	3.1	3.3	3.6	V
Input Voltage for	ViH	GND=0V	0.7DVDD	-	DVDD	V
TFT Panel	VIL	GND=0V	0	ı	0.3DVDD	
Supply Current for TFT Panel	DIDD	DIDD@DVDD=3.3V	-	100	150	mA



1.5 Optical Characteristics

TFT LCD Module

DVDD= 3.3 V, Ta=25°C

<u>ltem</u>	<u>s</u>	<u>ymbol</u>	<u>Condition</u>	Min.	Typ.	Max.	unit	Ξ
Response time	,	Tr+Tf	Ta = 25°C θX, θY = 0°	-	30	45	ms	Note 2
	Тор	θΥ+			80	1		
Viewing angle	Bottom	θΥ-	CR ≥ 10		80	ı	Deg.	Note 4
viewing angle	Left	θΧ-	CR 2 10		80	ı	Deg.	Note 4
	Right	θΧ+			80	-		
Contrast ratio		CR		500	600	-	-	Note 3
	White	Χ		0.23	0.28	0.33		
	VVIIILE	Υ	Ta = 25°C θX , θY = 0°	0.31	0.36	0.41		
Color of CIE	Red	Χ		0.53	0.58	0.63		
Coordinate		Υ		0.32	0.37	0.42		Note1
(With B/L)	Green	Х	0, 01 - 0	0.27	0.32	0.37	-	Note
(VVIIII D/L)	Green	Υ		0.56	0.61	0.66		
	Blue	Х		0.08	0.13	0.18		
	Diue	Υ		0.08	0.13	0.18		
Average Brightness Pattern=white		IV	IF=160mA	310	500	-	cd/m2	Note1
display (With B/L)*2								
Uniformity (With B/L) *1		ΔΒ	IF=160mA	70	-	-	%	Note1



Note 1:

*1: △B=B(min) / B(max) * 100%

*2: Measurement Condition for Optical Characteristics:

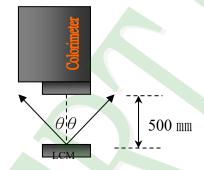
a: Environment: 25°C±5°C / 60±20%R.H, no wind, dark room below 10 Lux at typical lamp current and typical operating frequency

b: Measurement Distance: 500 ± 50 mm, $(\theta = 0^{\circ})$

c: Equipment: TOPCON BM-7 fast, (field 1°), after 10 minutes operation

d: The uncertainty of the C.I.E coordinate measurement ±0.01, Average Brightness ± 4%





Colorimeter=BM-7 fast

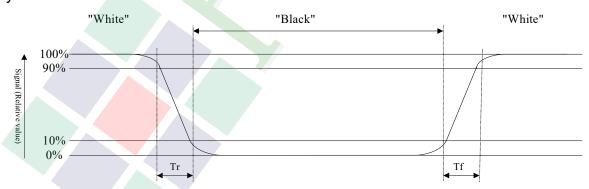
To be measured at the center area of panel with a viewing cone of 1° by Topcon luminance meter BM-7, after 10 minutes operation (module)

Note 2: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time), respectively. The response time is defined as the time interval between the 10% and 90% of Amplitudes.

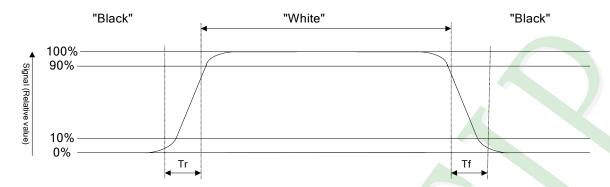
Refer to figure as below:

Normally White





Normally Black



Note 3: Definition of contrast ratio:

Contrast ratio is calculated with the following formula

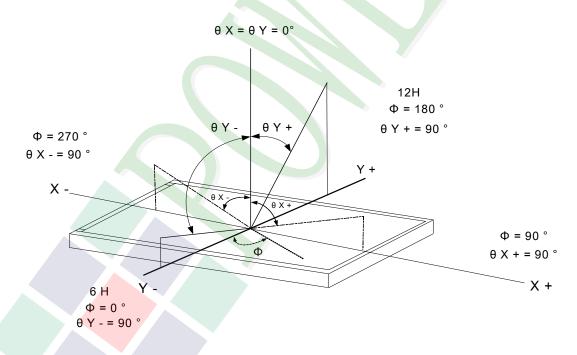
Photo detector output when LCD is at "White" state

Contrast ratio (CR) =

Photo detector output when LCD is at "Black" state

Note 4: Definition of viewing angle:

Refer to figure as below:





1.6 Backlight Characteristics

Maximum Ratings

<u>Item</u>	<u>Symbol</u>	Min.	Max.	<u>Unit</u>	<u>Remark</u>
LED Forward Current	l _F	-	30	mA	
LED Reverse Voltage	VR	-	5.0	V	Each LED
Power Dissipation	PD		99	mW	

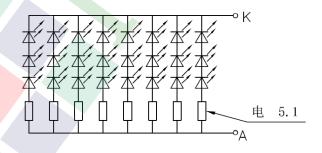
Electrical / Optical Characteristics

<u>Item</u>	<u>Symbol</u>	Conditions	Min.	Тур.	Max.	<u>Unit</u>
Forward Voltage	VF		8.4	9.0	10.2	V
Average Brightness (Without LCD)	IV	IF= 160 mA	6300	7560	-	cd/m ²
CIE Color Coordinate	X		0.25	_	0.31	
(Without LCD)	Y		0.26	-	0.32	1
Color			White			

Note 1: The LED Supply Voltage is defined by the number of LED at Ta=25℃ and I∟=160mA.

Note 2: The "LED life time" is defined as the module brightness decrease to 50% original brightness at Ta=25°C and I∟=160 mA. The LED life time could be decreased if operating I∟ is larger than 160mA.

B/L Internal Circuit Diagram



Other Description

Item	Conditions	Description
Life Time	Ta =25°ℂ IF=160 mA	20,000 hrs



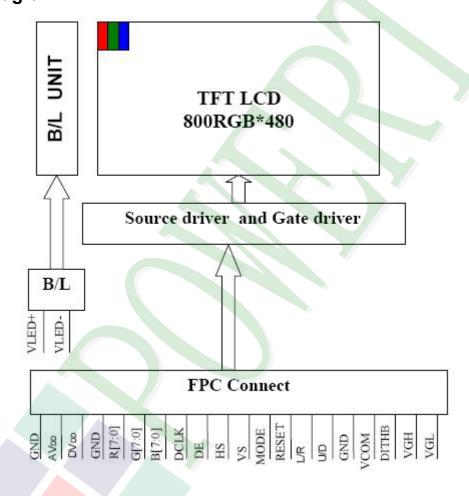
2. Module Structure

2.1 Counter Drawing

2.1.1 LCM Mechanical Diagram

* See Appendix

2.1.2 Block Diagram





2.2 Interface Pin Description

TFT LCM Interface

Pin#	<u>Name</u>	<u>Description</u>
1	VLED+	Power For LED backlight (+).
2	VLED+	Power For LED backlight (+).
3	VLED-	Power For LED backlight (-).
4	VLED-	Power For LED backlight (-).
5	GND	Power ground.
6	VCOM	No Function, Not Connection
7	DVDD	Power for Digital Circuit.
8	MODE	No Function, Not Connection
9	DE	Input data enable control. When DE mode, active High to enable data
10	VS	Vertical sync signal. Negative polarity
11	HS	Horizontal sync signal. Negative polarity
12	B7	Blue Data (MSB).
13	B6	Blue Data.
14	B5	Blue Data.
15	B4	Blue Data.
16	В3	Blue Data.
17	B2	Blue Data.
18	B1	Blue Data.
19	В0	Blue Data (LSB).
20	G7	Green Data (MSB).
21	G6	Green Data.
22	G5	Green Data.
23	G4	Green Data.
24	G3	Green Data.
25	G2	Green Data.
26	G1	Green Data.
27	G0	Green Data (LSB).
28	R7	Red Data (MSB).
29	R6	Red Data.



Pin#	<u>Name</u>	<u>Description</u>
30	R5	Red Data.
31	R4	Red Data.
32	R3	Red Data.
33	R2	Red Data.
34	R1	Red Data.
35	R0	Red Data (LSB).
36	GND	Power Ground
37	DCLK	Sample clock. Latch data at DCLK falling edge.
38	GND	Power Ground.
39	L/R	Horizontal scan direction control.
40	U/D	Vertical scan direction control
41	VGH	No Function,Not Connection
42	VGL	No Function,Not Connection
43	AVDD	No Function,Not Connection
44	RESET	Global reset pin. Low active.
45	NC	No connection.
46	VCOM	No Function,Not Connection
47	DITHB	No Function,Not Connection
48	GND	Power Ground.
49	NC	No connection.
50	NC	No connection.



2.3 Timing Characteristics

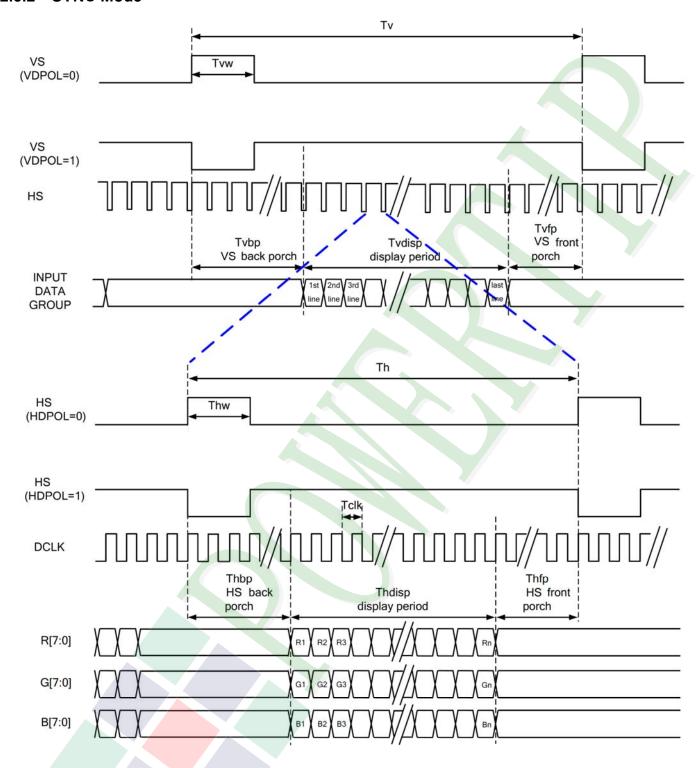
2.3.1 RGB Mode Selection Table

RGB Mode Selection Table	<u>DCLK</u>	<u>HSYNC</u>	<u>VSYNC</u>	<u>DE</u>
SYNC - DE Mode	Input	Input	Input	Input
SYNC Mode	Input	Input	Input	GND
DE Mode	Input	GND	GND	Input

Note: "Input" means these signals are driven by host side

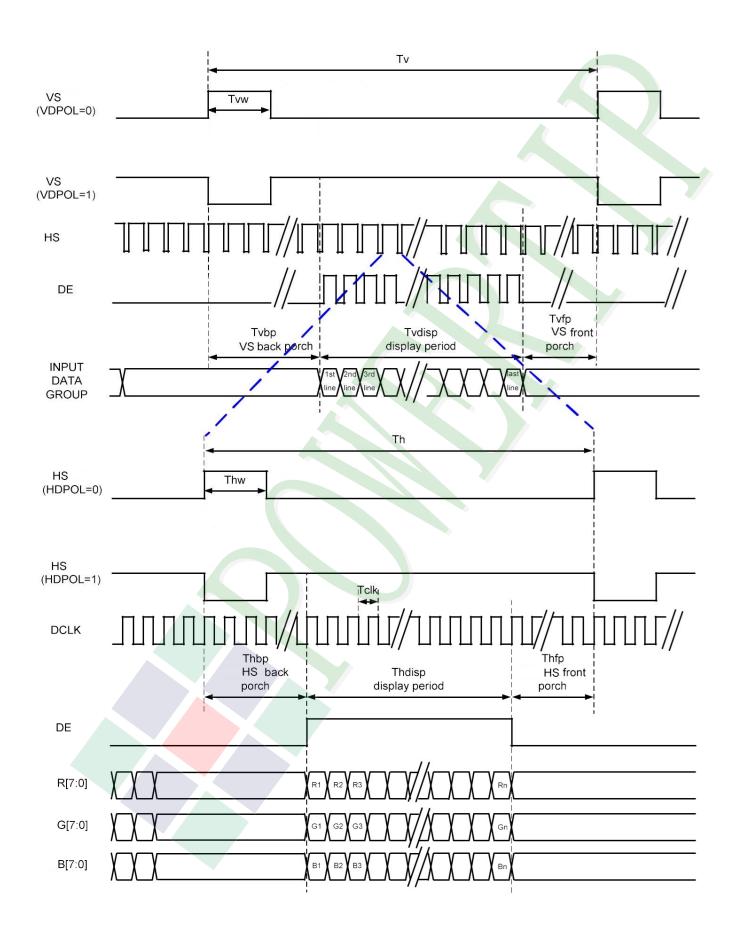


2.3.2 SYNC Mode



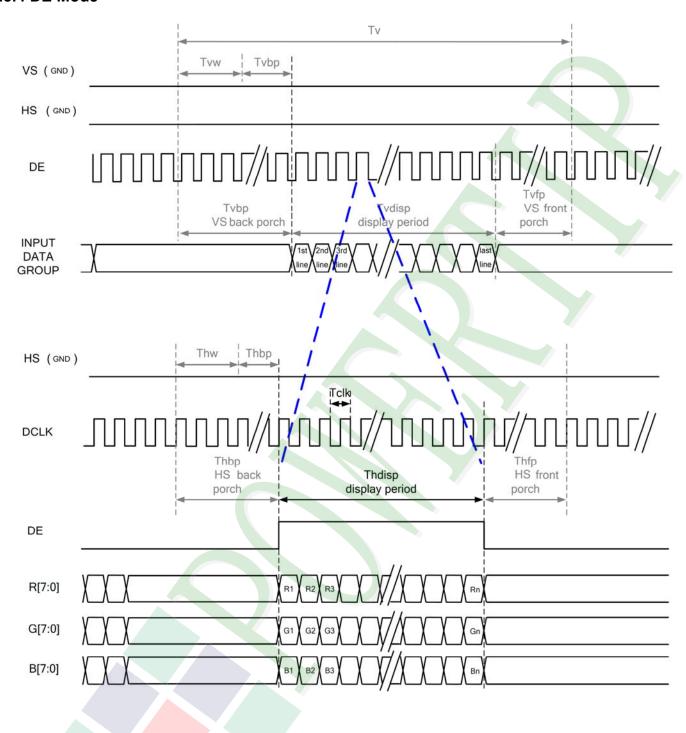


2.3.3 SYNC-DE Mode





2.3.4 DE Mode





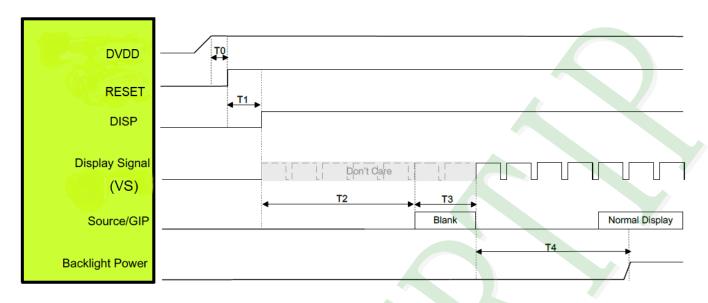
2.3.5 Parallel 24-bit RGB Input Timing Table

	Parallel 24-bit RGB Interface Timing Table						
	<u>ltem</u>	<u>Symbol</u>	Min.	Typ.	Max.	<u>Unit</u>	<u>Remark</u>
DC	LK Frequency	Fclk	23	25	27	MHz	
	Period Time	Th	808	816	896	DCLK	
	Display Period	Thdisp		800		DCLK	
HS	Back Porch	Thbp	4	8	24	DCLK	
	Front Porch	Thfp	4	8	24	DCLK	
	Pulse Width	Thw	2	4	8	DCLK	
	Period Time	Tv	496	512	528	HSYNC	
	Display Period	Tvdisp		480		HSYNC	
VS	Back Porch	Tvbp	8	16	24	HSYNC	
	Front Porch	Tvfp	8	16	24	HSYNC	
	Pulse Width	Tvw	2	4	8	HSYNC	



2.3.6 Power On Sequence

1 Power Mode



<u>Symbol</u>	<u>Description</u>	Min. Time	<u>Unit</u>
T0	System power stability to /RESET signal	≥1	ms
T1	/RESET= "High" to DISP="High"	≧10	ms
T2	DISP="High" to Source/GIP scan blank	85	ms
Т3	IC scan blanking signal	≧33	ms
T4	Display Signal output to Backlight Power on	≧100	ms

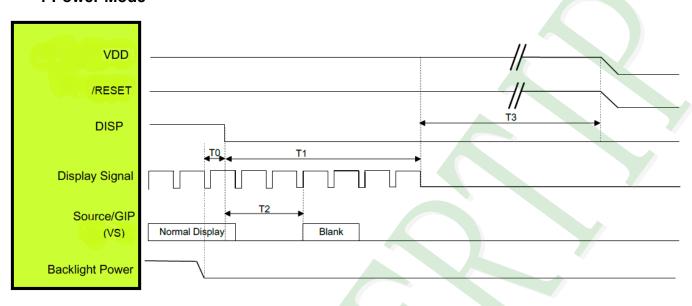
Note: 1. When DISP pull "H" or "L", IC will execute the internal power on or power off procedures .Please be careful about the timing of DISP and do not interrupt it during power on or power off procedure, otherwise unexpected errors will occur.

2. RGB interface Display signal: DCLK; VS; HS; DE; R[7:0]; G[7:0]; B[7:0] .



2.3.7 Power Off Sequence

1 Power Mode



<u>Symbol</u>	<u>Description</u>	Min. Time	<u>Unit</u>
T0	Backlight Power off to DISP="Low"	≥1	ms
T1	DISP="Low" to IC internal voltage discharge complete	≥100	ms
T2	DISP="Low" to Source/GIP scan blank (base on Display Signal Frame Rate 60Hz)	≦50	ms
Т3	IC internal voltage discharge is completed to VDD off	≥0	ms

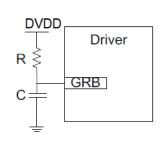
Note: 1. When DISP pull "H" or "L", IC will execute the internal power on or power off procedures .Please be careful about the timing of DISP and do not interrupt it during power on or power off procedure, otherwise unexpected errors will occur.

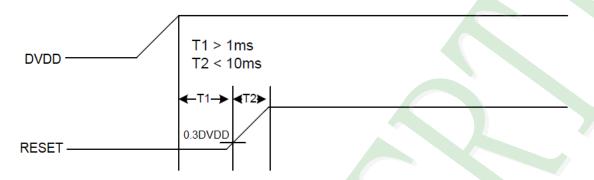
2. RGB interface Display signal: DCLK; VS; HS; DE; R[7:0]; G[7:0]; B[7:0] .



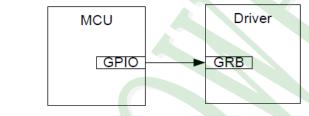
2.5 Reset timing

1.The /RESET pin with external RC circuit.





(2) The GRB pin controlled by MCU.

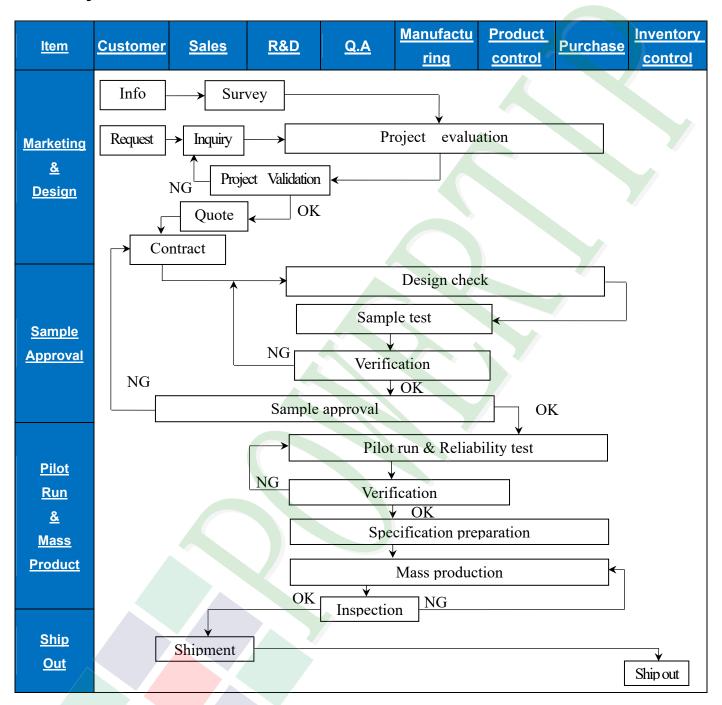




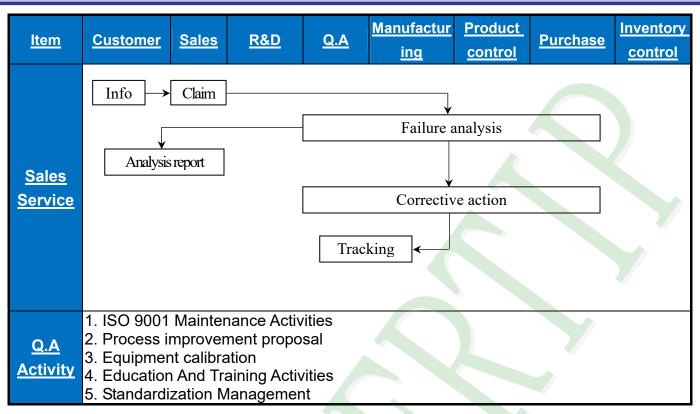


3. Quality Assurance System

3.1 Quality Assurance Flow Chart









3.2 Inspection Specification

◆Scope: The document shall be applied to TFT-LCD Module for 3. 5" −15″ (Ver.B01).

♦ Inspection Standard: MIL-STD-105E Table Normal Inspection Single Sampling Level II.

◆Equipment: Gauge, MIL-STD, Powertip Tester, Sample

◆Defect Level: Major Defect AQL: 0. 4; Minor Defect AQL: 1. 5

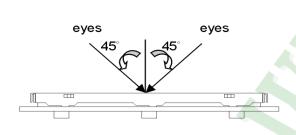
♦OUT Going Defect Level: Sampling

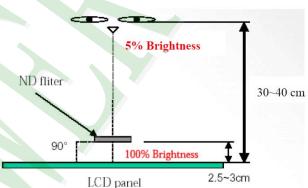
♦Standard of the product appearance test:

a. Manner of appearance test:

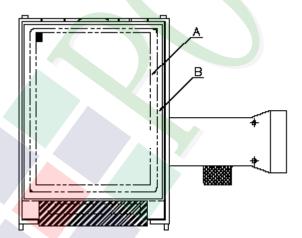
(1). The test best be under 20W×2 fluorescent light(about 300lux ~500lux) and distance of view must be at 30~40 cm.

(2). The test direction is base on about around 45° of vertical line.





(3). Definition of area.



A area: viewing area

B area: Outside of viewing area

(4). Standard of inspection: (Unit: mm)



◆Specification For TFT-LCD Module 3.5"~15":

<u>NO</u>	<u>ltem</u>	<u>Criterion</u>	Level				
		1.1 The part number is inconsistent with work order of production.	Major				
01	Product condition	1.2 Mixed product types.	Major				
		1.3 Assembled in inverse direction.	Major				
02	Quantity	2.1 The quantity is inconsistent with work order of production.	Major				
03	Outline dimension	3.1 Product dimension and structure must conform to structure diagram.	Major				
		4.1 Missing line character and icon.	Major				
		4.2 No function or no display.	Major				
		4.3 Display malfunction.	Major				
04	Electrical Testing	4.4 LCD viewing angle defect.					
		4.5 Current consumption exceeds product specifications.					
		4.6 Mura cannot be seen through 5% ND filter at 50% Gray, should be judged by the viewing angle of 90 degree.	Minor				
		Item Acceptance (Q'ty)					
		Bright Dot ≤ 4					
		Dot Dark Dot ≤ 5					
	Dot defect	Defect Joint Dot ≤ 3					
		Total ≤ 7					
05	(Bright dot,		Minor				
05	Dark dot)	5.1 Inspection pattern: full white, full black, Red, Green and blue screens.	IVIIIIOI				
	On -display	5.2 It is defined as dot defect if defect area > 1/2 dot.					
	on diopidy	5.3 The distance between two dot defect \geq 5 mm.					
		5.4 Bright dot : Dots appear bright and unchanged in visible with					
		5% ND filter is defined. 5.5 Tiny bright dot: bright dot area ≦1/2 dot.					
		a. Dots appear bright and unchanged in visible with 5% ND					
		filter is defined defect and is judged in accordance with 6.1					
		b. Dots invisible with 5% ND Filter is Ignored.					



◆Specification For TFT-LCD Module 3. 5″ ~15″:

<u>NO</u>	<u>ltem</u>	<u>Criterion</u> <u>L</u>				<u>Level</u>				
		6.1 Rd	6.1 Round type (Non-display or display):							
	Black or white Dot, scratch, contamination			on (diameter Φ ≤ 0 < Φ ≤ 0 Φ > 0 Total).25).50	Accep A area Ignore 5 0 5		(Q'ty) B are) a	
	Round type	6.2 Lir	ne type(No	n-display	or displ	ay):				
	$\begin{array}{c c} X & \hline \\ \hline & Y \\ \hline \end{array}$	<u>m</u>	nodule size	Length (L)	Wi	dth (W)		ccept (Q't	tance y) B area	
06	$\Phi = (x+y)/2$					W ≦ 0.03	Ign	ore		Minor
	Line type	<u>3.5</u>	" to less	L ≦ 10.0	0.03	<w ≦<br="">0.05 <w td="" ≦<=""><td></td><td>1</td><td>Ignore</td><td></td></w></w>		1	Ignore	
	₩ W		<u>9"</u>	L ≦5.0		0.10 W > 0.10	As ro	ound pe	ignore	
	L				Total	110	ţ	5		
						W ≦ 0.05	lgn	ore		
		9"	to 15"	L ≦ 10.0	0.05	<w ≦<br="">0.10</w>		5	Ignore	
						W > 0.10		ound pe		
					Total			5		
		<u>.</u>				Acce	ptance	e (Q't	v)	
		DI		diameter:	_	A area			area	
07	Polarizer			$ \Phi \leq 0.25 $ $ \Phi \leq 0.50 $		Ignore 4				Minor
	Bubble			$\Phi \leq 0.80$		1		lg	nore	
				Φ >0.80		0				
			To	otal		5				



◆Specification For TFT-LCD Module 3. 5″~15″:

NO	Item	<u>Criterion</u>			
NO	<u>Item</u>	Symbols: X: The length of crack Z: The thickness of crack T: The thickness of glass 8. 1 General glass chip: 8. 1. 1 Chip on panel surface and crack between panels:	Level		
08	The crack of glass	SP SP SP [NG]	Minor		
		Seal width Z			
		X Y Z Crack can't enter			
		yiewing area ≤ 1/2 t			
		\leq a Crack can't exceed the half of SP width. 1/2 t $<$ Z \leq 2 t			



◆Specification For TFT-LCD Module 3. 5″~15″:

<u>NO</u>	<u>Item</u>	<u>Criterion</u>	Level
		X: The length of crack Z: The thickness of crack t: The thickness of glass 8.1.2 Corner crack:	
		X Y Z	
		≤1/5 a Crack can't enter viewing area Z ≤ 1/2 t	
		\leq 1/5 a Crack can't exceed the half of SP width. 1/2 t $<$ Z \leq 2 t	
08	The crack of glass	8.2 Protrusion over terminal: 8.2.1 Chip on electrode pad: X X X X X X X X	Minor



◆Specification For TFT-LCD Module 3. 5″ ~15″:

<u>NO</u>	<u>Item</u>	<u>Criterion</u>				
	Item The crack of glass	Symbols: X: The length of crack Z: The thickness of crack t: The thickness of glass 8.2.2 Non-conductive portion: X X X X X X X X X X X X	Level			



◆Specification For TFT-LCD Module 3. 5″ ~15″:

NO	<u>Item</u>	<u>Criterion</u>	Level
		9.1 Backlight can't work normally.	Major
09	Backlight elements	9.2 Backlight doesn't light or color is wrong.	Major
		9.3 Illumination source flickers when lit.	Major
	General	10.1 Pin type, quantity, dimension must match type in structure diagram.	Major
		10.2 No short circuits in components on PCB or FPC.	Major
40		10.3 Parts on PCB or FPC must be: no wrong parts, missing parts or excess parts.	Major
10	appearance	10.4 Product packaging must the same as specified on packaging specification sheet.	Minor
		10.5 The folding and peeled off in polarizer are not acceptable.	Minor
		10.6 The PCB or FPC between B/L assembled distance(PCB or FPC) is $~\leq 1.5$ mm.	Minor



4. Reliability Test

4.1 Reliability Test Condition

(Ver.B01)

<u>NO.</u>	TEST ITEM	TEST CONDITION		
1	High Temperature Storage Test	Keep in +80 ±5°C 240 hrs		
2	Low Temperature Storage Test	Keep in -30 ±5℃ 240 hrs		
3	High Temperature / High Humidity Storage Test	Keep in 60 °C / 90% R.H duration for 240 hrs (Excluding the polarizer)		
4	Temperature Cycling Storage Test	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$		
5	ESD Test	Air Discharge: Apply 2 KV with 5 times Discharge for each polarity +/- 1. Temperature ambiance: 15°C ~35°C 2. Humidity relative: 30% ~60% 3. Energy Storage Capacitance(Cs+Cd): 150pF±10% 4. Discharge Resistance(Rd): 330Ω±10% 5. Discharge, mode of operation: Single Discharge (time between successive discharges at least 1 sec) (Tolerance if the output voltage indication: ±5%)		
6	Vibration Test (Packaged)	 Sine wave 10~55 Hz frequency (1 min/sweep) The amplitude of vibration: 1.5 mm Each direction (X, Y, Z) duration for 2 hrs 		
7	Drop Test (Packaged)	Packing Weight (Kg) Drop Height (cm) 0 ~ 45.4 122 45.4 ~ 90.8 76 90.8 ~ 454 61 Over 454 46 Drop Direction : %1 corner / 3 edges / 6 sides each 1 time		

©Result Evaluation Criteria :

Under the display quality test conditions with normal operations with normal operation state.

Do not change these conditions as such changes may affect practical display function.

(Normal operation state) Temperature: +20~30°C Humidity: 50~70%

Atmospheric pressure: 86~106Kpa



5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

- 5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

5.2 HANDLING

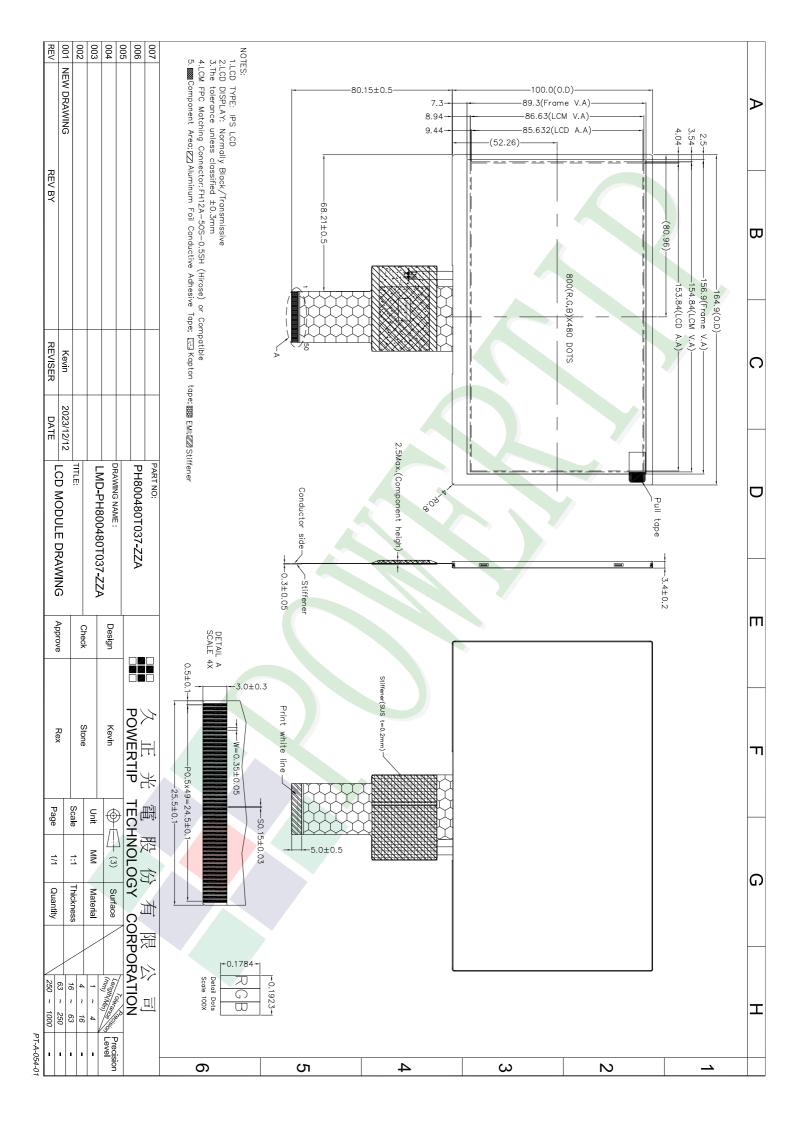
- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module, be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So, please handle it very carefully, do not touch, push or rub the exposed polarizing with anything harder than an HB pencil lead (glass, tweezers, etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands, this will stain the display area.
- 5.2.7 Do not use ketonic solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is $320 \pm 10^{\circ}$ C and $3 \sim 5$ sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM.
- 5.2.10 Caution! (LCM products with Capacitive Touch Panel)
 Strong EMI-sources such as switch-mode power supplies (SPS) can lead to touch malfunction (e.g., ghost-touches). Therefore, the touch needs to be thoroughly tested inside the target application.
- 5.2.11 CAUTION: Continuously displaying same static image will result in high possibility of image sticking/image burn-in effect due to TFT panel characteristic.
- 5.2.12 Double-sided tape designed to be attached with the customer's mechanical device, please follow up the rules and regulations published by the original manufacturer of double-side tape for the attachment operation.

5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is 25° C \pm 5° C and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush, shake, or jolt the module.

5.4 TERMS OF WARRANTY

- 5.4.1 Applicable warrant period
 The period is within thirteen months since the date of shipping out under normal using and storage conditions.
- 5.4.2 Unaccepted responsibility
 - This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.



	:.001			Approve	Check	Contact
	cuments NO. PKG-PH800480T	Packaging	Specifications	Rex	Rex	Stone
.Pac	ckaging Material: (per carton)					
No.	Item	Model	Dimensions (mm)	1Pcs Weight	Quantity	Total Weight
1	成品 (LCM)	PH800480T037-ZAA	164.9 X 100.0 X 3.4	0.106	60	6.36
2	靜電袋(1)Antistatic Bag	BAG240170ARABA	240 X 170	0.0048	60	0.288
3	上蓋(2)EPE	FOAM00000078	310 X 250 X 90	0.1	4	0.4
4	下座(3)EPE	FOAM00000079	310 X 250 X 100	0.17	4	0.68
5	保麗龍板(4)Polylon board	OTPLB00000017 BX52732536CDBA	510 X 310 X 15 527 X 325 X 360	0.025	3	0.075
6 7	外紙箱(5)Carton	DAJZ13ZJ30CDBA	321 A 323 A 300	1.092	1	1.092
8						
9						
	(2)上蓋 EPE 評電袋+LCM Antistatic Bag+LCM			(4)保麗龍机 Polylon l		
	(4					
	(3)下座 EPE Label TAPE	₩ W		·紙箱 Carton		