



SPECIFICATIONS

CUSTOMER	:	_____
SAMPLE CODE	:	SH800480T013-IBC24
MASS PRODUCTION CODE	:	PH800480T013-IBC24
SAMPLE VERSION	:	01
SPECIFICATIONS EDITION	:	001
DRAWING NO. (Ver.)	:	LMD-PH800480T013-IBC24 (Ver.001)
PACKAGING NO. (Ver.)	:	PKG-PH800480T013-IBC24 (Ver.001)

Customer Approved

Date:

Approved	Checked	Designer
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- Preliminary specification for design input
- Specification for sample approval

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1. SPECIFICATIONS

1.1 Features

Item	Standard Value
Display Type	800 * (RGB) * 480
LCD Type	a-Si TFT , Normally white , Transmissive type
Screen size(inch)	7.0 inch
Viewing Direction	6 O'clock
Backlight Type	LED B/L
Interface	RGB Interface
ROHS	THIS PRODUCT CONFORMS THE ROHS OF PTC Detail information please refer website : http://www.powertip.com.tw/news_detail.php?Key=1&cID=1

1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	192.96 (W) * 110.76 (L) *5.3 (H)	mm

LCD panel

Item	Standard Value	Unit
Active Area (LCD)	154.08 (W) * 85.92 (L)	mm

Note : For detailed information please refer to LCM drawing.

1.3 Absolute Maximum Ratings

Module

Item	Symbol	Condition	Min.	Max.	Unit	Remark
Power Supply Voltage	DV_{DD}	GND=0	-0.3	5.0	V	-
	AV_{DD}		6.5	13.5	V	
	V_{GH}		-0.3	40.0	V	
	V_{GL}	AGND=0	-20.0	0.3	V	
	$V_{GH} - V_{GL}$	-	-	40.0	V	
Operating Temperature	T_{OP}	-	-20	70	°C	
Storage Temperature	T_{ST}	-	-30	80	°C	

The absolute maximum rating values of this product are not allowed to be exceeded at any times. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

1.4 DC Electrical Characteristics

GND = 0V, $T_a = 25^\circ\text{C}$

Item	Symbol	Min.	Typ.	Max.	Unit	Remark
Supply Voltage	DV_{DD}	3.0	3.3	3.6	V	-
	V_{GH}	15.3	16.0	16.7		
	V_{GL}	-7.7	-7.0	-6.3		
	AV_{DD}	10.2	10.4	10.6		
VCOM	V_{COM}	3.4	3.6	4.2	V	
Input signal Voltage	V_{IH}	$0.7DV_{DD}$	-	DV_{DD}	V	
	V_{IL}	0	-	$0.3DV_{DD}$		

1.5 Optical Characteristics

TFT LCD Module

 $DV_{DD} = 3.3 V, Ta=25^{\circ}C$

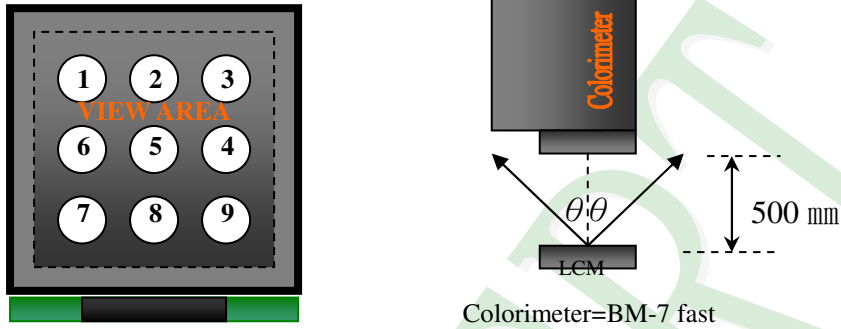
Item		Symbol	Condition	Min.	Typ.	Max.	unit	-
Response time	Rise	Tr	-	-	10	20	ms	Note 2
	Fall	Tf		-	15	30		
Viewing angle	Top	$\theta Y+$	CR \geq 10	40	50	-	Deg.	Note 4
	Bottom	$\theta Y-$		60	70	-		
	Left	$\theta X-$		60	70	-		
	Right	$\theta X+$		60	70	-		
Contrast ratio		CR		400	500	-	-	Note 3
Color of CIE Coordinate (With B/L&TP)	White	X	If= 160 mA	0.24	0.29	0.34	-	Note1
		Y		0.27	0.32	0.37		
	Red	X		0.51	0.56	0.61		
		Y		0.29	0.34	0.39		
	Green	X		0.28	0.33	0.38		
		Y		0.54	0.59	0.64		
	Blue	X		0.10	0.15	0.20		
		Y		0.02	0.07	0.12		
Average Brightness Pattern=white display (With B/L&TP)*1		IV	If= 160 mA	310	440	-	cd/m ²	Note1
Uniformity (With B/L&TP)*2		ΔB	If= 160 mA	70	-	-	%	Note1

Note 1:

*1 : $\Delta B = B(\min) / B(\max) * 100\%$

*2 : Measurement Condition for Optical Characteristics:

- a : Environment: $25^{\circ}\text{C} \pm 5^{\circ}\text{C}$ / $60 \pm 20\% \text{R.H}$, no wind , dark room below 10 Lux at typical lamp current and typical operating frequency.
- b : Measurement Distance: $500 \pm 50 \text{ mm}$, ($\theta = 0^{\circ}$)
- c : Equipment: TOPCON BM-7 fast , (field 1°) , after 10 minutes operation.
- d : The uncertainty of the C.I.E coordinate measurement ± 0.01 , Average Brightness $\pm 4\%$



To be measured at the center area of panel with a viewing cone of 1° by Topcon luminance meter BM-7, after 10 minutes operation (module)

Note2: Definition of response time:

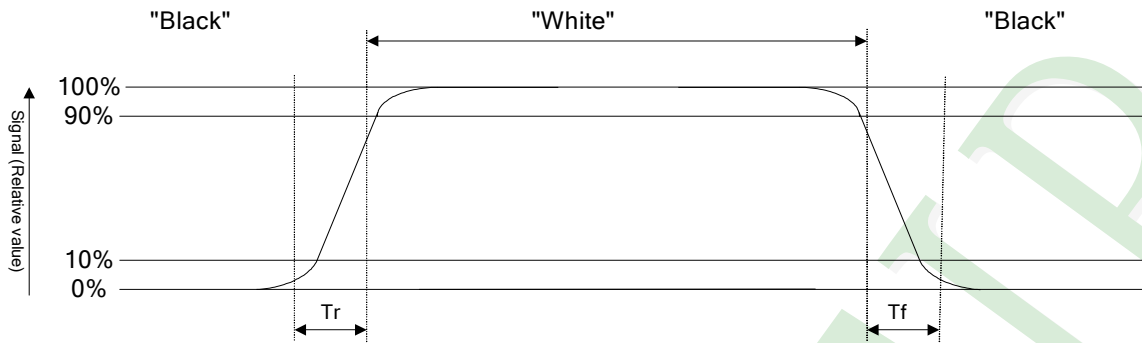
The output signals of photo detector are measured when the input signals are changed from "black" to "white"(falling time) and from "white" to "black"(rising time), respectively. The response time is defined as the time interval between the 10% and 90% of Amplitudes.

Refer to figure as below:

Normally White



Normally Black



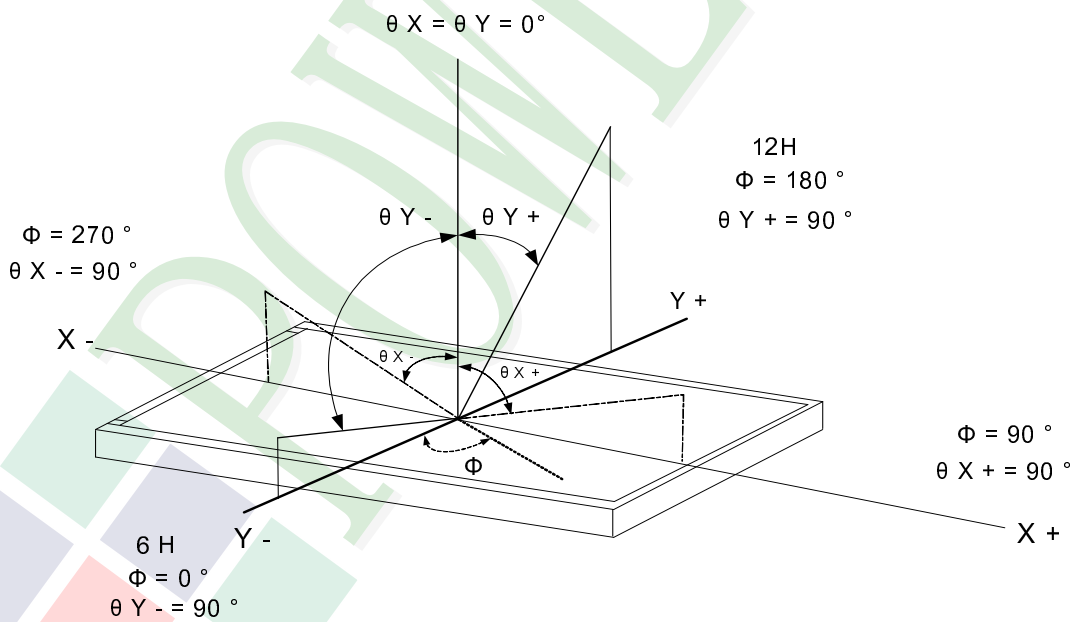
Note3: Definition of contrast ratio:

Contrast ratio is calculated with the following formula

$$\text{Contrast ratio (CR)} = \frac{\text{Photo detector output when LCD is at "White" state}}{\text{Photo detector output when LCD is at "Black" state}}$$

Note4: Definition of viewing angle:

Refer to figure as below:



1.6 Backlight Characteristics

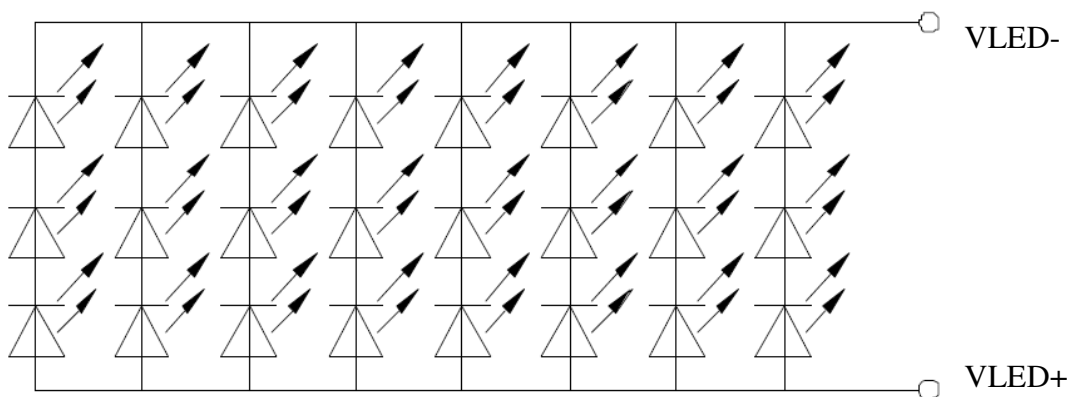
Maximum Ratings

Item	Symbol	Conditions	Min.	Max.	Unit
LED Forward Current	IF	Ta =25°C	-	30*8	mA
LED Reverse Voltage	VR		-	5	V
Power consumption	Pd		-	90*24	mW

Electrical / Optical Characteristics

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit
Forward Voltage	VF	If= 160 mA	9.0	9.6	10.2	V
Average Brightness (Without LCD &T/P)	IV		8600	10300	-	cd/m ²
CIE Color Coordinate (Without LCD &T/P)	X		0.25	0.28	0.31	-
	Y		0.26	0.29	0.32	
Color	White					

Internal Circuit



Other Description

Item	Conditions	Description
Life Time	Ta =25°C IF= 160 mA	20000 hrs

1.7 Touch Panel Characteristics

Features

Item	Standard Value
Touch Panel Size	7"
Touch type	Projective Capacitive Touch Panel
Input Method	Finger / 10 Points touch
Output Interface	I ² C
IC	ICNT8952
I ² C Address	0x48(7 Bits)

I²C Address

Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
1	0	0	1	0	0	0	R/W

Bit 0: 0 for Write / 1 for Read

Mechanical Specifications

Item	Standard Value	Unit
Viewing Area	154.88 (W) x 86.72 (H)	mm

Absolute Maximum Ratings

Item	Symbol	Condition	Min.	Max.	Unit
Supply voltage	TPVDD	-	-0.3	+6.0	V
Operating Temperature	T _{OP}	-	-20	+70	°C
Storage Temperature	T _{ST}	-	-30	+80	°C

DC Electrical Characteristics

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Power Supply Voltage	TPVDD	-	2.8	3.3	3.6	V
Input High Voltage	VIH	-	0.85 * TPVDD	-	-	V
Input Low Voltage	VIL	-	-	-	0.15 * TPVDD	V

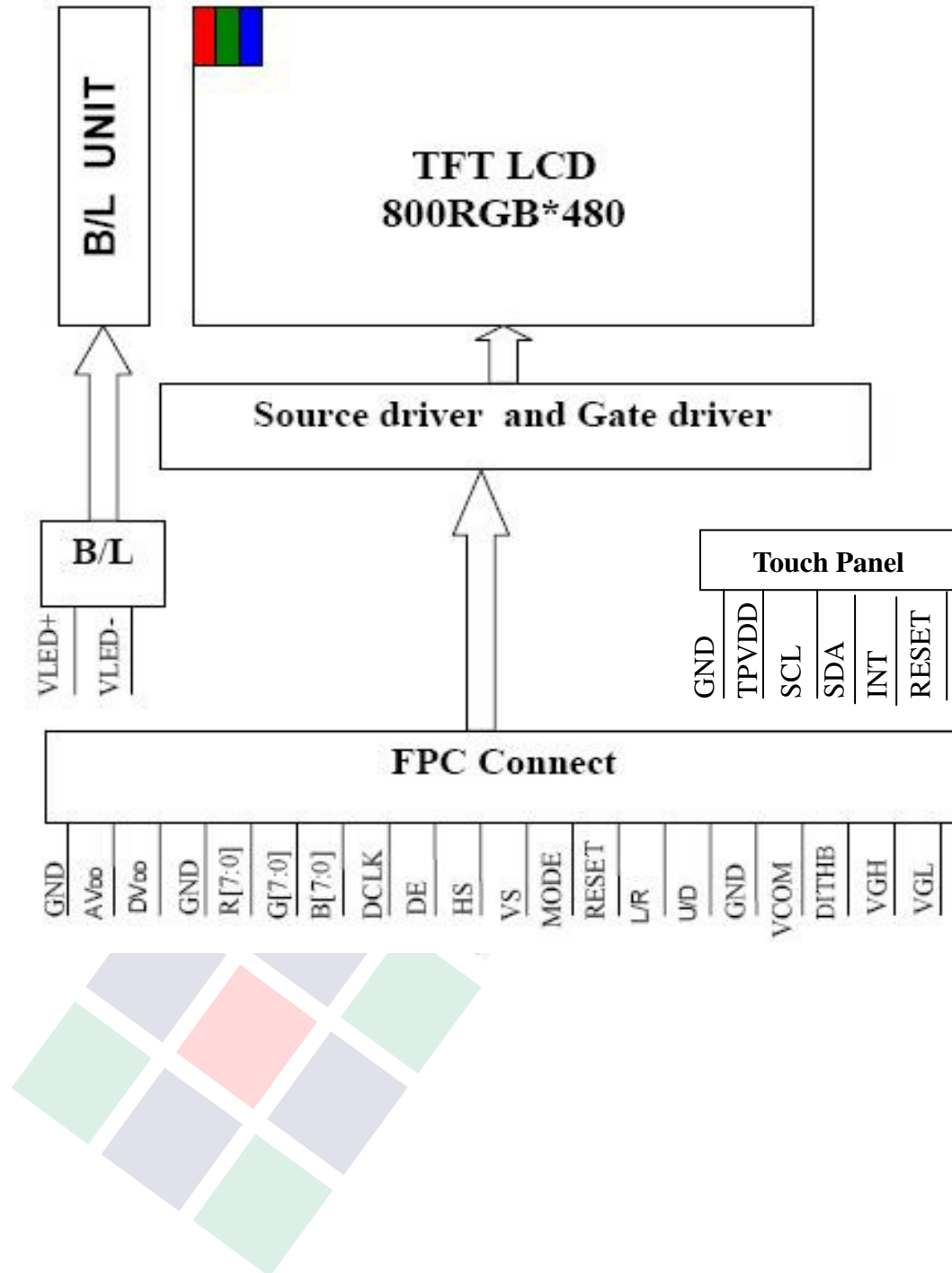
2. MODULE STRUCTURE

2.1 Counter Drawing

2.1.1 LCM Mechanical Diagram

* See Appendix

2.1.2 Block Diagram



2.2 Interface Pin Description

Pin NO.	SYMBOL	DESCRIPTION	Type:Remark
1	V _{LED+}	Power For LED backlight (+).	Power
2	V _{LED+}	Power For LED backlight (+).	Power
3	V _{LED-}	Power For LED backlight (-).	Power
4	V _{LED-}	Power For LED backlight (-).	Power
5	GND	Power ground.	Power
6	V _{com}	Common voltage.	I
7	DV _{DD}	Power for Digital Circuit.	I
8	MODE	DE/SYNC mode select.	I,Note 1
9	DE	Data Input Enable.	I
10	VS	Vertical Sync Input.	I
11	HS	Horizontal Sync Input.	I
12	B7	Blue Data (MSB).	I
13	B6	Blue Data.	I
14	B5	Blue Data.	I
15	B4	Blue Data.	I
16	B3	Blue Data.	I
17	B2	Blue Data.	I
18	B1	Blue Data.	I:Note 2
19	B0	Blue Data (LSB).	I:Note 2
20	G7	Green Data(MSB).	I
21	G6	Green Data.	I
22	G5	Green Data.	I
23	G4	Green Data.	I
24	G3	Green Data.	I
25	G2	Green Data.	I
26	G1	Green Data.	I:Note 2
27	G0	Green Data (LSB).	I:Note 2
28	R7	Red Data (MSB).	I
29	R6	Red Data.	I
30	R5	Red Data.	I
31	R4	Red Data.	I
32	R3	Red Data.	I
33	R2	Red Data.	I
34	R1	Red Data.	I:Note 2
35	R0	Red Data (LSB).	I:Note 2
36	GND	Power Ground	Power
37	DCLK	Sample clock	I:Note 3

Pin NO.	SYMBOL	DESCRIPTION	Type:Remark
38	GND	Power Ground.	Power
39	L/R	Left / right selection.	I:Note 4
40	U/D	Left / right selection.	I:Note 4
41	V _{GH}	Gate On Voltage.	Power
42	V _{GL}	Gate OFF Voltage.	Power
43	AV _{DD}	Power for Analog Circuit.	Power
44	RESET	Global reset pin.	I:Note 5
45	NC	No connection.	-
46	V _{COM}	Common Voltage.	I
47	DITHB	Dithering Function.	I:Note 6
48	GND	Power Ground.	Power
49	NC	No connection.	-
50	NC	No connection.	-

T/P PIN

Pin No.	Symbol	Function
1	GND	Ground. (T/P)
2	TPVDD	Power.(T/P)
3	SCL	I ² C Clock. (T/P)
4	SDA	I ² C Data.(T/P)
5	INT	Active Low. (T/P)
6	RESET	RESET. (T/P)

I: input

Note 1: DE/SYNC mode select. Normally pull high.

When select DE mode, MODE="1", VS and HS must pull high.

When select SYNC mode, MODE="0", DE must be grounded.

Note 2: When input 18 bits RGB data, the two low bits of R,G and B data must be grounded.

Note 3: Data shall be latched at the falling edge of DCLK.

Note 4: Selection of scanning mode.

Setting of scan control input		Scanning direction
U/D	L/R	
GND	DV _{DD}	Up to down, left to right
DV _{DD}	GND	Down to up, right to left
GND	GND	Up to down, right to left
DV _{DD}	DV _{DD}	Down to up, left to right

Note 5: Global reset pin. Active low to enter reset state. Suggest to connect with an RC reset circuit for stability. Normally pull high.

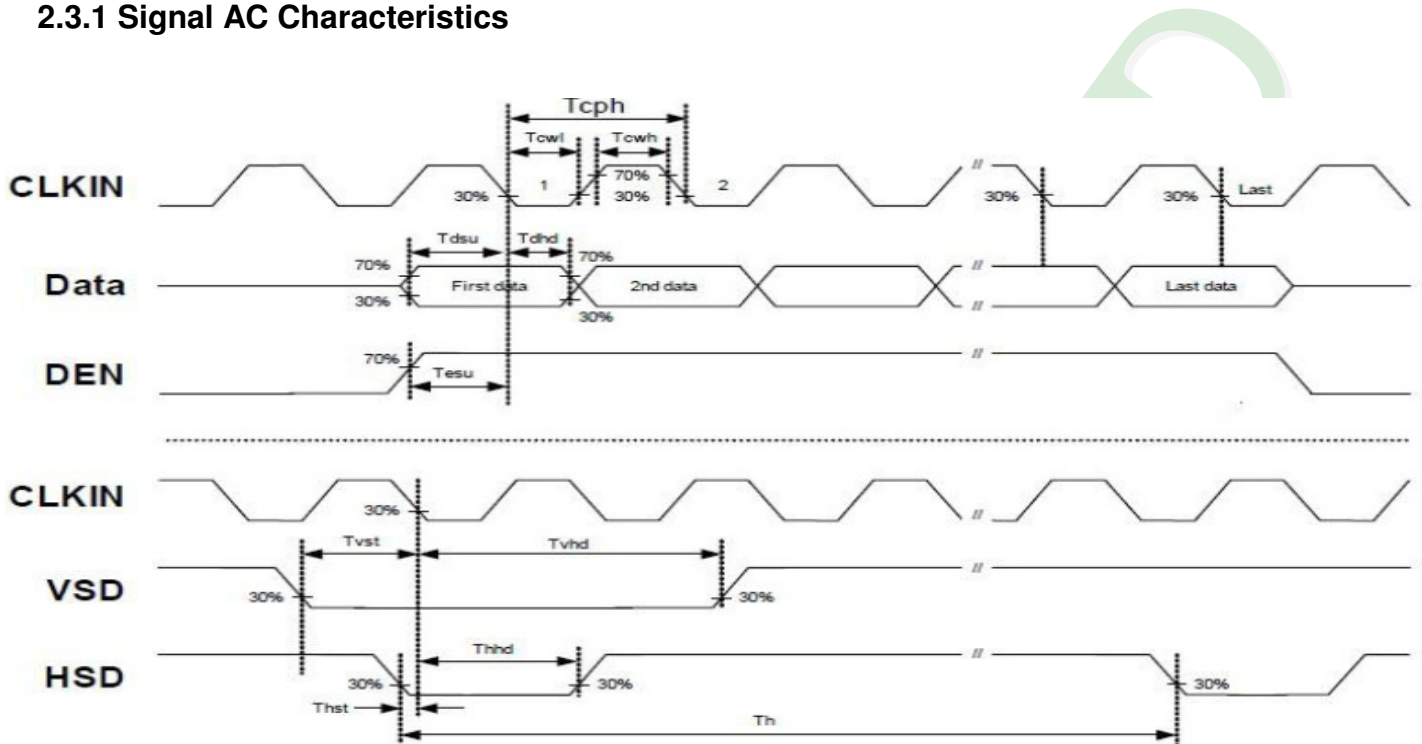
Note 6: Dithering function enable control, normally pull high.

When DITHB="1", Disable internal dithering function.

When DITHB="0", Enable internal dithering function.

2.3 Timing Characteristics

2.3.1 Signal AC Characteristics



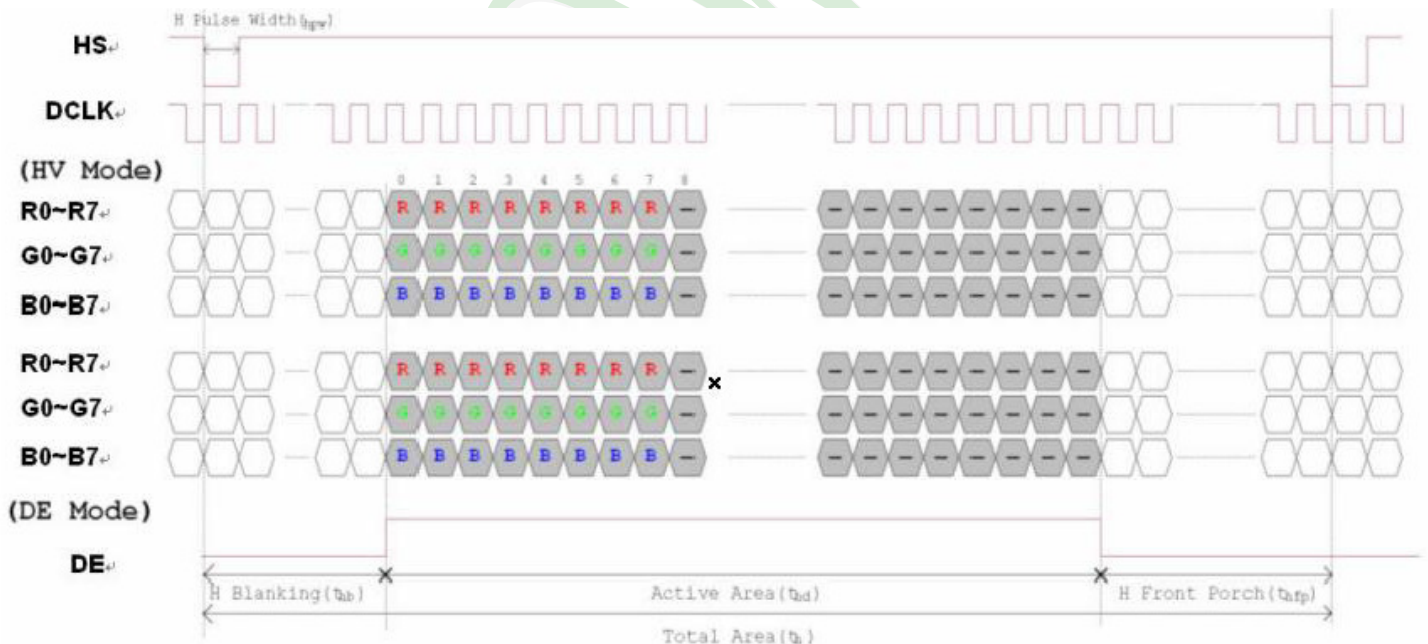
Item	Symbol	Values			Unit	Remark
		Min	Typ	Max.		
HS setup time	T _{hst}	8	-	-	ns	
HS hold time	T _{hhd}	8	-	-	ns	
VS setup time	T _{vst}	8	-	-	ns	
VS setup time	T _{vhd}	8	-	-	ns	
VS setup time	T _{dsu}	8	-	-	ns	
VS setup time	T _{dhd}	8	-	-	ns	
DE setup time	T _{esu}	8	-	-	ns	
DE hole time	T _{ehd}	8	-	-	ns	
DV _{DD} Power On Slew rate	T _{POR}	-	-	20	ms	From 0 to 90%DV _{DD}
RESET pulse width	T _{Rst}	1	-	-	ms	
DCLK cycle time	T _{coh}	20	-	-	ns	
DCLK pulse duty	T _{cwh}	40	50	60	%	

2.3.2 Input Timing Setting

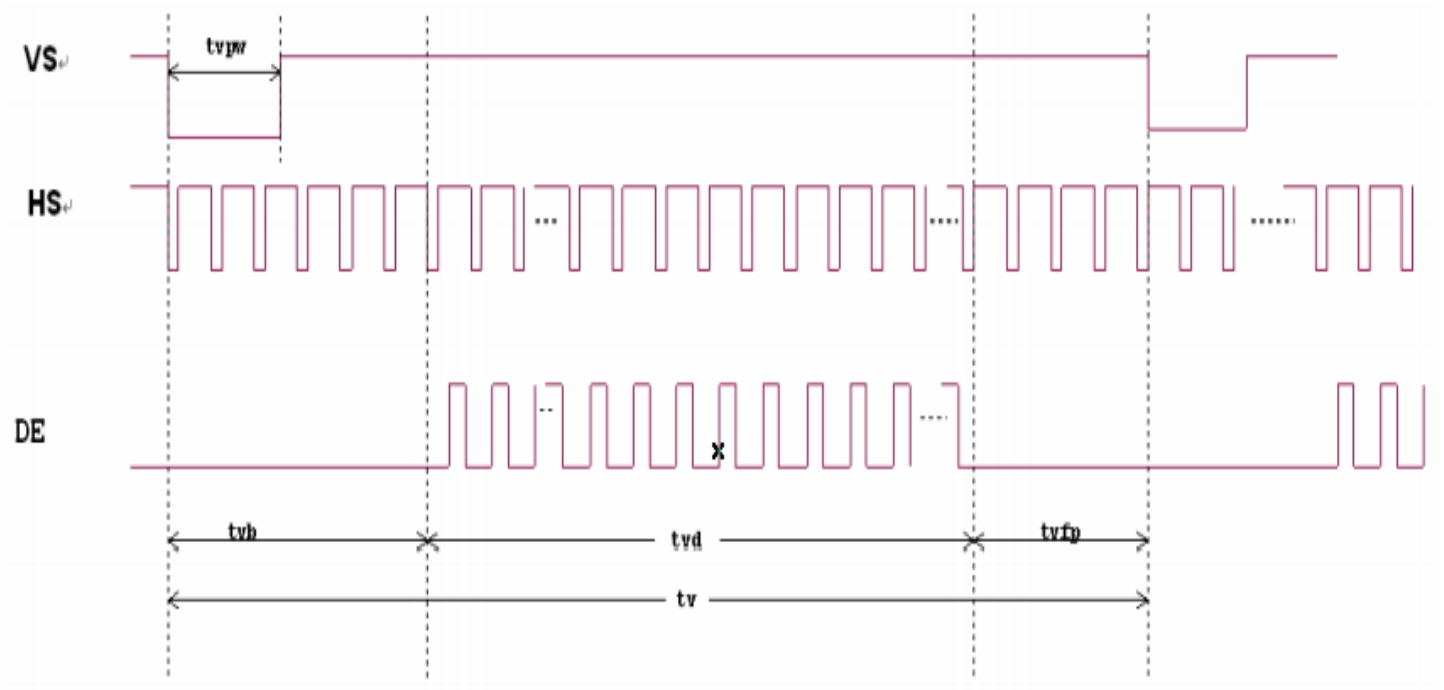
Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Horizontal Display Area	Thd		800		DCLK	
DCLK Frequency	Fclk	26.4	33.3	46.8	MHz	
One Horizontal Line	Th	862	1056	1200	DCLK	
HS pulse width	Thpw	1		40	DCLK	
HS Blanking	Thb	46	46	46	DCLK	
HS Front Porch	Thfp	16	210	354	DCLK	

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Vertical Display Area	Tvd		480		TH	
VS period time	Tv	510	525	650	TH	
VS pulse width	Tvpw	1		20	TH	
VS Blanking	Tvb	23	23	23	TH	
VS Front Porch	Tvfp	7	22	147	TH	

Horizontal input timing diagram

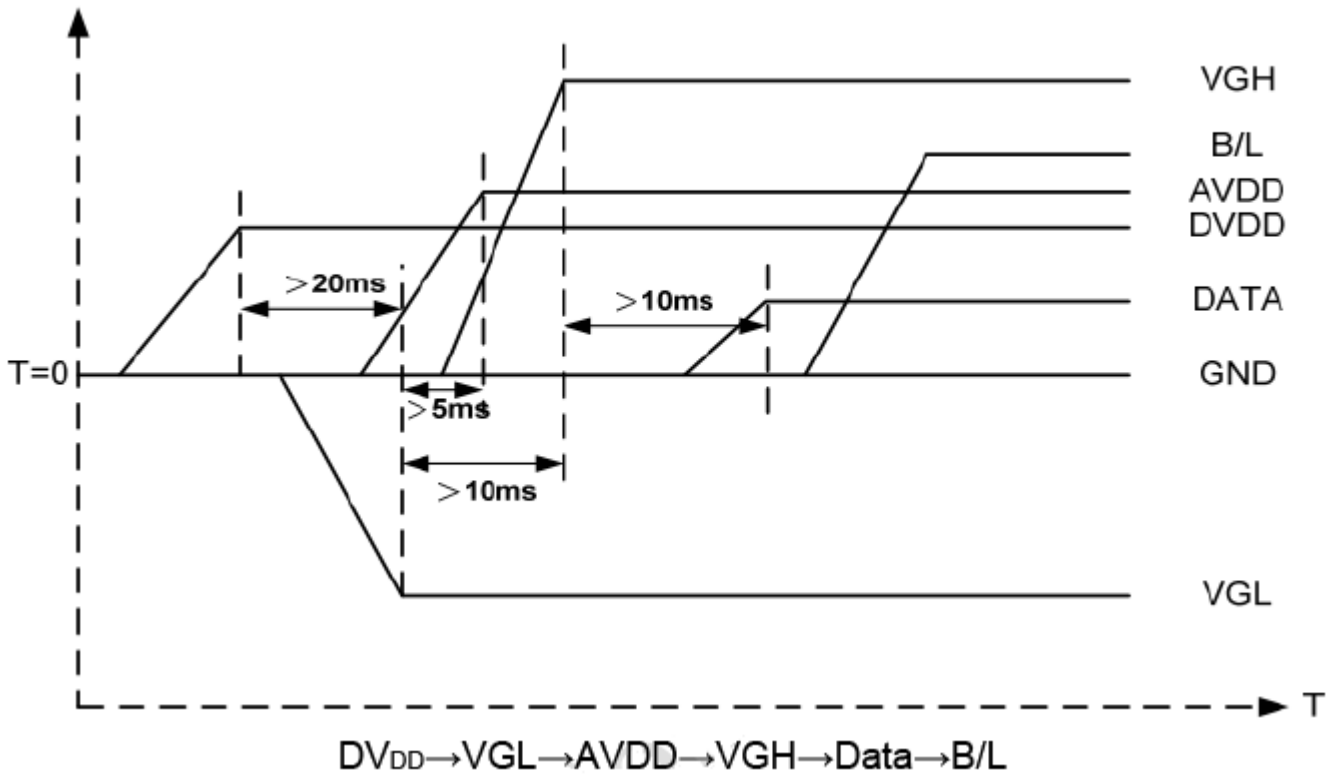


Vertical input timing diagram

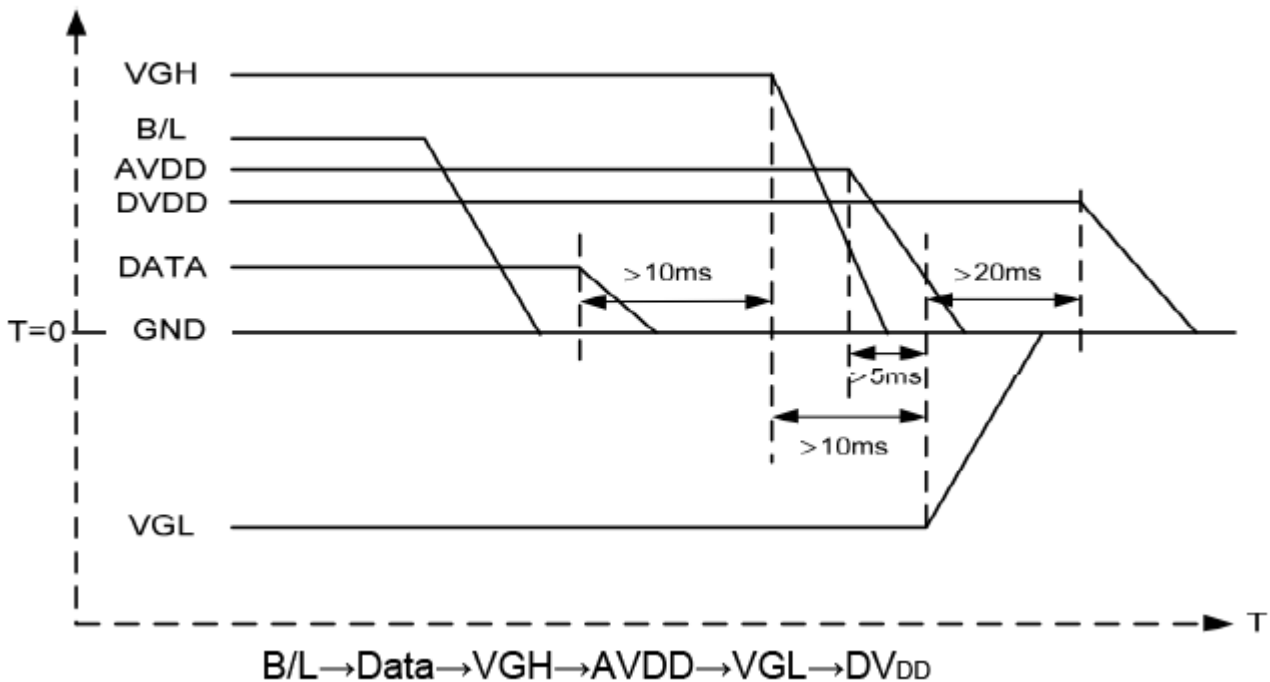


2.3.3 Power On/Off Characteristics

a. Power on:

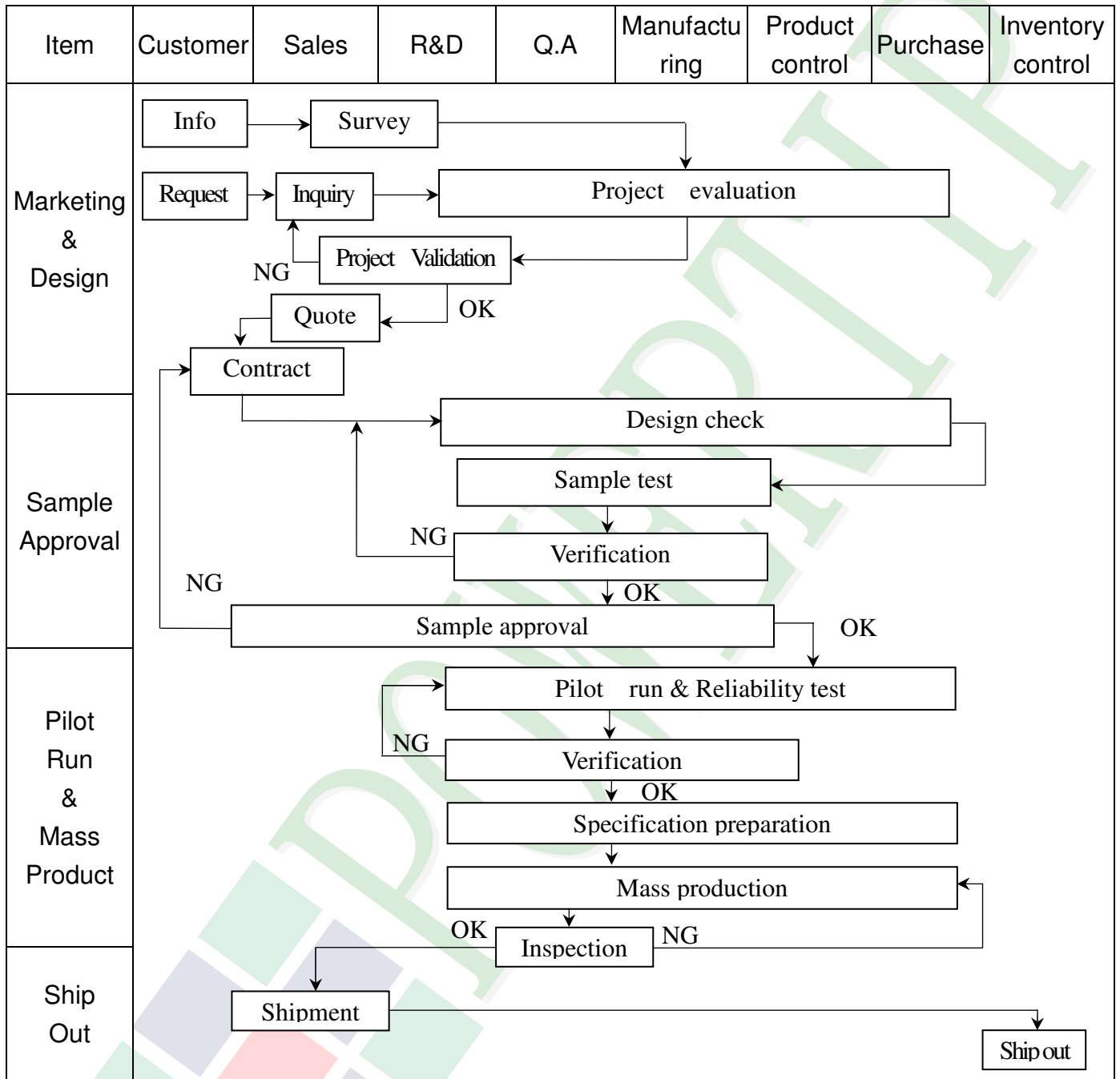


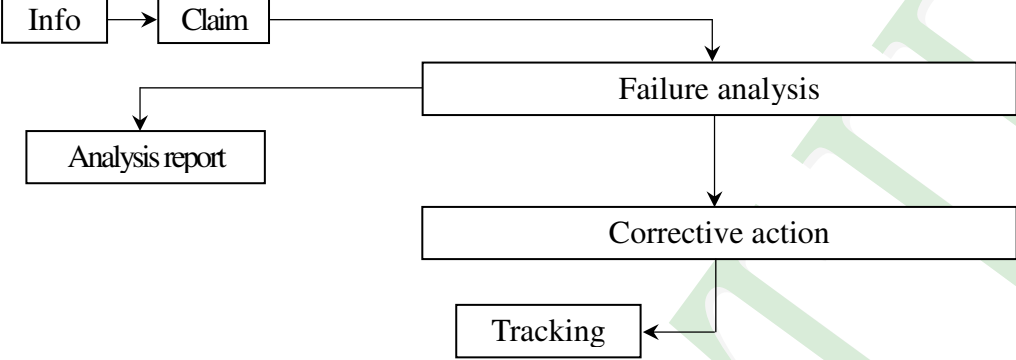
b. Power off:



3. QUALITY ASSURANCE SYSTEM

3.1 Quality Assurance Flow Chart



Item	Customer	Sales	R&D	Q.A	Manufacturing	Product control	Purchase	Inventory control
Sales Service	 <pre> graph TD Info[Info] --> Claim[Claim] Claim --> Failure[Failure analysis] Failure --> Report[Analysis report] Failure --> Action[Corrective action] Action --> Tracking[Tracking] </pre>							
Q.A Activity	1. ISO 9001 Maintenance Activities 3. Equipment calibration 5. Standardization Management				2. Process improvement proposal 4. Education And Training Activities			

3.2 Inspection Specification

◆Scope : The document shall be applied to TFT-LCD Module for 3.5" ~15" (Ver.B01).

◆Inspection Standard : MIL-STD-105E Table Normal Inspection Single Sampling Level II.

◆Equipment : Gauge 、MIL-STD 、Powertip Tester 、Sample

◆Defect Level : Major Defect AQL : 0.4 ; Minor Defect AQL : 1.5

◆OUT Going Defect Level : Sampling.

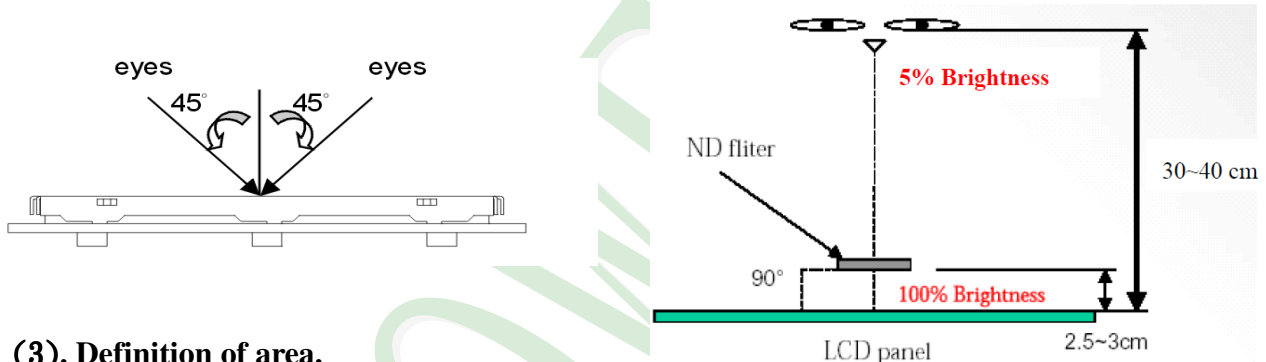
◆Standard of the product appearance test :

a. Manner of appearance test :

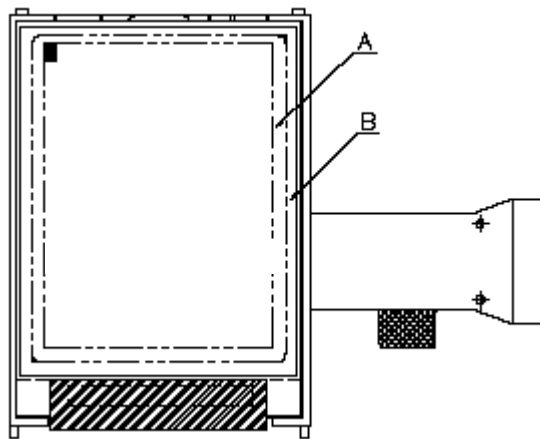
(1). The test best be under 20W×2 fluorescent light(about 300lux ~500lux)

， and distance of view must be at 30~40 cm.

(2). The test direction is base on about around 45° of vertical line.



(3). Definition of area.



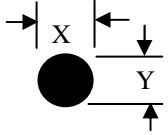
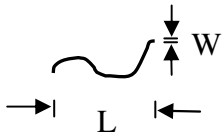
A area : viewing area

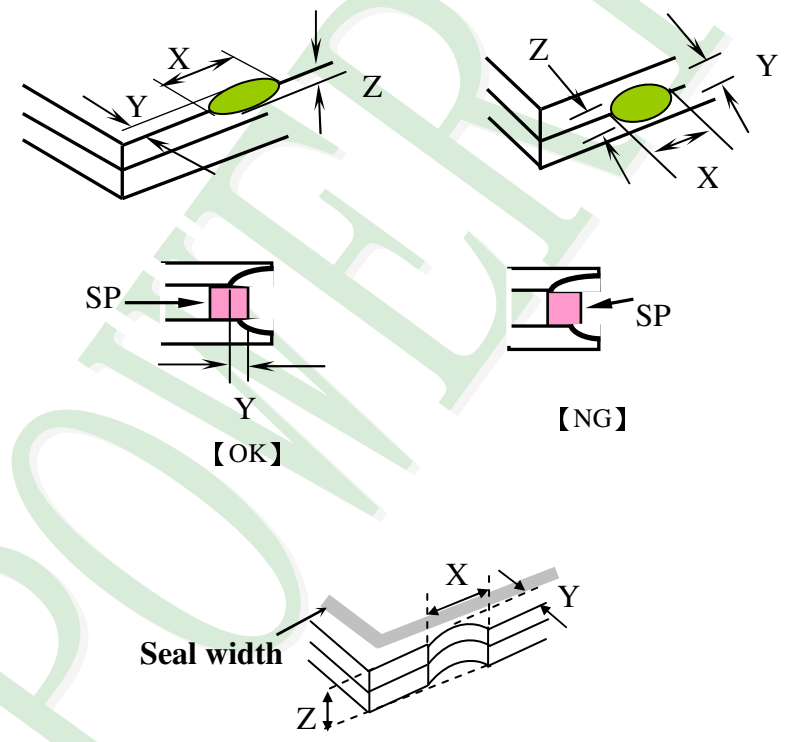
B area : Outside of viewing area

(4). Standard of inspection : (Unit : mm)

NO	Item	Criterion	Level												
01	Product condition	1.1The part number is inconsistent with work order of production.	Major												
		1.2 Mixed product types.	Major												
		1.3 Assembled in inverse direction.	Major												
02	Quantity	2.1The quantity is inconsistent with work order of production.	Major												
03	Outline dimension	3.1 Product dimension and structure must conform to structure diagram.	Major												
04	Electrical Testing	4.1 Missing line character and icon.	Major												
		4.2 No function or no display.	Major												
		4.3 Display malfunction.	Major												
		4.4 LCD viewing angle defect.	Major												
		4.5 Current consumption exceeds product specifications.	Major												
		4.6 Mura can not be seen through 5% ND filter at 50% Gray screen , should be judged by the viewing angle of 90 degree.	Minor												
05	Dot defect (Bright dot 、 Dark dot) On -display	<table border="1"> <thead> <tr> <th colspan="2">Item</th> <th>Acceptance (Q'ty)</th> </tr> </thead> <tbody> <tr> <td rowspan="4">Dot Defect</td> <td>Bright Dot</td> <td>≤ 4</td> </tr> <tr> <td>Dark Dot</td> <td>≤ 5</td> </tr> <tr> <td>Joint Dot</td> <td>≤ 3</td> </tr> <tr> <td>Total</td> <td>≤ 7</td> </tr> </tbody> </table>	Item		Acceptance (Q'ty)	Dot Defect	Bright Dot	≤ 4	Dark Dot	≤ 5	Joint Dot	≤ 3	Total	≤ 7	Minor
		Item		Acceptance (Q'ty)											
		Dot Defect	Bright Dot	≤ 4											
			Dark Dot	≤ 5											
			Joint Dot	≤ 3											
Total	≤ 7														
5.1 Inspection pattern : full white , full black , Red , Green and blue screens.															
5.2 It is defined as dot defect if defect area $> 1/2$ dot.															
5.3 The distance between two dot defect ≥ 5 mm.															
5.4 Bright dot that can not be seen through 5% ND filter.															

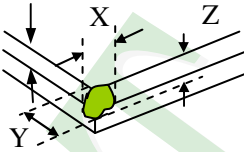
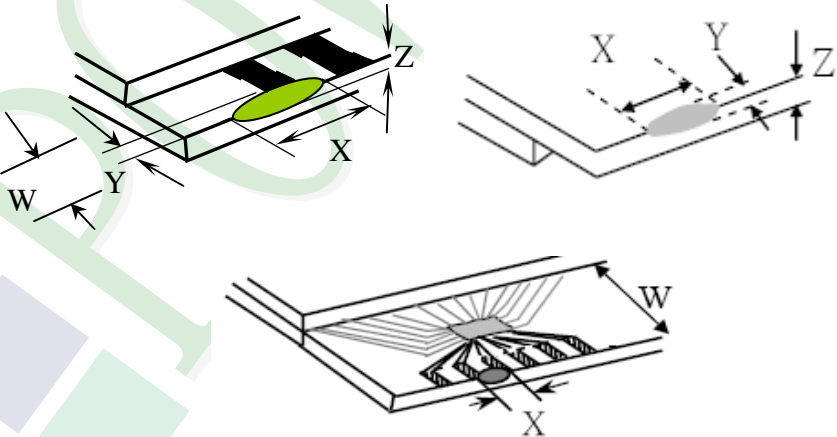
◆Specification For TFT-LCD Module 3.5" ~15" :
(Ver.B01)

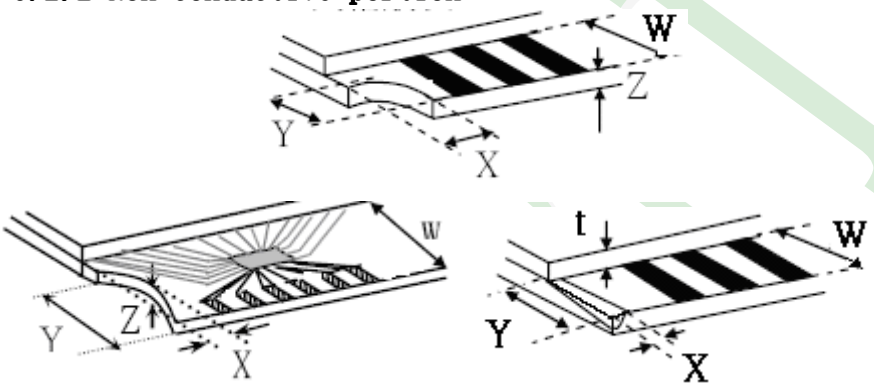
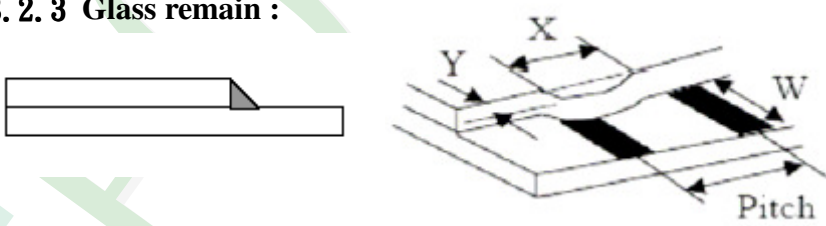
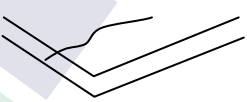
NO	Item	Criterion	Level																																																					
06	<p>Black or white dot、scratch、contamination</p> <p>Round type</p>  <p>$\Phi = (x + y) / 2$</p> <p>Line type</p> 	<p>6. 1 Round type (Non-display or display) :</p> <table border="1"> <thead> <tr> <th rowspan="2">Dimension (diameter : Φ)</th> <th colspan="2">Acceptance (Q'ty)</th> </tr> <tr> <th>A area</th> <th>B area</th> </tr> </thead> <tbody> <tr> <td>$\Phi \leq 0.25$</td> <td>Ignore</td> <td rowspan="3">Ignore</td> </tr> <tr> <td>$0.25 < \Phi \leq 0.50$</td> <td>5</td> </tr> <tr> <td>$\Phi > 0.50$</td> <td>0</td> </tr> <tr> <td>Total</td> <td>5</td> <td></td> </tr> </tbody> </table> <p>6. 2 Line type(Non-display or display) :</p> <table border="1"> <thead> <tr> <th rowspan="2">module size</th> <th rowspan="2">Length (L)</th> <th rowspan="2">Width (W)</th> <th colspan="2">Acceptance (Q'ty)</th> </tr> <tr> <th>A area</th> <th>B area</th> </tr> </thead> <tbody> <tr> <td rowspan="5">3.5" to less 9"</td> <td>---</td> <td>$W \leq 0.03$</td> <td>Ignore</td> <td rowspan="5">Ignore</td> </tr> <tr> <td>$L \leq 10.0$</td> <td>$0.03 < W \leq 0.05$</td> <td>4</td> </tr> <tr> <td>$L \leq 5.0$</td> <td>$0.05 < W \leq 0.10$</td> <td>2</td> </tr> <tr> <td>---</td> <td>$W > 0.10$</td> <td>As round type</td> </tr> <tr> <td>Total</td> <td></td> <td>5</td> </tr> <tr> <td rowspan="4">9" to 15"</td> <td>---</td> <td>$W \leq 0.05$</td> <td>Ignore</td> <td rowspan="4">Ignore</td> </tr> <tr> <td>$L \leq 10.0$</td> <td>$0.05 < W \leq 0.10$</td> <td>5</td> </tr> <tr> <td>---</td> <td>$W > 0.10$</td> <td>As round type</td> </tr> <tr> <td>Total</td> <td></td> <td>5</td> </tr> </tbody> </table>	Dimension (diameter : Φ)	Acceptance (Q'ty)		A area	B area	$\Phi \leq 0.25$	Ignore	Ignore	$0.25 < \Phi \leq 0.50$	5	$\Phi > 0.50$	0	Total	5		module size	Length (L)	Width (W)	Acceptance (Q'ty)		A area	B area	3.5" to less 9"	---	$W \leq 0.03$	Ignore	Ignore	$L \leq 10.0$	$0.03 < W \leq 0.05$	4	$L \leq 5.0$	$0.05 < W \leq 0.10$	2	---	$W > 0.10$	As round type	Total		5	9" to 15"	---	$W \leq 0.05$	Ignore	Ignore	$L \leq 10.0$	$0.05 < W \leq 0.10$	5	---	$W > 0.10$	As round type	Total		5	Minor
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08	The crack of glass	<p>Symbols :</p> <p>X : The length of crack Z : The thickness of crack t : The thickness of glass</p> <p>Y : The width of crack. W : terminal length a : LCD side length</p>	Minor						
		<p>8.1 General glass chip :</p> <p>8.1.1 Chip on panel surface and crack between panels:</p>  <table border="1" data-bbox="539 1579 1353 1870"> <thead> <tr> <th>X</th> <th>Y</th> <th>Z</th> </tr> </thead> <tbody> <tr> <td>$\leq a$</td> <td>Crack can't enter viewing area</td> <td>$\leq 1/2 t$</td> </tr> <tr> <td>$\leq a$</td> <td>Crack can't exceed the half of SP width.</td> <td>$1/2 t < Z \leq 2 t$</td> </tr> </tbody> </table>		X	Y	Z	$\leq a$	Crack can't enter viewing area	$\leq 1/2 t$
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◆ Specification For TFT-LCD Module 3.5" ~15" :

(Ver.B01)

NO	Item	Criterion	Level												
08	The crack of glass	<p>Symbols :</p> <p>X : The length of crack Z : The thickness of crack t : The thickness of glass</p> <p>Y : The width of crack. W : terminal length a : LCD side length</p> <hr/> <p>8.1.2 Corner crack :</p>  <table border="1" data-bbox="523 768 1337 1059"> <thead> <tr> <th>X</th> <th>Y</th> <th>Z</th> </tr> </thead> <tbody> <tr> <td>$\leq 1/5 a$</td> <td>Crack can't enter viewing area</td> <td>$Z \leq 1/2 t$</td> </tr> <tr> <td>$\leq 1/5 a$</td> <td>Crack can't exceed the half of SP width.</td> <td>$1/2 t < Z \leq 2 t$</td> </tr> </tbody> </table>	X	Y	Z	$\leq 1/5 a$	Crack can't enter viewing area	$Z \leq 1/2 t$	$\leq 1/5 a$	Crack can't exceed the half of SP width.	$1/2 t < Z \leq 2 t$				
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		<p>8.2 Protrusion over terminal :</p> <p>8.2.1 Chip on electrode pad :</p>  <table border="1" data-bbox="560 1697 1345 1872"> <thead> <tr> <th></th> <th>X</th> <th>Y</th> <th>Z</th> </tr> </thead> <tbody> <tr> <td>Front</td> <td>$\leq a$</td> <td>$\leq 1/2 W$</td> <td>$\leq t$</td> </tr> <tr> <td>Back</td> <td>$\leq a$</td> <td>$\leq W$</td> <td>$\leq 1/2 t$</td> </tr> </tbody> </table>		X	Y	Z	Front	$\leq a$	$\leq 1/2 W$	$\leq t$	Back	$\leq a$	$\leq W$	$\leq 1/2 t$	Minor
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		<p>8.2.2 Non-conductive portion :</p>  <table border="1" data-bbox="625 967 1257 1093"> <thead> <tr> <th>X</th> <th>Y</th> <th>Z</th> </tr> </thead> <tbody> <tr> <td>$\leq 1/3 a$</td> <td>$\leq W$</td> <td>$\leq t$</td> </tr> </tbody> </table> <p>⊙ If the chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode terminal specifications.</p> <p>8.2.3 Glass remain :</p>  <table border="1" data-bbox="545 1523 1241 1648"> <thead> <tr> <th>X</th> <th>Y</th> <th>Z</th> </tr> </thead> <tbody> <tr> <td>$\leq a$</td> <td>$\leq 1/3 W$</td> <td>$\leq t$</td> </tr> </tbody> </table> <p>8.2.4 Cracking</p>  <p style="text-align: center;">Not Allowed</p>		X	Y	Z	$\leq 1/3 a$	$\leq W$	$\leq t$	X	Y	Z
X	Y	Z										
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◆Specification For TFT-LCD Module 3.5" ~15" :
(Ver.B01)

NO	Item	Criterion	Level
09	Backlight elements	9. 1 Backlight can't work normally.	Major
		9. 2 Backlight doesn't light or color is wrong.	Major
		9. 3 Illumination source flickers when lit.	Major
10	General appearance	10. 1 Pin type 、 quantity 、 dimension must match type in structure diagram.	Major
		10. 2 No short circuits in components on PCB or FPC .	Major
		10. 3 Parts on PCB or FPC must be the same as on the production characteristic chart .There should be no wrong parts , missing parts or excess parts.	Major
		10. 4 Product packaging must the same as specified on packaging specification sheet.	Minor
		10. 5 The folding and peeled off in polarizer are not acceptable.	Minor
		10. 6 The PCB or FPC between B/L assembled distance(PCB or FPC) is ≤ 1.5 mm.	Minor

5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

- 5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module, be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So, please handle it very carefully, do not touch, push or rub the exposed polarizing with anything harder than an HB pencil lead (glass, tweezers, etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands, this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is $320 \pm 10^{\circ}\text{C}$ and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM
- 5.2.10 Caution!(LCM products with Capacitive Touch Panel)
Strong EMI-sources such as switch-mode power supplies (SMPS) can lead to touch malfunction (e.g. ghost-touches).
Therefore, the touch needs to be thoroughly tested inside the target application.
- 5.2.11 CAUTION: Continuously displaying same static image will result in high possibility of image sticking/image burn-in effect due to TFT panel characteristic.
- 5.2.12 Double-sided tape designed to be attach with the customer's mechanical device, please follow up the rules and regulations published by the original manufacturer of double-sided tape for the attachment operation.

5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is $25^{\circ}\text{C} \pm 5^{\circ}\text{C}$ and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush, shake, or jolt the module.

5.4 TERMS OF WARRANTY

- 5.4.1 Applicable warrant period The period is within thirteen months since the date of shipping out under normal using and storage conditions.
- 5.4.2 Unaccepted responsibility
This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.

Ver.001

LCM包裝規格書

Documents NO. PKG-PH800480T013-IBC24

LCM Packaging Specifications
(For Tray)

Approve	Check	Contact
Oliver	Oliver	Stone

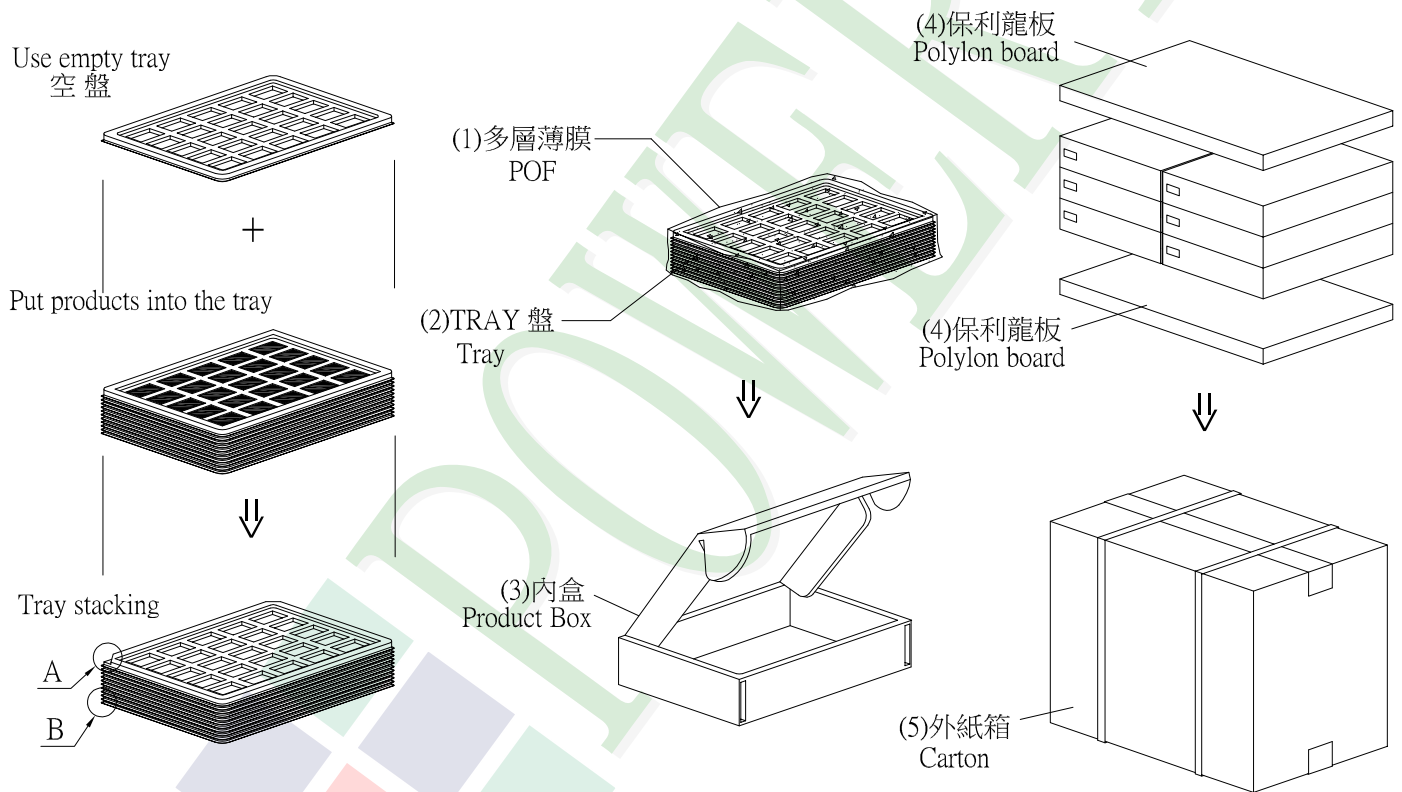
1. 包裝材料規格表 (Packaging Material) : (per carton)

No.	Item	Model	Dimensions (mm)	1Pcs Weight	Quantity	Total Weight
1	成品 (LCM)	PH800480T013-IBC24	192.96 X 110.76 X 5.3	0.18	72	12.96
2	多層薄膜(1)POF	OTFILM0BA03ABA	19"X350X0.015	—	6	—
3	TRAY 盤 (2)Tray	TY00000000375	352 X 260 X 14.0	0.1	42	4.2
4	內盒(3)Product Box	BX36627063ABBA	383 X 270 X 66	0.182	6	1.092
5	保利龍板(4)Polylon board	OTPLB00PL08ABA	550 X 393 X 20	0.0284	2	0.0568
6	外紙箱(5)Carton	BX57041027CCBA	570 X 410 X 265	1.0	1	1.0
7						
8						
9						

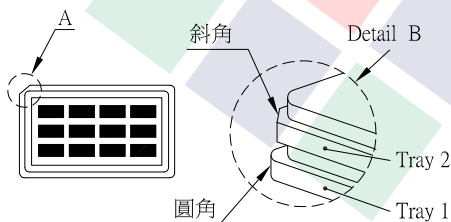
2. 一整箱總重量 (Total LCD Weight in carton) : 19.31 Kg±10%

3. 單箱數量規格表 (Packaging Specifications and Quantity) :

(1) LCM quantity per box : no per tray	2	x no of tray	6	=	12
(2) Total LCM quantity in carton : quantity per box	12	x no of boxes	6	=	72



特記事項 (REMARK)



4. TRAY盤相疊時, 需旋轉180度, 請詳見B視圖
Rotate tray 180 degrees and place on top of stack.
Check the tray stack using Fig. B.