

## **SPECIFICATIONS**

CUSTOMER :	
SAMPLE CODE	SH320240T-023-103Q
MASS PRODUCTION CODE	PH320240T-023-103Q
SAMPLE VERSION :	01
SPECIFICATIONS EDITION	008
DRAWING NO. (Ver.)	LMD-PH320240T-023-I03Q (Ver.003)
PACKAGING NO. (Ver.)	PKG-PH320240T-023-I03Q (Ver.001)

## **Customer Approved**

Date:

A	pproved	Checked	Designer			
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■ Spec	ification for sample a					
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## History of Version

<u>Date</u> (mm / dd / yyyy)	<u>Ver.</u>	<u>Edi.</u>	Description	<u>Page</u>	<u>Design by</u>
12/07/2012	01	001	New Drawing.	-	Ackey
12/17/2012	01	002	Modify Interface Pin Description (LEDA->A , LEDK->K , Y2->Y+ , X2->X+ , Y1->Y- , X1->X-)	-	Ackey
01/17/2013	01	003	New Sample.	-	Ackey
04/03/2014	01	004	Modify CR & Viewing Angle. Add CN & Initcode.	6,17 Appendix	Ackey
08/25/2015	01	005	Show Backlight Life Time	8	張斌
02/02/2016	01	006	Modify Initial Code Comment.	14	Ackey
03/22/2018	01	007	Update Optical Characteristics.	6	Ackey
02/07/2023	01	008	Modify Pin 39 Interface Describe.	14	Ackey



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## **1. SPECIFICATIONS**

### 1.1 Features

#### Main LCD Panel

<u>ltem</u>	Standard Value			
Display Type	320* (R 、G 、B) * 240 Dots			
LCD Type	Normally white , Transmissive type			
Screen size(inch)	3.5(Diagonal)			
Viewing Direction	6 O'clock			
Color configuration	R.G.B. vertical stripe			
Backlight	White LED			
Interface	Digital 24-bits Parallel RGB HSYNC,VSYNC.3Wires SPI			
Other				
(controller / driver IC)	Himax: HX8238-D			
	THIS PRODUCT CONFORMS THE ROHS OF PTC			
ROHS	Detail information please refer website :			
	http://www.powertip.com.tw/news_detail.php?Key=1&cID=1			

## **1.2 Mechanical Specifications**

<u>ltem</u>	Standard Value	<u>Unit</u>
Outline Dimension	76.9(W) * 63.9 (L) * 4.75 (H)(Max)	mm
LCD panel		
<u>ltem</u>	Standard Value	<u>Unit</u>
Active Area	70.08 (W) * 52.56 (L)	mm
Touch panel		
<u>ltem</u>	Standard Value	<u>Unit</u>
Viewing Area(T/P)	72.08 (W) x 54.56 (L)	mm
Active Area(T/P)	71.08 (W) x 53.56 (L)	mm

Note : For detailed information please refer to LCM drawing.



## **1.3 Absolute Maximum Ratings**

#### Module

<u>ltem</u>	<u>Symbol</u>	<b>Condition</b>	<u>Min.</u>	<u>Max.</u>	<u>Unit</u>
System Power Supply Voltage	VDD	GND=0	-0.3	4.0	V
Booster Reference Supply Voltage	VCI	GND=0	GND-0.3	3.96	V
Operating Temperature	T <sub>OP</sub>	Excluded T/P	-20	70	°C
Storage Temperature	Тѕт	Excluded T/P	-30	80	°C
Storage Humidity	HD	Ta < 60 °C	20	90	%RH

### **1.4 DC Electrical Characteristics**

Module		GND = 0V, Ta = 25°C						
<u>ltem</u>	<u>Symbol</u>	<u>Condition</u>	<u>Min.</u>	<u>Typ.</u>	<u>Max.</u>	<u>Unit</u>		
Power Supply Voltage	VDD	-	3.0	3.3	3.6	V		
Booster Reference Supply Voltage	VCI	-	3.0	3.3	3.6	V		
V <sub>COM</sub> High Voltage	Vсомн	-	-	-	5.54	V		
V <sub>COM</sub> Low Voltage	VCOML		-2.8	-	-	V		
	VIH		0.8VDD	-	VDD	V		
Input H/L Level Voltage	VIL	-	0	-	0.2VDD	V		
	VOH		0.9VDD	-	VDD	V		
Output H/L Level Voltage	VOL	-	-	-	0.1VDD	V		
Supply Current	IDD	VDD=VCI=3.3V Pattern= black*1	-	9	14	mA		

Note1: Maximum current display.



## **1.5 Optical Characteristics**

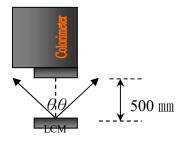
VDD=VCI=3.3V, Ta=25°C

<u>ltem</u>		<u>Symbol</u>	<b>Condition</b>	<u>Min.</u>	<u>Тур.</u>	<u>Max.</u>	<u>unit</u>	-
Response tim	ne	Tr + Tf	Ta = 25°C θX, θY = 0°	-	35	53	ms	Note2
	Тор	θY+		-	60	-		
Viewing angle	Bottom	θY-	CR ≥ 10	-	60	1	Deg.	Note4
viewing angle	Left	θХ-		-	60	ľ	Deg.	NOIE4
	Right	θΧ+		-	60	-		
Contrast rati	0	CR		500	600	-	-	Note3
	White	Х		0.26	0.31	0.36		
	vvriite	Y		0.29	0.34	0.41		
	Ded	Х	Ta = 25°C	0.59	0.64	0.69		
Color of CIE Coordinate	Red	Y	θX , θY = 0°	0.30	0.35	0.40		Note1
(With B/L & TP)	Croon	Х	0, 01 - 0	0.29	0.34	0.39	-	NOLET
	Green	Y		0.55	0.60	0.65		
	Blue	Х		0.09	0.14	0.19		
	Diue	Y		0.03	0.08	0.13		
Average Brightr Pattern=white di		IV	IF= 20 mA	320	400		cd/m <sup>2</sup>	Note1
Uniformity		∆ B		80	-	-	%	Note1

Note1:

- $1 : \triangle B=B(min) / B(max) \times 100\%$
- 2 : Measurement Condition for Optical Characteristics:
  - a : Environment: 25°C±5°C / 60±20%R.H , no wind , dark room below 10 Lux at typical lamp current and typical operating frequency.
  - b : Measurement Distance: 500 ± 50  $\, \text{mm}^{-3}$  (0= 0°)
  - c : Equipment: TOPCON BM-7 fast , (field 1°) , after 10 minutes operation.
  - d : The uncertainty of the C.I.E coordinate measurement  $\pm 0.01$  , Average Brightness  $\pm 4\%$





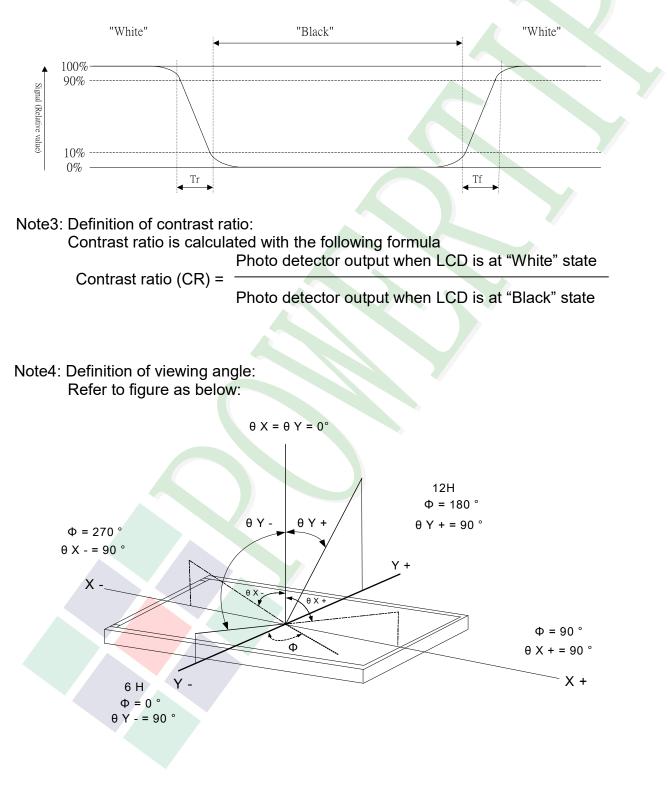
Colorimeter=BM-7 fast



Note2: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "black" to "white"(falling time) and from "white" to "black"(rising time), respectively. The response time is defined as the time interval between the 10% and 90% of Amplitudes.

Refer to figure as below:





## **1.6 Backlight Unit Characteristics**

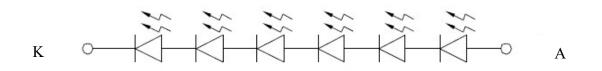
#### **Maximum Ratings**

<u>ltem</u>	<u>Symbol</u>	<b>Conditions</b>	<u>Min.</u>	Max.	<u>Unit</u>
Forward Current	IF	Ta =25℃	-	48	mA
Power Dissipation	PD	Ta =25℃	-	540	mW

#### Electrical / Optical Characteristics

<u>ltem</u>	<u>Symbol</u>	<b>Conditions</b>	<u>Min.</u>	<u>Typ.</u>	<u>Max.</u>	<u>Unit</u>
Forward Voltage	VF	IF= 20 mA	-	19.2	21	V
Average Brightness (Without LCD & TP)	IV		3800	4500	-	cd/m <sup>2</sup>
Color of CIE Coordinate	Х	IF= 20 mA	0.28	0.30	0.32	
(Without LCD & TP)	Y		0.28	0.30	0.32	-
Color			White			

## Internal Circuit Diagram



Other Description

<u>ltem</u>	<b>Conditions</b>	<b>Description</b>
Life Time	Ta =25℃	20000 hrs
	IF= 20mA	



## 1.7 Touch Panel Specification

### 1.7.1 General Standard Specification

<u>ltem</u>	Specification					
Input Method	Finger or stylus pen					
ITO Glass	T=0.7mm , 400Ω/ ±100Ω					
ITO Film	T=0.188mm , 400 Ω/ ±150Ω Anti					
Operating Temperature Range	-20°C ~70°C,20~90%RH(Except for dew gathering)					
Operating Temperature Range	-30°C~40°C,90%RH↓,41°C~80°C,60%RH↓ (Except for dew gathering)					
Surface Hardness	3H-prressure 500gf,45deg.					
Hitting Durability	1,000,000 times min. (Tip R 8 mm & R0.8mm)					
Pen Sliding Durability	100,000 times min. (Tip R0.8mm)					
Insulation Impedance	DC25V 1min,20MΩ ↑					
Light Transparency	78%min					
Linearity	Linearity Force 172g $\pm$ 1.5% ( $\pm$ 1.5% After environmental and life test)					
Lincovity Fores	80gf less input with stylus pen (R0.8mm)					
Linearity Force	Activation force guarantee area: 3.0mm inside of Active Area.					
Activation Force	120gf(Typical 20gf) less individual point on with stylus pen 9RR0.8mm.					
Activation Force	Activation force guarantee area: 5.0mm inside of Active Area.					
Bouncing	<10ms					
Impact Desistance	No damage when $\phi$ 9mm steel ball is dropped on the surface from 30 cm					
Impact Resistance	height at 1 time.					
Flexible Pattern Heat Seal	500gf/cm(peeling upward by 90deg)					
Peeling Strength						
Flexible Pattern Bending	Bending 3 times by bending radius R1.0 mm.					
Resistance	The requirements in 4-2 shall be satisfied					
Flexible Pattern Insert/Pull	5 times at least. The requirements in 4-2 shall be satisfied.					
Out Resistance	5 times at least. The requirements in 4-2 shall be satisfied.					
	Not in operation: The requirements in 3 to 4 shall be satisfied after sweep					
Vibration Resistance	vibration of 2G 15~55Hz(1 min.) is given for 30 min. each in the directions of					
	X, Y, Z.					
Package Drop	No damage to the product.(1corner edge, 2 ridges, 4 surfaces, drop					
	from 50 cm height)					
	After 4.5Kg load for 1 min AL plate 1.0.5×5cm					
	is applied to the center area					
Static load resistance	(25 cm <sup>2</sup> ) of the Touch panel,					
	the requirements in 3 and 4,					
	shall be satisfied.					



#### 1.7.2 Optical Characteristic

1.7.2.1 test by light measure device and the result should be 80%min.

#### **1.7.3 Electrical Characteristics**

- 1.7.3.1 Insulation Resistance. 10 M $\Omega$  or more (DC 25V 1min)
- 1.7.3.2 Resistance Between Terminals.Direction X (Film side): 250Ω~ 850ΩDirection Y (Glass side): 100Ω~ 600Ω
- 1.7.3.3 Linearity.
  - $\pm$  1.5% Measuring method, Linearity(%) =  $\frac{\triangle V}{FV-SV}$  X 100
  - ± 1.5% (after environmental and life test)
  - $\triangle$ V: The difference between the ideal voltage and measured voltage on the each measuring line.
  - SV: Voltage of starting Points
  - EV: Voltage of Ending Points
- 1.7.3.4 Operating Voltage. 5V DC. Max Voltage : 7V DC.
- 1.7.3.5 Bouncing

<10ms

#### 1.7.4 Attention of Mounting Condition

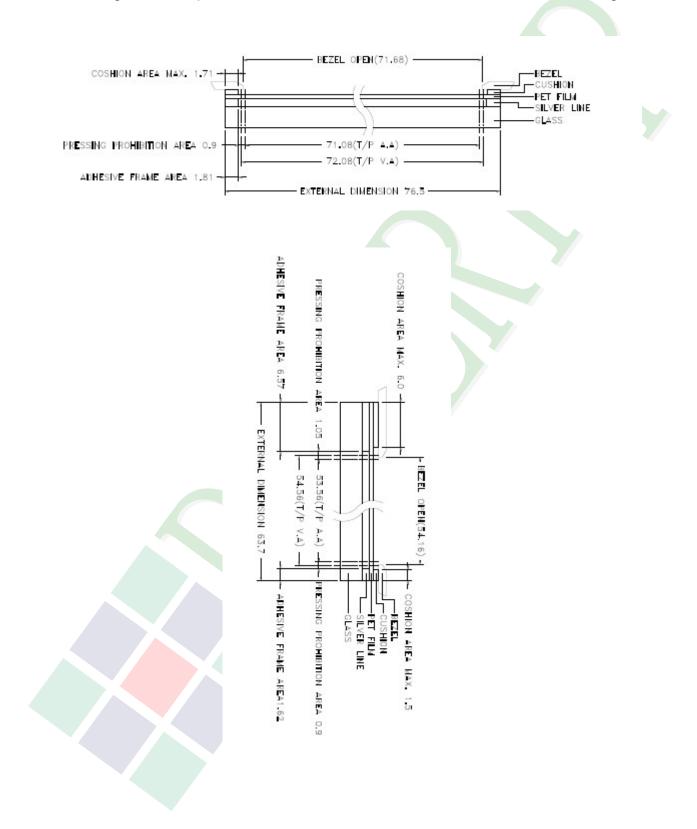
1.7.4.1The gasket support of touch panel must be designed on the outside of Viewable area, as well as to avoid pressing on touch panel accidentally, the enclosure must be designed with enough clearance to panel surface.

To avoid pressing error on touch panel accidentally, please remain space between the surface of panel and the Bezel.

- 1.7.4.2Bezel opening must be between Viewable area and Active area. Bezel opening must not touch Viewable area.
- 1.7.4.3 We recommend elastic material made support.

1.7.4.4 Do not use adhesive to bond top surface (ITO Film) of touch panel with enclosure.

1.7.4.5 The edge of touch panel is conductive. Don't touch it with metal after mounting.



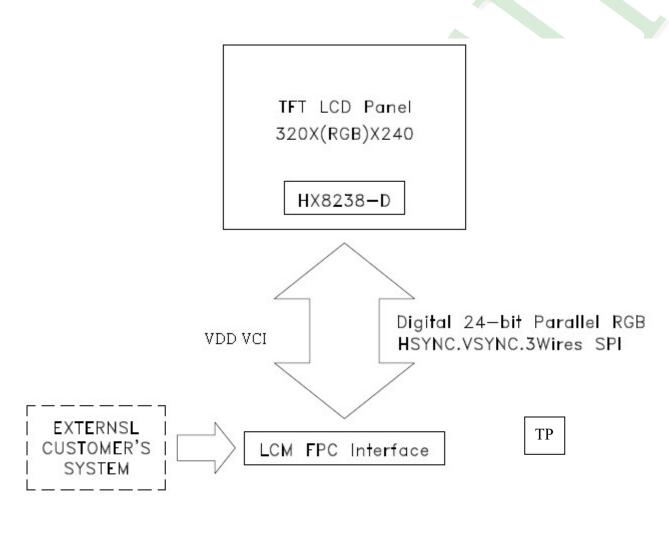


# 2. MODULE STRUCTURE

## 2.1 Counter Drawing

### 2.1.1 LCM Mechanical Diagram

- \* See Appendix
- 2.1.2 Block Diagram





### 2.2 Interface Pin Description

<u>Pin No.</u>	<u>Symbol</u>	<b>Function</b>
1	А	LED Anode.
2	К	LED Cathode.
3	GND	Ground.
4	VCI	Booster Reference Supply Voltage.
5	ID	Note1.
6	VDD	Power Supply Voltage.
7	GND	Ground.
8	RESB	Reset.
9	CSB	Chip select Input: CSB = L - selected and accessible. CSB = H - is not selected and not accessible.
10	SCK	SPI Clock Input.
11	SDO	SPI Data Output. The data is valid on the falling edge of the SCK signal.
12	SDI	SPI Data Input. The data is latched on the rising edge of the SCK signal.
13	GND	Ground.
14	В0	
15	B1	
16	B2	
17	В3	Graphic display Blue data.
18	B4	Graphic display blue data.
19	В5	
20	B6	
21	B7	



<u>Pin No.</u>	<u>Symbol</u>	<b>Function</b>							
22	G0								
23	G1								
24	G2								
25	G3	Graphic display Green data.							
26	G4								
27	G5								
28	G6								
29	G7								
30	R0								
31	R1								
32	R2								
33	R3								
34	R4	Graphic display Red data.							
35	R5								
36	R6								
37	R7								
38	GND	Ground.							
39	DCLK	Video Clock Input. Match Programing Initcode the data is latched on the falling edge of DCLK.							
40	HSYNC	Horizontal Sync Input.							
41	VSYNC	Vertical Sync Input.							

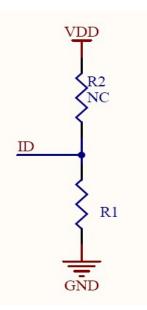


<u>Pin No.</u>	<u>Symbol</u>	<u>Function</u>
42	DEN	Video Data Enable Input. VSYNC+HSYNC mode - This pin is shorted to GND normally and the back/front porch is determined by the control register. VSYNC+HSYNC+DE mode - The valid data is determined by the VSYNC+HSYNC+DEN pin. DE mode - VSYNC and HSYNC are unused and shorted to GND. The valid input. data is determined by DEN pin.
43	GND	Ground.
44	SEL0	
45	SEL1	Note2.
46	SEL2	
47	Y+	Touch Panel Y_Top.
48	Х+	Touch Panel X_Right.
49	Y-	Touch Panel Y_Bottom.
50	Х-	Touch Panel X_Left.



### Note1: ID code Circuit

Vendor ID (On FPC, ID resistor as specified in vendor table shall be connected to this pin, and other side of the resistor shall be connected to GND)



R1=44.2KΩ

## Note2: Define the input interface mode

SEL2	SEL1	<u>SEL0</u>	<u>Format</u>	Operating frequency
0	0	0	Parallel-RGB data format	6.5MHz
0	0	1	Serial-RGB data format	19.5MHz
0	1	0	CCIR 656 data format(640RGB)	24.54MHz
0	1	1	CCIR 656 data format(720RGB)	27MHz
1	0	0	YUV mode A data format(Cr-Y-Cb-Y)	24.54MHz
1	0	1	YUV mode A data format(Cr-Y-Cb-Y)	27MHz
1	1	0	YUV mode B data format(Cr-Y-Cr-Y)	27MHz
1	1	1	YUV mode B data format(Cr-Y-Cr-Y)	24.54MHz

Input format	DOTCLK Freq(MHz)	<u>Display data</u>	Active area(DOTCLK)		
YUV mode	24.54	640	1280		
	27	720	1440		



### 2.2.1 Refer Initial code:

(void ini)	tial_Main(void)	// For HX8238-D
MOV	DPH,#00H	;Register 0001
MOV	DPL,#01H	
CALL	COM_SER	
MOV	DPH,#63H	
MOV	DPL,#00H	
CALL	DATA_SER	
MOV	DPH,#00H	;Register 0002
MOV	DPL,#02H	
CALL	COM_SER	
MOV	DPH,#02H	
MOV	DPL,#00H	
CALL	DATA_SER	
MOV	DPH,#00H	;Register 0003
MOV	DPL,#03H	
CALL	COM_SER	
MOV	DPH,#011 <mark>00100B</mark>	;DB3 ~ DB0
MOV	DPL,#01100100B	
CALL	DATA_SER	



MOV         DPH,#00H         ;Register 0004           MOV         DPL,#04H		VII LILLI	
CALLCOM_SERMOVDPH,#04HMOVDPL,#C7H,Parallel 24 bitsCALLDATA_SERMOVDPH,#00H,Register 0005MOVDPL,#05HCALLCOM_SERMOVDPH,#FCHCALLDATA_SERMOVDPL,#30HCALLDATA_SERMOVDPL,#04HCALLCOM_SERMOVDPL,#04HCALLCOM_SERMOVDPL,#04HCALLCOM_SERMOVDPL,#04HCALLDATA_SERMOVDPL,#04HCALLDATA_SERMOVDPL,#04HCALLCOM_SERMOVDPL,#04HCALLCOM_SERMOVDPL,#04HCALLCOM_SERMOVDPL,#02HCALLCOM_SERMOVDPL,#02HCALLCOM_SERMOVDPL,#02HCALLCOM_SERMOVDPL,#02HCALLCOM_SERMOVDPL,#02HCALLCOM_SERMOVDPL,#02HCALLCOM_SERMOVDPL,#02DHCALLCOM_SERMOVDPL,#02DHCALLCOM_SERMOVDPL,#02DHCALLCOM_SERMOVDPL,#02DHCALLCOM_SERMOVDPL,#02DHCALLCOM_SERMOVDPL,#02DHCALLCOM_SERMOVDP	MOV	DPH,#00H	;Register 0004
MOVDPH,#04HMOVDPL,#C7H;Parallel 24 bitsCALLDATA_SERMOVDPH,#00H;Register 0005MOVDPL,#05HCALLCOM_SERMOVDPL,#60HCALLDATA_SERMOVDPL,#60HCALLOATA_SERMOVDPL,#00HRegister 000AMOVDPL,#00HRegister 000AMOVDPL,#00HKegister 000AMOVDPL,#00HCALLCOM_SERMOVDPL,#00HKegister 000DMOVDPL,#00HKegister 000DKegister 000DKegister 000DKegister 000DKegister 00DKegister 00D </td <td>MOV</td> <td>DPL,#04H</td> <td></td>	MOV	DPL,#04H	
MOVDPL,#C7H;Parallel 24 bitsCALLDATA_SERMOVDPH,#00HRegister 0005MOVDPL,#05HCALLCOM_SERMOVDPL,#80HCALLDATA_SERMOVDPH,#00HRegister 000AMOVDPL,#03HMOVDPH,#00HCALLCOM_SERMOVDPL,#04HCALLCOM_SERMOVDPL,#08HCALLDATA_SERMOVDPL,#04HMOVDPL,#04HMOVDPL,#04HMOVDPL,#04HMOVDPL,#04HGALLCOM_SERMOVDPL,#00HRegister 000DMOVDPL,#00HGALLCOM_SERMOVDPL,#00HGALLCOM_SERMOVDPL,#00HMOVDPL,#00HMOVDPL,#00HMOVDPL,#00HMOVDPL,#00HMOVDPL,#00HMOVDPL,#00HMOVDPL,#00HMOVDPL,#00HMOVDPL,#00HMOVDPL,#00HMOVDPL,#00HMOVDPL,#00HMOVDPL,#00HMOVDPL,#00HMOVDPL,#00HMOVDPL,#00HMOVDPL,#00DHMOVDPL,#00DHMOVDPL,#00DHMOVDPL,#00DHMOVDPL,#00DHMOVDPL,#00DHMOVDPL,#00DHM	CALL	COM_SER	
CALL       DATA_SER         MOV       DPH,#00H       ;Register 0005         MOV       DPL,#05H	MOV	DPH,#04H	
MOVDPH,#00H;Register 0005MOVDPL,#05HCALLCOM_SERMOVDPH,#FCHMOVDPL,#80HCALLDATA_SERMOVDPH,#00H,Register 000AMOVDPL,#0AHCALLCOM_SERMOVDPH,#00H,Register 000AMOVDPH,#00H,Register 000AMOVDPH,#00H,Register 000DMOVDPH,#00H,Register 000DMOVDPL,#00H,Register 000DMOVDPL,#00H,Register 000DMOVDPL,#00H,Register 000DMOVDPL,#00H,Register 000DMOVDPL,#00H,Register 000DMOVDPL,#00H,Register 000DMOVDPL,#00H,Register 000D	MOV	DPL,#C7H	;Parallel 24 bits
MOV DPL,#05H CALL COM_SER MOV DPH,#FCH MOV DPL,#80H CALL DATA_SER MOV DPH,#00H ;Register 000A MOV DPL,#0AH CALL COM_SER MOV DPL,#0AH CALL COM_SER MOV DPL,#08H CALL DATA_SER MOV DPL,#00H ;Register 000D MOV DPL,#00H ;Register 000D MOV DPL,#00H ;Register 000D	CALL	DATA_SER	
MOV DPL,#05H CALL COM_SER MOV DPH,#FCH MOV DPL,#80H CALL DATA_SER MOV DPH,#00H ;Register 000A MOV DPL,#0AH CALL COM_SER MOV DPL,#0AH CALL COM_SER MOV DPL,#08H CALL DATA_SER MOV DPL,#00H ;Register 000D MOV DPL,#00H ;Register 000D MOV DPL,#00H ;Register 000D			
CALL       COM_SER         MOV       DPH,#FCH         MOV       DPL,#80H         CALL       DATA_SER         MOV       DPH,#00H         KOV       DPL,#0AH         CALL       COM_SER         MOV       DPL,#0AH         CALL       COM_SER         MOV       DPL,#0AH         CALL       DPH,#40H         MOV       DPL,#08H         CALL       DATA_SER         MOV       DPL,#00H         Kegister 000D         MOV       DPL,#00H	MOV	DPH,#00H	;Register 0005
MOV DPH,#FCH MOV DPL,#80H CALL DATA_SER MOV DPH,#00H ;Register 000A MOV DPL,#0AH CALL COM_SER MOV DPH,#40H MOV DPL,#08H CALL DATA_SER MOV DPL,#08H CALL COM_SER MOV DPL,#00H ;Register 000D	MOV	DPL,#05H	
MOV       DPL,#80H         CALL       DATA_SER         MOV       DPH,#00H       ;Register 000A         MOV       DPL,#0AH         CALL       COM_SER         MOV       DPH,#40H         MOV       DPL,#08H         CALL       DATA_SER         MOV       DPL,#00H         MOV       DPL,#08H         CALL       DATA_SER         MOV       DPL,#00H         MOV       DPH,#00H         FRegister 000D         MOV       DPH,#00H         FRegister 00D         MOV       DPH,#00H         GALL       COM_SER         HOV       DPH,#00H         MOV       DPH,#00H	CALL	COM_SER	
CALL       DATA_SER         MOV       DPH,#00H       ;Register 000A         MOV       DPL,#0AH         CALL       COM_SER         MOV       DPH,#40H         MOV       DPL,#08H         CALL       DATA_SER         MOV       DPH,#00H         ;Register 000D         MOV       DPH,#00H         ;Register 000D         MOV       DPH,#00H         ;Register 000D         MOV       DPH,#00H         ;Register 00D         MOV       DPH,#00H         ;Register 00D         MOV       DPH,#00H         ;Register 00D         MOV       DPH,#00H         ;Register 00D         MOV       DPH,#00H	MOV	DPH,#FCH	
MOV DPH,#00H ;Register 000A MOV DPL,#0AH CALL COM_SER MOV DPH,#40H MOV DPL,#08H CALL DATA_SER MOV DPH,#00H ;Register 000D MOV DPL,#0DH CALL COM_SER	MOV	DPL,#80H	
MOV       DPL,#0AH         CALL       COM_SER         MOV       DPH,#40H         MOV       DPL,#08H         CALL       DATA_SER         MOV       DPH,#00H         ;Register 000D         MOV       DPL,#00H	CALL	DATA_SER	
MOV       DPL,#0AH         CALL       COM_SER         MOV       DPH,#40H         MOV       DPL,#08H         CALL       DATA_SER         MOV       DPH,#00H         ;Register 000D         MOV       DPL,#00H			
CALL COM_SER   MOV DPH,#40H   MOV DPL,#08H   CALL DATA_SER   MOV DPH,#00H   ;Register 000D   MOV DPL,#0DH   CALL COM_SER   MOV DPL,#00H	MOV	DPH,#00H	;Register 000A
MOVDPH,#40HMOVDPL,#08HCALLDATA_SERMOVDPH,#00HRegister 000DMOVDPL,#0DHCALLCOM_SERMOVDPH,#0000010B	MOV	DPL,#0AH	
MOVDPL,#08HCALLDATA_SERMOVDPH,#00H;Register 000DMOVDPL,#0DHCALLCOM_SERMOVDPH,#0000010B	CALL	COM_SER	
CALLDATA_SERMOVDPH,#00H;Register 000DMOVDPL,#0DHCALLCOM_SERMOVDPH,#0000010B	MOV	DPH,#40H	
MOVDPH,#00H;Register 000DMOVDPL,#0DHCALLCOM_SERMOVDPH,#0000010B	MOV	DPL,#08H	
MOV DPL,#0DH CALL COM_SER MOV DPH,#0000010B	CALL	DATA_SER	
MOV DPL,#0DH CALL COM_SER MOV DPH,#0000010B			
CALL COM_SER MOV DPH,#0000010B	MOV	DPH,#00H	;Register 000D
MOV DPH,#00000010B	MOV	DPL,#0DH	
	CALL	COM_SER	
MOV DPL,#00110001B ;DB5 ~ DB0 VLCD63	MOV	DPH,#00000010B	
	MOV	DPL,#00110001B	;DB5 ~ DB0 VLCD63



CALL DATA\_SER

MOV	DPH,#00H	;Register 000E
-----	----------	----------------

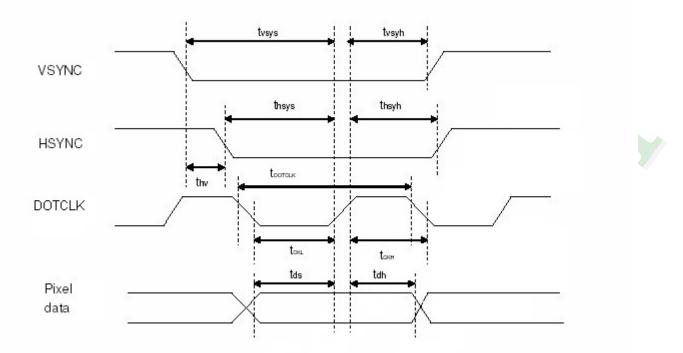
- MOV DPL,#0EH
- CALL COM\_SER
- MOV DPH,#00101110B ;DB4 ~ DB0 VCOM
- MOV DPL,#1000000B ;DB7 ~ DB6
- CALL DATA\_SER

CALL DELAY2

}



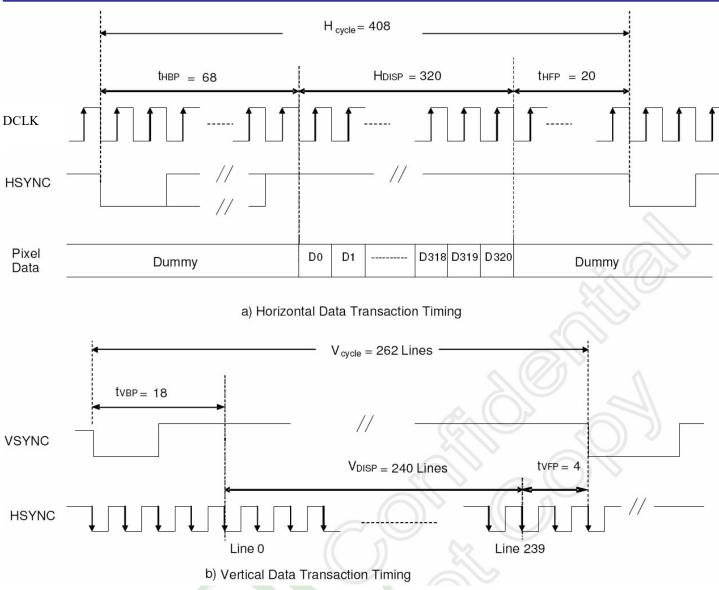
## 2.3 Timing Characteristics



Characteristics	Symbol	Mi	in.	<u>Тур.</u>		Max.		Unit
	<u>Symbol</u>	<u>24 bit</u>	<u>8 bit</u>	<u>24 bit</u>	<u>8 bit</u>	<u>24 bit</u>	<u>8 bit</u>	<u>Unit</u>
DOTCLK Frequency	tDOTCLK	-		6.5	19.5	10	30	MHz
DOTCLK Period	tDOTCLK	100	33.3	154	51.3	-		ns
Vertical Sync Setup Time	tvsys	20	10	-		-		ns
Vertical Sync Hold Time	tvsyh	20	10	-		-		ns
Horizontal Sync Setup	theye	20	10					20
Time	thsys	20	10	-		-		ns
Horizontal Sync Hold	theyb	20	10					nc
Time	thsyh	20	10	-		-		ns
Phase difference of Sync	thv	1				2	40	tDOTCLK
Signal Falling Edge	uiv		I	-		24	+0	IDUTUEN
DOTCLK Low Period	tCKL	50	15	-		-		ns
DOTCLK High Period	tCKH	50	15	-		-		ns
Data Setup Time	tds	12	10	-		-		ns
Data hold Time	tdh	12	10	-		-		ns
Reset pulse width	tRES	1	0	-			-	ns

Note : The interface of this module can drive by digital 24-bit data.



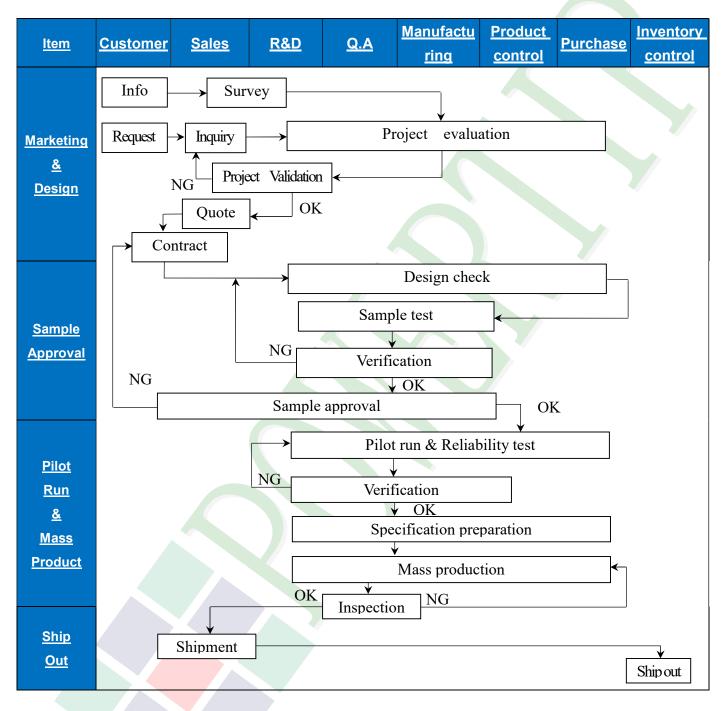


Data transaction timing in parallel RGB (24 bit) interface (SYNC mode)



## 3. Quality Assurance System

## 3.1 Quality Assurance Flow Chart



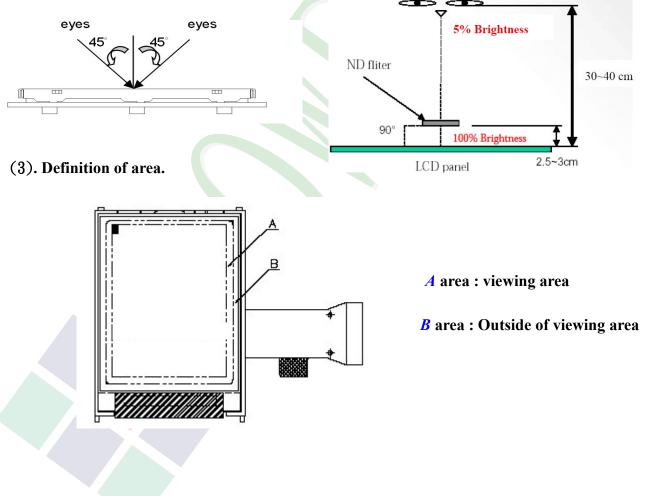


<u>ltem</u>	<u>Customer</u>	<u>Sales</u>	<u>R&amp;D</u>	<u>Q.A</u>	<u>Manufactur</u> <u>ing</u>	Product control	Purchase	Inventory control
<u>Sales</u> <u>Service</u>	Info →	Claim -		Trac	Failure a Correctiv king			
<u>Q.A</u> <u>Activity</u>	<ol> <li>ISO 9001</li> <li>Process i</li> <li>Equipmer</li> <li>Educatior</li> <li>Standardi</li> </ol>	mproven nt calibra n And Tra	nent propo Ition aining Activ	sal ⁄ities				

POWERTIP

### 3.2 Inspection Specification

- Scope : The document shall be applied to TFT-LCD Module for 3. 5" ~15" (Ver.B01).
- ◆Inspection Standard : MIL-STD-105E Table Normal Inspection Single Sampling Level Ⅱ.
- Equipment : Gauge \ MIL-STD \ Powertip Tester \ Sample
- ◆Defect Level : Major Defect AQL : 0.4 ; Minor Defect AQL : 1.5
- **OUT Going Defect Level** : Sampling.
- ◆Standard of the product appearance test :
  - a. Manner of appearance test :
  - (1). The test best be under 20W×2 fluorescent light(about 300lux ~500lux)
    - and distance of view must be at 30~40 cm.
  - (2). The test direction is base on about around  $45^{\circ}$  of vertical line.



(4). Standard of inspection : (Unit : mm)

**On** -display

◆Sp	ecification For TFT-L	CD Module 3. 5	5″~15″:		(Ver.				
<u>NO</u>	<u>Item</u>		Criter	<u>ion</u>					
		1.1The part production.	number is inconsis	tent with work order of					
01	Product condition	1.2 Mixed pro	oduct types.						
		1.3 Assemble	1 in inverse direc	tion.					
02	Quantity	2.1The quanti	ty is inconsistent	with work order of productio	n.				
03	Outline dimension	3.1 Product d diagram.	3.1 Product dimension and structure must conform to structure diagram.						
		4.1 Missing line character and icon.							
		4. 2 No function or no display.							
		4. 3 Display malfunction.							
04	Electrical Testing	4. 4 LCD viewing angle defect.							
		4. 5 Current co	4. 5 Current consumption exceeds product specifications.						
		4. 6 Mura can screen , sł	not be seen through hould be judged by th	5% ND filter at 50% Gray e viewing angle of 90 degree.					
			<u>Item</u>	<u>Acceptance (Q'ty)</u>					
			Bright Dot	≦ 4					
	Dot defect	Dot		≦ 5					
	(Bright dot <b>\</b>	Defe	ct Joint Dot	≦ 3					
05	Dark dot)		Total	≦ 7	۲ ۲				

screens. 5. 2 It is defined as dot defect if defect area >1/2 dot.

5.4 Bright dot that can not be seen through 5% ND filter.

5.3 The distance between two dot defect  $\geq 5$  mm.

(Ver.B01)

Level

Major

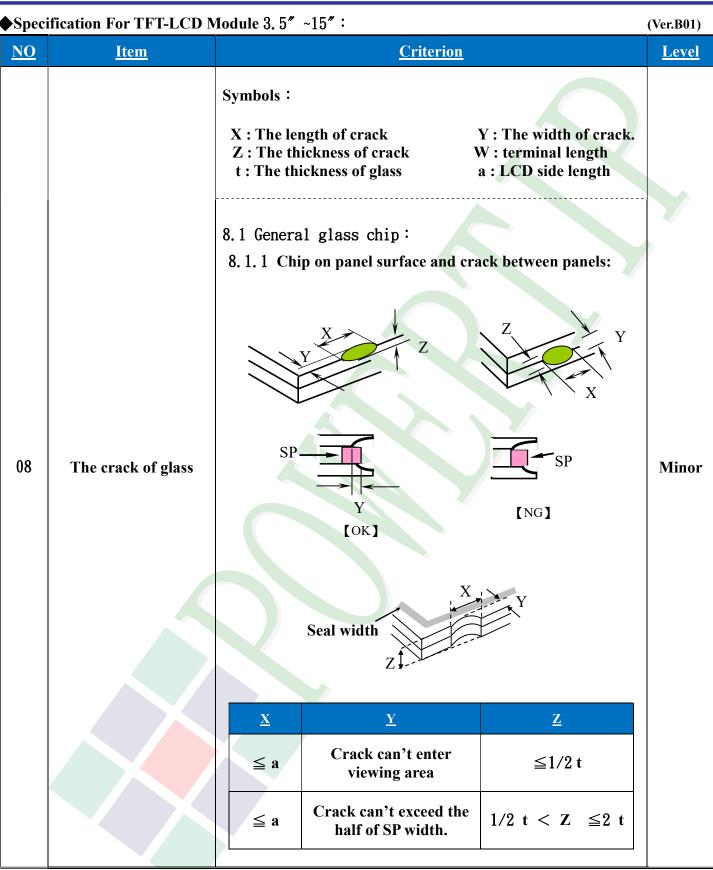
Minor

Minor

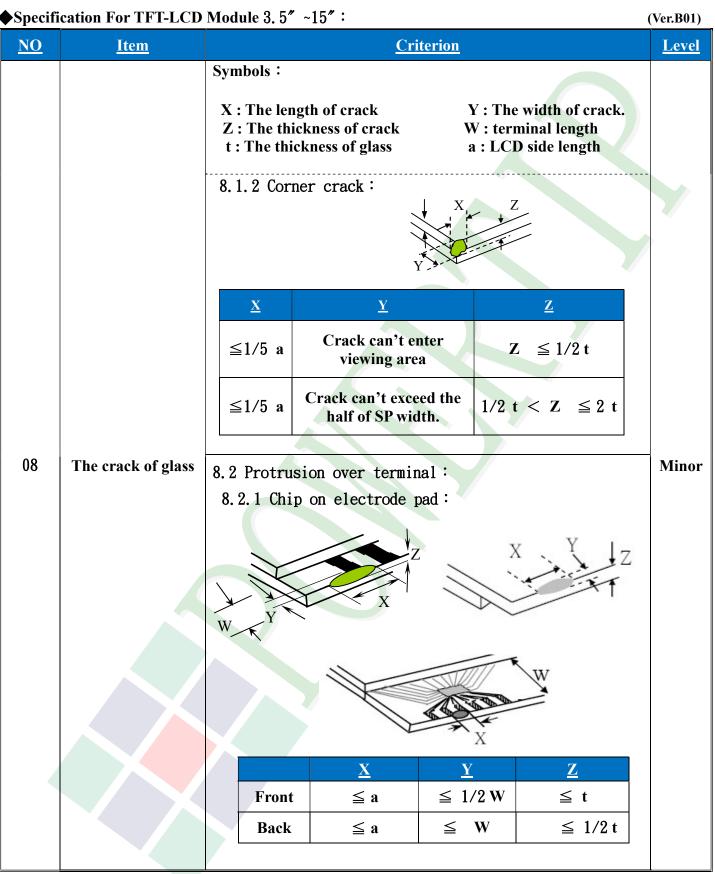


◆Specification For TFT-LCD Module 3. 5″~15″: (Ver.B0						
<u>NO</u>	<u>Item</u>	<u>Criterion</u>	Level			
06	Black or white dot $\cdot$ scratch $\cdot$ contamination Round type $\rightarrow  _X  _{\underline{+}}$ $\Phi = (x+y)/2$ Line type $\downarrow_L  _{\underline{+}}$ W	6. 1 Round type (Non-display or display) : $ \frac{\overrightarrow{\text{Dimension (diameter : } 0)}{A \text{ area} & B \text{ area}} \\  \hline 0.25 < \Phi \le 0.25 & \text{Ignore} \\ \hline 0.25 < \Phi \le 0.50 & 5 & \text{Ignore} \\ \hline 0.25 < \Phi \le 0.50 & 0 & \text{Ignore} \\ \hline 0.25 < \Phi \le 0.50 & 0 & \text{Ignore} \\ \hline 0.25 < \Phi \le 0.50 & 0 & \text{Ignore} \\ \hline 0.25 < \Phi \le 0.50 & 0 & \text{Ignore} \\ \hline 0.25 < \Phi \le 0.50 & 0 & \text{Ignore} \\ \hline 0.25 < \Phi \le 0.50 & 0 & \text{Ignore} \\ \hline 0.25 < \Phi \le 0.50 & 0 & \text{Ignore} \\ \hline 0.25 < \Phi \le 0.50 & 0 & \text{Ignore} \\ \hline 1. \le 10.0 & 0.03 < W \le 0.05 & 4 & \text{Ignore} \\ \hline 1. \le 5.0 & 0.05 < W \le 0.10 & 2 & \text{Ignore} \\ \hline 1. \le 5.0 & 0.05 < W \le 0.10 & 2 & \text{Ignore} \\ \hline 0.25 < \Phi \le 0.05 & \text{Ignore} & \text{Ignore} \\ \hline 0.25 < \Phi \le 0.05 & \text{Ignore} & \text{Ignore} \\ \hline 0.25 < \Phi \le 0.10 & 5 & \text{Ignore} \\ \hline 0.25 < \Phi \le 0.10 & 5 & \text{Ignore} \\ \hline 0.25 < \Phi \le 0.10 & 5 & \text{Ignore} \\ \hline 0.25 < \Phi \le 0.10 & 5 & \text{Ignore} \\ \hline 0.25 < \Phi \le 0.10 & 5 & \text{Ignore} \\ \hline 0.25 < \Phi \le 0.10 & 5 & \text{Ignore} \\ \hline 0.25 < \Phi \le 0.10 & 5 & \text{Ignore} \\ \hline 0.25 < \Phi \le 0.10 & 5 & \text{Ignore} \\ \hline 0.25 < \Phi \le 0.10 & 5 & \text{Ignore} \\ \hline 0.25 < \Phi \le 0.10 & 5 & \text{Ignore} \\ \hline 0.25 < \Phi \le 0.10 & 5 & \text{Ignore} \\ \hline 0.25 < \Phi \le 0.10 & 5 & \text{Ignore} \\ \hline 0.25 < \Phi \le 0.10 & 5 & \text{Ignore} \\ \hline 0.25 < \Phi \le 0.10 & 5 & \text{Ignore} \\ \hline 0.25 < \Phi \le 0.10 & 5 & \text{Ignore} \\ \hline 0.25 = \Phi = 0.25 & \text{Ignore} \\ \hline 0.25 = \Phi $	Minor			
		Total 5				
07	Polarizer Bubble	Dimension (diameter : $\Phi$ )Acceptance (Q'tv) $\Phi \leq 0.25$ Ignore $0.25 < \Phi \leq 0.50$ 4 $0.50 < \Phi \leq 0.80$ 1 $\Phi > 0.80$ 0Total5	Minor			

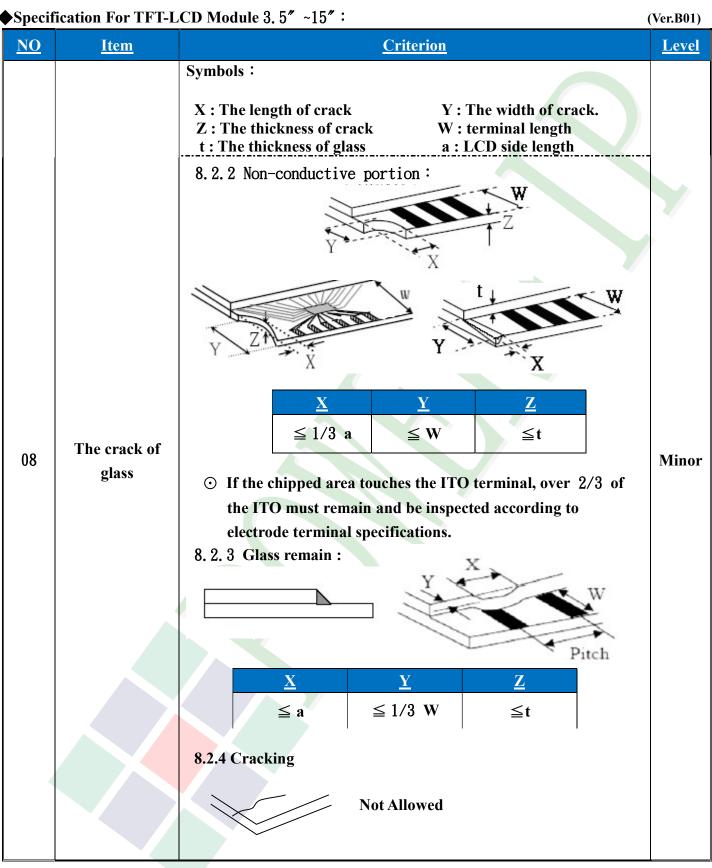














## Specification For TFT-LCD Module 3. 5" ~15": (Ver.B01) NO Item Criterion Level 9.1 Backlight can't work normally. Major **Backlight** 09 9. 2 Backlight doesn't light or color is wrong. Major elements 9. 3 Illumination source flickers when lit. Major 10.1 Pin type v quantity v dimension must match type in structure Major diagram. 10. 2 No short circuits in components on PCB or FPC. Major 10.3 Parts on PCB or FPC must be the same as on the Major production characteristic chart .There should be no wrong parts, missing parts or excess parts. General 10 appearance 10. 4 Product packaging must the same as specified on packaging Minor specification sheet. 10. 5 The folding and peeled off in polarizer are not acceptable. Minor 10. 6 The PCB or FPC between B/L assembled distance(PCB or Minor **FPC**) is $\leq 1.5$ mm.



## 4. Reliability Test

### 4.1 Reliability Test Condition

(Ver.B01)

<u>NO.</u>	TEST ITEM	TEST CO	<u>ONDITION</u>	
1	High Temperature Storage Test	Keep in 80 ±5℃ 240 hrs		
2	Low Temperature Storage Test	Keep in −30 ±5°C 240 hrs		
3	High Temperature / High Humidity Storage Test	Keep in 60 °C / 90% R.H duration for 240 hrs (Excluding the polarizer)		
	Tomporature Cualing		$\rightarrow 80^{\circ}C \rightarrow +25^{\circ}C$	
4	Temperature Cycling Storage Test	(30mins) (5mins)	(30mins) (5mins)	
	Storage rest	20 0	Cycle	
		Condition A – Bare Display		
		Air Discharge:	Contact Discharge:	
		Apply 2 KV with 5 times	Apply 250V with 5 times	
		Discharge for each polarity +/-	discharge for each polarity +/-	
		Condition A:		
		Product features can be operated norma	lly after the test and the testing process.	
		function to reduce or abnormal phenor	nenon is occurred. No impact from Es	
		discharge.		
		Condition B –With complete applicati	on/product designed by customer Ladv	
		which meet global ESD standard XXX	XXXXX to sustain below Air and Conta	
5	rmed at customer side directly.			
-	ESD Test	Air Discharge:	Contact Discharge:	
		Apply 15KV with 5 times	Apply 8KV with 5 times	
		Discharge for each polarity +/-	discharge for each polarity +/-	
		Condition B:		
			ally hofers the test, but the testing pres	
		Product features may be operating normally before the test, but the testing proce		
		affected by ESD discharge and resulted in reducing or abnormal function		
		Auto-reply function must be done to reset (Re-set) or via the operator's move, t		
		function will go back to normal. Application / Product itself should include ESD protection design to me		
		international standard of each product category.		
		internetional stand i - f i	- <b>4</b>	



<u>NO.</u>	TEST ITEM	TEST CONDITION		
6	Vibration Test (Packaged)	<ol> <li>Sine wave 10~55 Hz frequency (1 min/sweep)</li> <li>2. The amplitude of vibration: 1, 5 mm</li> <li>3. Each direction (X, Y, Z) duration for 2 hrs</li> </ol>		
7	Drop Test (Packaged)	Packing Weight (Kg)         Drop Height (cm)           0 ~ 45.4         122           45.4 ~ 90.8         76           90.8 ~ 454         61           Over 454         46		

 $\bigcirc$  Result Evaluation Criteria :

Under the display quality test conditions with normal operations with normal operation state. Do not change these conditions as such changes may affect practical display function. (Normal operation state)

Temperature : +20~30°C

Humidity: 50~70%

Atmospheric pressure: 86~106Kpa



#### **5. PRECAUTION RELATING PRODUCT HANDLING**

#### 5.1 SAFETY

- 5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

#### 5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module, be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So, please handle it very carefully, do not touch, push or rub the exposed polarizing with anything harder than an HB pencil lead (glass, tweezers, etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands, this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is  $320 \pm 10^{\circ}$ C and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM
- 5.2.10 Caution!( LCM products with Capacitive Touch Panel)Strong EMI-sources such as switch-mode power supplies (SMPS) can lead to touch malfunction (e.g. ghost-touches).

Therefore, the touch needs to be thoroughly tested inside the target application.

- 5.2.11 CAUTION: Continuously displaying same static image will result in high possibility of image sticking/image burn-in effect due to TFT panel characteristic.
- 5.2.12 Double-sided tape designed to be attach with the customer's mechanical device, please follow up the rules and regulations published by the original manufacturer of double-sided tape for the attachment operation.

#### 5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is  $25^{\circ}C \pm 5^{\circ}C$  and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush, shake, or jolt the module.

#### 5.4 TERMS OF WARRANTY

- 5.4.1 Applicable warrant period The period is within thirteen months since the date of shipping out under normal using and storage conditions.
- 5.4.2 Unaccepted responsibility

This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.

