SP	FC	IFI	CAT		NS
JE	$-\mathbf{c}$			10	110

CUSTOMER . PTC

SAMPLE CODE . SH240320T068-LAB08

MASS PRODUCTION CODE . PH240320T068-LAB08

SAMPLE VERSION . 01

SPECIFICATIONS EDITION . 003

DRAWING NO. (Ver.) . JLMD-PH240320T068-LAB08_001

PACKAGING NO. (Ver.) . JPKG-PH240320T068-LAB08_001

Customer Approved

Date:

JS RD APPROVED

er

Approved	Checked	Designer
閆偉	劉進	陳璐

☐ Preliminary specification for design input

■ Specification for sample approval

POWERTIP TECH. CORP.

Headquarters:

No.8, 6th Road, Taichung Industrial Park,

Taichung, Taiwan

台中市 407 工業區六路 8號

TEL: 886-4-2355-8168

FAX: 886-4-2355-8166

E-mail: sales@powertip.com.tw

Http://www.powertip.com.tw



History of Version

Date (mm / dd / yyyy)	Ver.	Edi.	Description	Page	Design by
01/18/2019	01	001	New Drawing	-	陳璐
03/05/2019	01	002	New Sample	-	陳璐
04/09/2019	01	003	Modify Initial code	16~18	陳璐

Total: 33 Page



Contents

1. SPECIFICATIONS

- 1.1 Features
- 1.2 Mechanical Specifications
- 1.3 Absolute Maximum Ratings
- 1.4 DC Electrical Characteristics
- 1.5 Optical Characteristics
- 1.6 Backlight Characteristics
- 1.7 Touch Panel Characteristics

2. MODULE STRUCTURE

- 2.1 Counter Drawing
- 2.2 Interface Pin Description
- 2.3 Timing Characteristics

3. QUALITY ASSURANCE SYSTEM

- 3.1 Quality Assurance Flow Chart
- 3.2 Inspection Specification

4. RELIABILITY TEST

4.1 Reliability Test Condition

5. PRECAUTION RELATING PRODUCT HANDLING

- 5.1 Safety
- 5.2 Handling
- 5.3 Storage
- 5.4 Terms of Warranty

Appendix: LCM Drawing

LCM Packaging

Note: For detailed information please refer to IC data sheet:

Primacy(TFT LCD): Sitronix: ST7789VI



1. SPECIFICATIONS

1.1 Features

Main LCD panel

Item	Standard Value
Display Type	240(R · G · B) * 320 Dots
LCD Type	Normally white , Transmissive type
Screen size(inch)	2.8 inch
Viewing Direction	12 O'clock
Color configuration	RGB-Strip
Interface	80-16bit parallel I/F Ⅱ
Other(controller/driver IC)	Sitronix: ST7789VI
	THIS PRODUCT CONFORMS THE ROHS OF PTC
ROHS	Detail information please refer website :
	http://www.powertip.com.tw/news.php?area_id_view=1085560481/

1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	50.0(W) * 69.2 (L) * 4.25 (H)max	mm

LCD panel

Item	Standard Value		
Active Area	43.2 (W) * 57.6 (L)	mm	





1.3 Absolute Maximum Ratings

Module

Item	Symbol	Condition	Min.	Max.	Unit
Contain David Constant	VCC	-	-0.3	+4.6	V
System Power Supply Voltage	VGH ~ VGL	-	-0.3	+30	V
Input Voltage	VIN	-	-0.3	VCC+0.5	V
Operating Temperature	T _{OP}	-	-20	+70	°C
Storage Temperature	T _{ST}	-	-30	+80	°C
Storage Humidity	H _D	Ta ≤ 40 °C	20	90	%RH

1.4 DC Electrical Characteristics

Module GND = 0V, Ta = 25°C

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Power Supply Voltage1	VCC	·	2.4	2.8	3.6	V
Input High Voltage	ViH	-	0.7 VCC	1	VCC	V
Input Low Voltage	VIL	-	GND	1	0.3 VCC	V
Output High Voltage	Vон	IOH=-0.1mA	0.8*VCC	1	VCC	V
Output Low Voltage	V _{OL}	IOL=0.1mA	GND	-	0.2*VCC	V
Supply Current	ICC	VCC = 2.8V	-	8	12	mA

Note1:Maximum current display



1.5 Optical Characteristics

TFT LCD Module

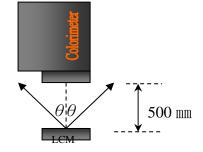
VCC = 2.8V, Ta=25°C

Item		Symbol	Condition	Min.	Тур.	Max.	unit	-
Response tim	ne	Tr+ Tf	-	-	30	45	ms	Note2
	Тор	θΥ+		-	60	-		
Viewing angle	Bottom	θΥ-	CR ≥ 10	ı	60	-	Deg.	Note4
viewing angle	Left	θΧ-	CR 2 10	ı	60	-	Deg.	NOIC4
	Right	θX+		1	60	-		
Contrast rati	o	CR	-	500	600	-	-	Note3
	\\/bito	Х		0.25	0.30	0.35		
	White	Υ		0.26	0.31	0.36		
0.10.0.4.015	Red	Х		0.57	0.62	0.67		
Color of CIE Coordinate	Reu	Υ	ΙΓ-00 m Δ	0.31	0.36	0.41		
(With B/L)	Green	X	IF=80 mA	0.28	0.33	0.38		
(2, 2)	Oreen	Υ		0.55	0.60	0.65		
	Blue	Χ		0.10	0.15	0.20		Note1
	Diue	Υ	Y		0.08	0.13		
Average Brightr	ness							
Pattern=white di	splay	IV	IF=80 mA	200	250	-	cd/m ²	
(With B/L) *	1	4						
Uniformity (With B/L)*2	2	△В	IF=80 mA	70	-	-	%	

Note 1:

- *1 : △B=B(min) / B(max) * 100%
- *2 : Measurement Condition for Optical Characteristics:
 - a: Environment: 25°C±5°C / 60±20%R.H, no wind, dark room below 10 Lux at typical lamp current and typical operating frequency.
 - b : Measurement Distance: $500 \pm 50 \text{ mm}$, $(\theta = 0^{\circ})$
 - c: Equipment: TOPCON BM-7 fast, (field 1°), after 10 minutes operation.
 - d: The uncertainty of the C.I.E coordinate measurement ±0.01, Average Brightness ± 4%





Colorimeter=BM-7 fast

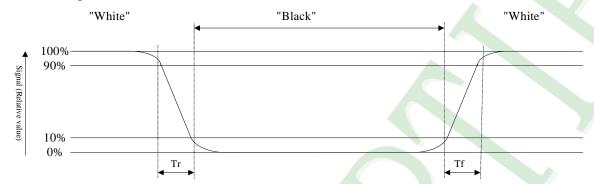
To be measured at the center area of panel with a viewing cone of 1° by Topcon luminance meter BM-7, after 10 minutes operation (module)



Note2: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time), respectively. The response time is defined as the time interval between the 10% and 90% of Amplitudes.

Refer to figure as below:



Note3: Definition of contrast ratio:

Contrast ratio is calculated with the following formula

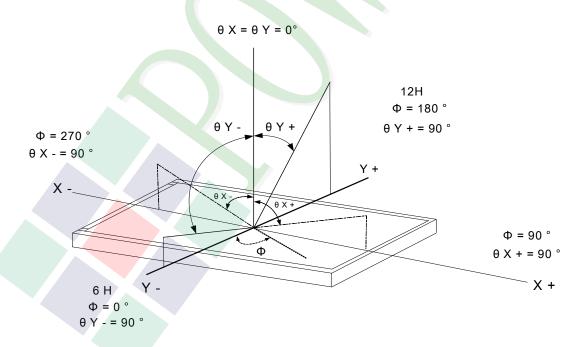
Photo detector output when LCD is at "White" state

Contrast ratio (CR) =

Photo detector output when LCD is at "Black" state

Note4: Definition of viewing angle:

Refer to figure as below:





1.6 Backlight Characteristics

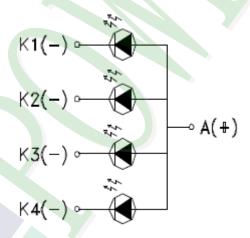
Maximum Ratings

Item	Symbol	Conditions	Min.	Max.	Unit
Forward Current	IF	Ta =25°℃	-	30*4	mA
Reverse Voltage	VR	Ta =25°ℂ	-	5	V
Power Dissipation	PD	Ta =25°ℂ	- (90*4	mW

Electrical / Optical Characteristics

Item	Symbol	Conditions	Min.	Тур.	Max.	Unit
Forward Voltage	VF		3.0	_	3.6	V
Average Brightness (without LCD)	IV	IF= 80 mA	5000	5500	<i>></i>	cd/m ²
CIE Color Coordinate	Х		0.26	0.28	0.33	
(Without LCD)	Y		0.26	0.28	0.33	-
Color			White			

Internal Circuit



Other Description

Item	Conditions	Description
Life Time	Ta =25°ℂ IF= 80 mA	20000 hrs



1.7 Touch Panel Characteristics

1.7.1 Optical Characteristics

Item	Specification			
1.Transparency	80% Min			

1.7.2 Mechanical Characteristic

Item	Specification
1.Input Method	Finger or stylus pen
2.Hardness of surface	3 H-pressure 500g of ,45deg.
3.FPC peeling strength	50gf min(Peeling upward by 90°)
4.Activation Force	50gf(Typical 20gf) less individual point with stylus pen(R0.8mm)
	Activation force guarantee area:5.0mm inside of Active Area.
5.Linearity Force	80gf less input with stylus pen(R0.8mm)
	Linearity force guarantee area:3.0mm inside of Active Area.

1.7.3 Electrical Characteristics

Item	Specification
1.Rated Voltage	DC 5V(DC 10V Max)
2.Resistance Between	Direction X (Glass side): 100Ω~ 600Ω
Terminals.	Direction Y (Film side): 250Ω~ 900Ω
3.Insulation Resistance	20 MΩ or more (DC 25 V 1min)
4.Linearity	 ≤1.5%. Linearity(%)= ΔV/ (EV-SV) *100. ΔV: The difference between the ideal voltage and measured voltage on the each measuring line. SV: Voltage of starting Points. EV: Voltage of Ending Points.
5.Bouncing	<10ms (Tip R 3.75mm, hardness 10°~20°, silicon rubber, 500gf operation : 40 mm/sec)



1.7.4 Reliability Characteristic

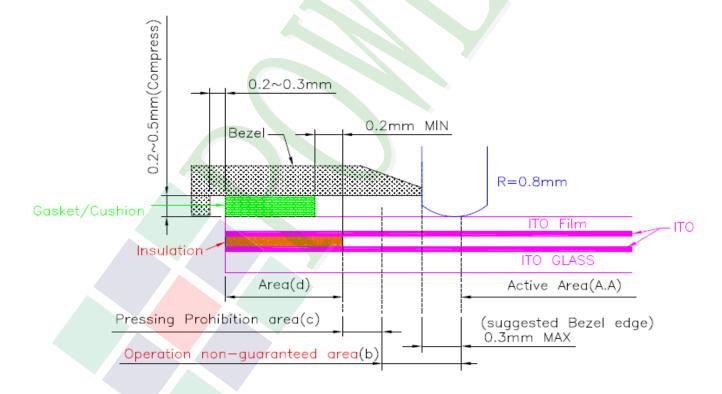
Item	Specification				
	·				
1.Hitting Durability	1,000,000times min.(Tip R 8mm&R0.8mm)				
2.Pen Sliding Durability	100,000 times min(Tip R0.8mm).				
	No damage when ψ9mm steel ball is dropped on the surface from 30 cm				
3.Impact Resistance	height at 1 time.				
4.Flexible pattern Bending	Bending 3 times by bending radius R1.0 mm				
Resistance					
5.Flexible Pattern Insert/Pull	5times at least .				
Out Resistance	Julies at least.				



1.7.5 Touch Panel Design/Handing Guide

- (1) Keep the gap, for example 0.2 to 0.3mm, between bezel edge and T/P edge.

 The reason is to avoid the bezel edge from contacting T/P surface that may cause "short" with bottom layer
- (2) Insertion a cushion material is recommended.
- (3) The cushion material should be limited on the busbar insulation paste area. If it is over the transparent insulation paste area, a "short" may be occurred.
- (4) Do not to use an adhesive tape to bond it on the front of T/P and hang it to the housing bezel.
- (5) Never expand the T/P top layer (PET Film) like a balloon by internal air pressure. The life of the T/P will extremely decreasing.
- (6) Top layer, PET, dimension is changing base on environment temperature and humidity. Please avoid a stress from housing bezel to top layer, because it may cause "waving".
- (7) The input to the Touch Panel sometimes distorts touch panel itself.
- (8)To use the stylus pen or fingernail sliding at the edge of the housing is prohibited. It would cause the cracking of the ITO coating and damage the touch panel. It also request not to press this area while assembling
- (9) Purpose: In order to prevent accidental use and performance deterioration, please keep the following precautions.



In order to prevent unusual performance degradation and malfunction of a touch panel, please carry out the set case designing and a touch panel assembling method after surely considering the definition of each area illustrated in above figure.



Area(a): Active area

The active area is guaranteed the position data detectable precision, operation force and other operations. it is strongly recommended to place the operation button or menu keys within the active area. Due to structure, the active area is less durable at the edge or close to the edge.

Area(b): Operation non-guaranteed area

This area does not guarantee a touch panel operation and its function. When this area is pressed, touch panel shows degradation of its performance and durability such as a pen sliding durability becomes about one-tenth compared with the active area (area-(a) as guaranteed area) and its operation force requires about double. About 0.5 mm outside from a boundary of the active area corresponds to this area.

Area(c): Pressing prohibition area

The area which forbids pressing, because an excessive load is applied to a transparent electrode (ITO) and a serious damage is given to a touch panel function by pressing. About 0.5 mm outside from Operation non-guaranteed area.

Area(d): Non-Active area

The area does not activate even if pressed.



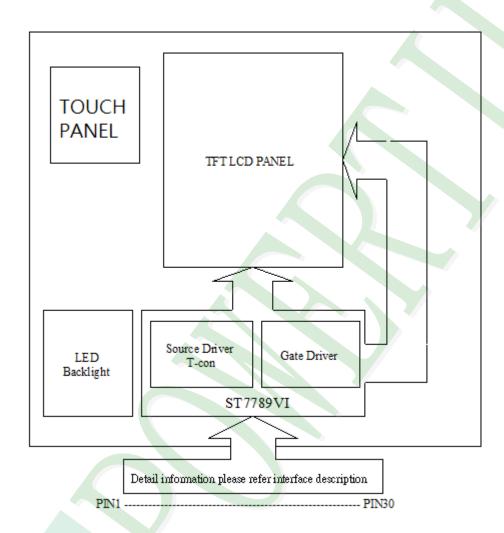


2.1 Counter Drawing

2.1.1 LCM Mechanical Diagram

* See Appendix

2.1.2 Block Diagram





2.2 Interface Pin Description

Pin No.	Symbol	Function
1	LEDK1-4	Power supply for LED Backlight Cathode input
2	LEDA	Power supply for LED Backlight Anode input
3	GND	Signal ground.(0V)
4	RESET	Reset input pin for TFT LCD. When RESET is "L", initialization is executed.
5	DB17	
6	DB16	
7	DB15	
8	DB14	
9	DB13	
10	DB12	
11	DB11	
12	DB10	Bi-directional data bus
13	DB8	Di-unectional data bus
14	DB7	
15	DB6	
16	DB5	
17	DB4	
18	DB3	
19	DB2	
20	DB1	
21	RD	Read signal input , active at Low.
22	WR/SCL	Write signal input , active at Low.
23	RS	When RS = 0: Command. When RS = 1: Display data.
24	CS	Chip select signal , Active at "L"
25	XR	Right side of touch panel.



Pin No.	Symbol	Function
26	YD	Bottom side of touch panel.
27	XL	Left side of touch panel.
28	YU	Up side of touch panel.
29	GND	Signal ground.(0V)
30	2.8 /VCC	Power supply for the internal logic circuit.



```
2.2.1 Reference Initial code
void LCD Init(void)
{
   LCD_WR_REG(0x01);
   delay_ms(100);
   LCD WR REG(0x11);
   delay ms(120);
   LCD WR REG(0x36);
   LCD_WR_DATA(0x00);
   LCD WR REG(0x3a);
   LCD_WR_DATA(0x55);
   LCD WR REG(0xb2);
   LCD WR DATA(0x0C);
   LCD WR DATA(0x0C);
   LCD WR DATA(0x00);
   LCD WR DATA(0x33);
   LCD WR DATA(0x33);
   LCD WR REG(0xb7);
   LCD WR DATA(0x35);
   LCD WR REG(0xbb);
   LCD WR DATA(0x19);
   LCD WR REG(0xc0);
   LCD WR DATA(0x2c);
   LCD WR REG(0xc2);
   LCD_WR_DATA(0x01);
   LCD WR REG(0xc3);
   LCD WR DATA(0x12);
```



```
LCD WR REG(0xc4);
  LCD WR DATA(0x20);
  LCD WR REG(0xc6);
  LCD WR DATA(0x0f);
  LCD WR REG(0xd0);
  LCD WR_DATA(0xa4);
  LCD WR DATA(0xa1);
/*-----*/
  LCD WR REG(0xe0);
  LCD WR DATA(0xd0);
  LCD_WR_DATA(0x04);
  LCD_WR_DATA(0x0d);
  LCD WR DATA(0x11);
  LCD WR DATA(0x13);
  LCD WR DATA(0x2b);
  LCD WR DATA(0x3f);
  LCD WR DATA(0x54);
  LCD WR DATA(0x4c);
  LCD WR DATA(0x18);
  LCD_WR_DATA(0x0d);
  LCD WR DATA(0x0b);
  LCD WR DATA(0x1f);
  LCD WR DATA(0x23);
  LCD WR REG(0xe1);
  LCD WR DATA(0xd0);
  LCD_WR_DATA(0x04);
  LCD WR DATA(0x0c);
  LCD WR DATA(0x11);
  LCD_WR_DATA(0x13);
  LCD WR DATA(0x2c);
  LCD_WR DATA(0x3f);
  LCD WR DATA(0x44);
  LCD WR DATA(0x51);
  LCD WR DATA(0x2f);
  LCD WR DATA(0x1f);
```



```
LCD_WR_DATA(0x1f);
  LCD_WR_DATA(0x20);
  LCD_WR_DATA(0x23);
/*-----*/
  LCD_WR_REG(0x29); //Display on
}
```



2.3 Timing Characteristics

8080 Series MCU Parallel Interface Characteristics: 18/16/9/8-bit Bus

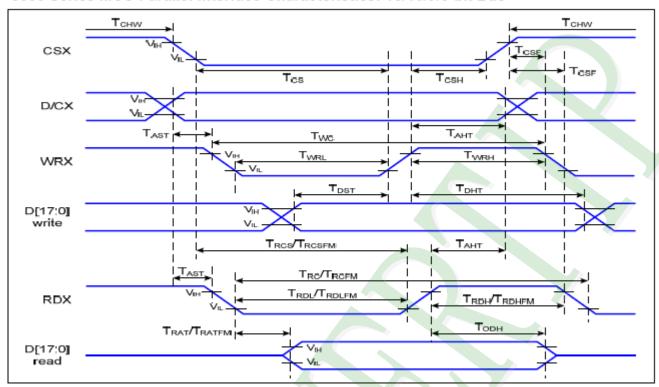


Figure 1 Parallel Interface Timing Characteristics (8080-Series MCU Interface)



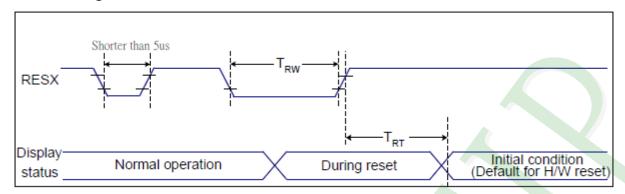


Signal	Symbol	Parameter	Min	Max	Unit	Description
D/CX	T _{AST}	Address setup time	0		ns	
DICX	T _{AHT}	Address hold time (Write/Read)	10		ns	
	T _{CHW}	Chip select "H" pulse width	0		ns	
	T _{cs}	Chip select setup time (Write)	15		ns	
CSX	T _{RCS}	Chip select setup time (Read ID)	45		ns	
CSA	T _{RCSFM}	Chip select setup time (Read FM)	355		ns	-
	T _{CSF}	Chip select wait time (Write/Read)	10		ns	
	T _{CSH}	Chip select hold time	10		ns	
	T _{wc}	Write cycle	66		ns	
WRX	T _{WRH}	Control pulse "H" duration	15		ns	
	T_{WRL}	Control pulse "L" duration	15		ns	
	T _{RC}	Read cycle (ID)	160		ns	
RDX (ID)	T_{RDH}	Control pulse "H" duration (ID)	90		ns	When read ID data
	T_{RDL}	Control pulse "L" duration (ID)	45		ns	
RDX	T _{RCFM}	Read cycle (FM)	450		ns	When read from
(FM)	T _{RDHFM}	Control pulse "H" duration (FM)	90		ns	frame memory
(I IVI)	T _{RDLFM}	Control pulse "L" duration (FM)	355		ns	manie memory
D[17:0]	T _{DST}	Data setup time	10	þ	ns	For CL=30pF
	T _{DHT}	Data hold time	10		ns	
	T _{RAT}	Read access time (ID)		40	ns	
	T_{RATFM}	Read access time (FM)		340	ns	
	T _{ODH}	Output disable time	20	80	ns	

8080 Parallel Interface Characteristics



Reset Timing:



Reset Timing

VDDI=1.65 to 3.6V, VDD=2.4 to 3.6V, AGND=DGND=0V, Ta=25 ℃

Related Pins	Symbol	Parameter	MIN	MAX	Unit
RESX	TRW	Reset pulse duration	10 -		us
	TRT Reset cancel	Donat cancel	-	5 (Note 1, 5)	ms
		Reset cancer		120 (Note 1, 6, 7)	ms

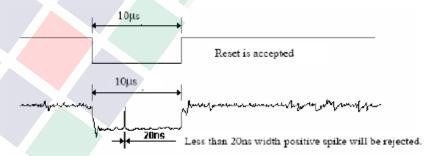
Reset Timing

Notes:

- The reset cancel includes also required time for loading ID bytes, VCOM setting and other settings from NVM (or similar device) to registers. This loading is done every time when there is HW reset cancel time (tRT) within 5 ms after a rising edge of RESX.
 - 2. Spike due to an electrostatic discharge on RESX line does not cause irregular system reset according to the table below:

RESX Pulse	Action
Shorter than 5us	Reset Rejected
Longer than 9us	Reset
Between 5us and 9us	Reset starts

- 3. During the Resetting period, the display will be blanked (The display is entering blanking sequence, which maximum time is 120 ms, when Reset Starts in Sleep Out –mode. The display remains the blank state in Sleep In –mode.) and then return to Default condition for Hardware Reset.
 - 4. Spike Rejection also applies during a valid reset pulse as shown below:

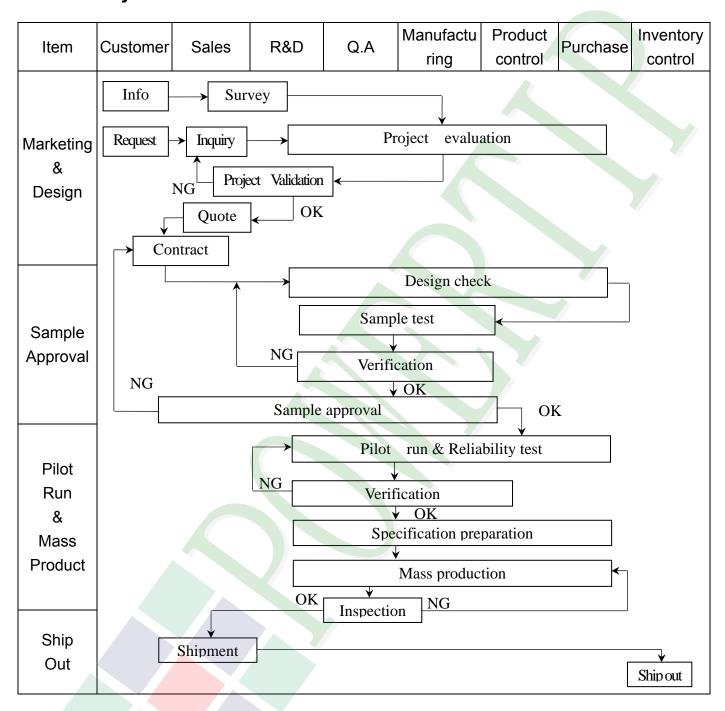


- 5. When Reset applied during Sleep In Mode.
- 6. When Reset applied during Sleep Out Mode.
- It is necessary to wait 5msec after releasing RESX before sending commands. Also Sleep Out command cannot be sent for 120msec.

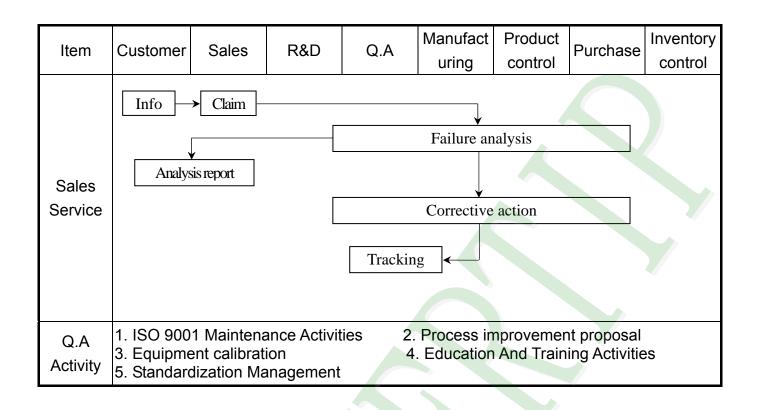


3. QUALITY ASSURANCE SYSTEM

3.1 Quality Assurance Flow Chart









3.2 Inspection Specification

◆Scope : The document shall be applied to TFT-LCD Module for less than 3, 5" (Ver.B01).

◆Inspection Standard: MIL-STD-105E Table Normal Inspection Single Sampling Level Ⅱ.

◆Equipment: Gauge、MIL-STD、Powertip Tester、Sample

◆Defect Level: Major Defect AQL: 0, 4 ; Minor Defect AQL: 1, 5

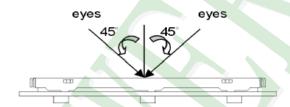
◆OUT Going Defect Level: Sampling.

◆Standard of the product appearance test:

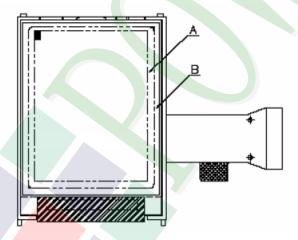
a. Manner of appearance test:

(1). The test best be under 20W×2 fluorescent light, and distance of view must be at 30 cm.

(2). The test direction is base on about around 45° of vertical line.



(3). Definition of area.



A area: viewing area

B area : Outside of viewing area

(4). Standard of inspection: (Unit: mm)



NO	Item		Criterion			Level
			1. 1The part number is inconsistent with work order of production.			Major
01	01 Product condition	1. 2 Mi	ixed prod	uct types.		Major
		1. 3 As	sembled i	in inverse direction.		Major
02	Quantity	2. 1The	2. 1The quantity is inconsistent with work order of production.			
03	Outline dimension		3. 1 Product dimension and structure must conform to structure diagram.			e Major
		4. 1 Mi	issing line	character and icon		Major
		4, 2 No function or no display.				Major
04	Electrical Testing	4. 3 Display malfunction.			Major	
		4. 4 LCD viewing angle defect.			Major	
		4. 5 Current consumption exceeds product specifications.			Major	
				Item	Acceptance (Q'ty)	
	Dot defect			Bright Dot	≦ 2	
			Dot	Dark Dot	≦ 3	
0.5	(Bright dot \		Defect	Joint Dot	≦ 2	3.51
05	Dark dot)			Total	≦ 3	Minor
		5. 1 Ins	spection	pattern : full white	, full black , Red , Green an	d
	On -display			blue screen	18.	
		5. 2 It i	is defined	as dot defect if defe	ect area $>1/2$ dot.	
	5. 3 The distance between two dot defect ≥ 5 mm.					



NO	Item	Criterion				
		6. 1 Round type (Non-display or display) :				
		Dimension	Acceptance	(Q'ty)		
	Black or white	(diameter ÷ Φ)	A area	B area		
	dot v scratch v	$\Phi \le 0.15$	Ignore			
	contamination	$0.15 < \Phi \leq 0.20$	2			
	Round type	$0.20 \ < \ \Phi \leq 0.30$	2	Ignore		
	→ _X ← _↓	$\Phi > 0.30$	0			
06	Y Y	Total	3		Minor	
	$\Phi = (x+y)/2$	6. 2 Line type(Non-display or d	lisplay) :	7	NIII OI	
	Line type	Dimension	Acceptai	ice (Q'ty)		
	∠ ↓	Length (L) Width (W)	A area	B area		
	¯ ₩	W ≤ 0.0	03 Ignore			
	→ _L	$L \le 5.0$ $0.03 < W \le 0.0$	05 3			
		W >0.0	05 As round type	Ignore		
		Total	3			
		Dimension (diameter : Φ)	Acceptance			
			A area	B area		
07	Polarizer	$\Phi \leq 0.20$	Ignore		N.T	
01	Bubble	$0.20 < \Phi \leq 0.50$	3	Ignore	Minor	
		Φ > 0.50	0	1511010		
		Total	3			



NO	Item	Criterion		Level
		Z : The thickness of crack	Y : The width of crack. W : terminal length a : LCD side length	
		8.1 General glass chip: 8.1.1 Chip on panel surface and cra	nck between panels:	
		Y Z	Z Y	
08	The crack of glass	SP Y [OK]	SP [NG]	Minor
		Seal width	Y	
		X Y	Z	
		≤ a Crack can't enter viewing area	≤1/2 t	
		≤ a Crack can't exceed the half of SP width.	1/2 t < Z ≤2 t	
4				



NO	Item	Criterion (ver	
		Symbols: X: The length of crack Z: The thickness of crack t: The thickness of glass 8. 1. 2 Corner crack:	
		X Y Z $\leq 1/5 \text{ a}$ Crack can't enter $Z \leq 1/2 \text{ t}$	
08	The crack of glass	viewing area Solution $2 = 1/2 t$ Viewing area Solution $2 = 1/2 t$ Solution $2 = 1$	Minor
	8.2 Protrusion over terminal:	-	
	8.	8. 2. 1 Chip on electrode pad: W X W X X X X X X X X X X	
		X Y Z	
		Front $\leq a$ $\leq 1/2 W$ $\leq t$	
		Back \leq a \leq W \leq 1/2 t	



Symbols:	
The crack of glass The crack of glass The crack of glass $X: The thickness of crack $	Minor



NO	Item	Criterion	Level
09	Backlight elements	9. 1 Backlight can't work normally.	Major
		9. 2 Backlight doesn't light or color is wrong.	Major
		9. 3 Illumination source flickers when lit.	Major
	General appearance	10. 1 Pin type `quantity `dimension must match type in structure diagram.	Major
		10. 2 No short circuits in components on PCB or FPC .	Major
10		10. 3 Parts on PCB or FPC must be the same as on the production characteristic chart .There should be no wrong parts , missing parts or excess parts.	Major
10		10. 4 Product packaging must the same as specified on packaging specification sheet.	Minor
		10.5 The folding and peeled off in polarizer are not acceptable.	Minor
		10. 6 The PCB or FPC between B/L assembled distance(PCB or FPC) is ≤1.5 mm.	Minor



4. RELIABILITY TEST

4.1 Reliability Test Condition

NO.	TEST ITEM	TEST CONDITION			
1	High Temperature Storage Test	Keep in +80 ±2°C 240hrs Surrounding temperature, then storage at normal condition 4hrs.			
2	Low Temperature Storage Test	Keep in −30 ±2°C 240hrs Surrounding temperature, then storage at normal condition 4hrs.			
3	High Temperature / High Humidity Storage Test	Keep in +60 °C /90% R.H duration for 240hrs Surrounding temperature, then storage at normal condition 4hrs. (Excluding the polarizer)			
4	Temperature Cycling Storage Test	$-30^{\circ}\mathbb{C} \rightarrow +25^{\circ}\mathbb{C} \rightarrow +80^{\circ}\mathbb{C} \rightarrow +25^{\circ}\mathbb{C}$ $(30_{\text{mins}}) (5_{\text{mins}}) (5_{\text{mins}})$ 20 Cycle Surrounding temperature, then storage at normal condition 4hrs.			
5	ESD Test	Air Discharge: Apply 2 KV with 5 times Discharge for each polarity +/- 1. Temperature ambiance: $15^{\circ} \sim 35^{\circ} \sim 35^$			
6	Vibration Test (Packaged)	 Sine wave 10~55 Hz frequency (1 min/sweep) The amplitude of vibration :1. 5 mm Each direction (X \ Y \ Z) duration for 2 Hrs 			
7	Drop Test (Packaged)	Packing Weight (Kg	122 76 61 46		



5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

- 5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module, be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So , please handle it very carefully ,do not touch , push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands, this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is 320±10°C and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM.
- 5.2.10 Caution!(LCM products with Capacitive Touch Panel)
 Strong EMI-sources such as switch-mode power supplies (SMPS) can lead to touch malfunction (e.g. ghost-touches).

Therefore, the touch needs to be thoroughly tested inside the target application.

5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is 25° C $\pm 5^{\circ}$ C and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush, shake, or jolt the module.

5.4 TERMS OF WARRANTY

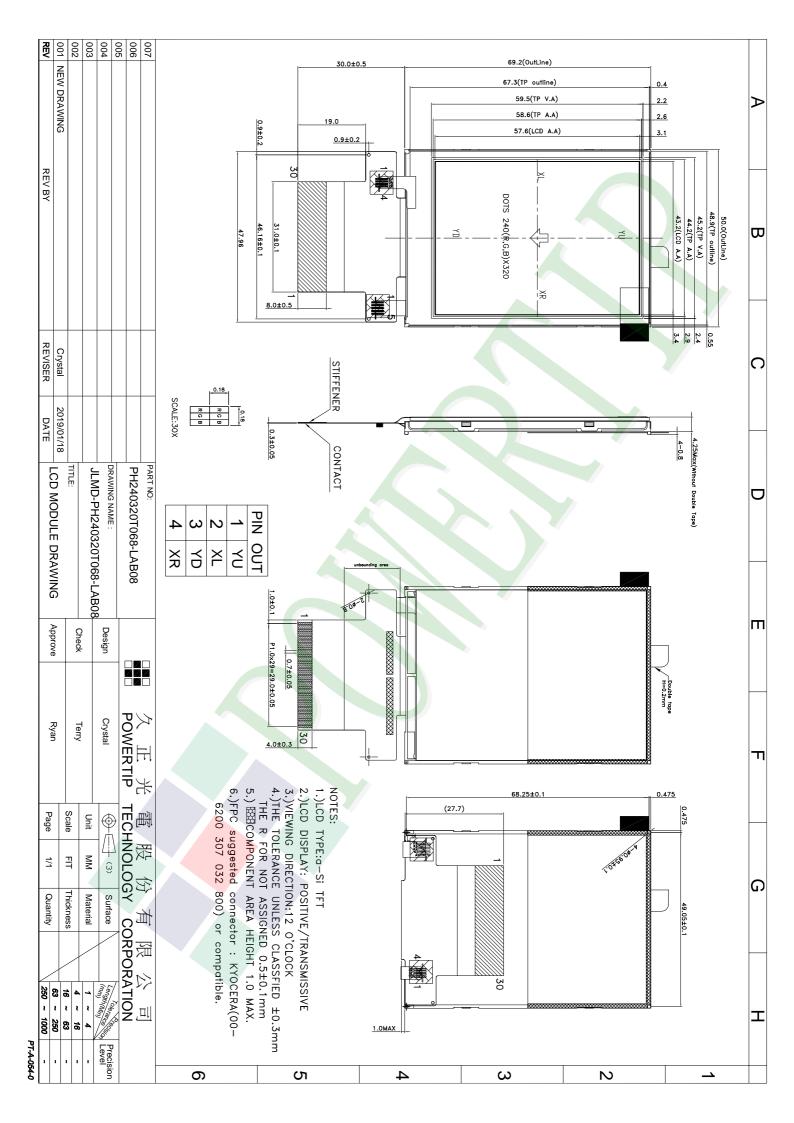
- 5.4.1 Applicable warrant period

 The period is within thirteen months since the date of shipping out under normal using and storage conditions.
- 5.4.2 Unaccepted responsibility
 - This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in



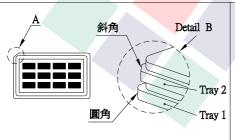
nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.





Approve Check Contact Ver.001 LCM包裝規格書 LCM Packaging Specifications Ryan Terry Crystal Documents NO. JPKG-PH240320T068-LAB08 (For Tray) 1.包裝材料規格表 (Packaging Material): (per carton) Item 1Pcs Weight Total Weight No. Model Dimensions (mm) Quantity 成品 (LCM) PH240320T068-LAB08 69.2 X 50 X 4.25 504 1 0.021 10.584 2 6 多層薄膜(1)POF 19"X350X0.015 OTFILM0BA03ABA 3 TRAY 盤 (2)Tray TYSG000000030 352 X 260 X 12.3 48 4.8 0.1 4 内盒(3)Product Box BX36627063ABBA 383 X 270 X 66 0.182 6 1.092 OTPLB00PL08ABA 2 5 550 X 393 X 20 0.0284 0.0568 保利龍板(4)Polylon board 6 外紙箱(5)Carton BX57041027CCBA 570 X 410 X 265 1.0 1 1.0 7 8 9 2.一 整箱總重量 (Total LCD Weight in carton): 3.單箱數量規格表 (Packaging Specifications and Quantity): (1)LCM quantity per box : no per tray x no of tray 7 12 84 (2) Total LCM quantity in carton: quantity per box x no of boxes 504 84 6 (4)保利龍板 Use empty tray 空盤 Polylon board (1)多層薄膜 **POF** Put products into the tray (2)TRAY 盤 (4)保利龍板 Tray Polylon board (3)内盒 Tray stacking Product Box (5)外紙箱 Carton

特 記 事 項 (REMARK)



4.TRAY盤相疊時,需旋轉180度,請詳見B視圖 Rotate tray 180 degrees and place on top of stack Check the tray stack using Fig. B.