

SPECIFICATIONS						
PTC						
SH128128T041-LAA07						
PH128128T041-LAA07						
01						
002						
JLMD-PH128128T041-LAA07_002						
JPKG-PH128128T041-LAA07_001						

# Customer Approved

Approved	Checked	Designer
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□ Preliminary specification for design input

Specification for sample approval

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# History of Version

Date	Ver.	Edi.	Description	Page	Design by
2018/2/26	01	001	New Drawing	-	夏子豪
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Total: 29 Pages



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## **1. SPECIFICATIONS**

#### **1.1 Features**

#### Main LCD Panel

Item	Standard Value
Display Type	128(R    G    B) * 128 Dots
LCD Type	Normally white TN, Transmissive type
Screen size(inch)	1.44 inch
Viewing Direction	12 O'clock
Color configuration	R.G.B. vertical stripe
Backlight	White LED
Interface	8 bit Parallel Interface
Other(controller / driver IC)	Sitronix: ST7735S
ROHS	THIS PRODUCT CONFORMS THE ROHS OF PTC Detail information please refer web side : http://www.powertip.com.tw/news.php?area_id_view=1085560481/

# **1.2 Mechanical Specifications**

Item	Standard Value	Unit
Outline Dimension	31.1 (W) * 36.9 (L) * 2.85 (H)	mm

#### **TFT LCD Panel**

Item	Standard Value			
Viewing Area	26.498 (W) *27.496 (L)	mm		
Active Area	25.498 (W) * 26.496 (L)	mm		

Note : For detailed information please refer to LCM drawing.



## **1.3 Absolute Maximum Ratings**

#### Module

Item	Symbol	Condition	Min.	Max.	Unit
Supply Voltage	VDD	-	-0.3	+4.8	V
Supply Voltage(Logic)	VDDI	-	-0.3	+4.6	V
Driver supply voltage	VGH-VGL	-	-0.3	+30.0	V
Logic input voltage range	VIN	-	-0.3	VDDI+0.3	V
Logic output voltage range	VO	-	-0.3	VDDI+0.3	V
Operating Temperature	TOP	-	-20	+70	°C
Storage Temperature	TST	-	-30	+80	°C
Storage Humidity	HD	Ta ≦ 60 °C		90	%RH

#### **1.4 DC Electrical Characteristics**

#### GND = 0V, Ta = 25°C Module Symbol Item Condition Min. Max. Unit Тур. System voltage VDD 2.7 3.0 3.3 V -Interface operation voltage VDDI 1.65 1.8 3.7 V -VIH Logic-high input voltage 2 0.7VDDI \_ VDDI V VIL VSS 0.3VDDI V Logic-low input voltage \_ \_ 0.8VDDI V Logic-high output voltage VOH IOH=-1.0mA VDDI -VOL Logic-low output voltage IOL=+1.0mA VSS 0.2VDDI V \_ Supply Current IDD VDD=3.0V 1.6 2.5 \_ mΑ



# **1.5 Optical Characteristics**

#### TFT LCD Panel

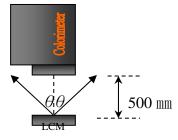
VDD=3.0V, Ta=25°C

		1			r			
Item		Symbol	Condition	Min.	Тур.	Max.	unit	-
Response tin	ne	Tr+ Tf	-	-	-	(	ms	Note2
	Тор	θY+		-	15	-		
Viewing angle	Bottom	θY-		-	45	-	Deg	Note4
	Left	θХ-	CR≥(10)	-	45	ľ	Deg.	NOLE4
	Right	θX+		-	45	1		
Contrast rati	0	CR	-	150	200	-	1	Note3
	\A/I=`1_=	Х		0.25	0.30	0.35		
	White	Y		0.26	0.31	0.36		
	Ded	X Y		0.59	0.64	0.69		
Color of CIE Coordinate	Red		IF=20mA	0.29	0.34	0.39		Note1
(With B/L)	Green	Х		0.27	0.32	0.37		NOICT
	Green	Y		0.52	0.57	0.62		
	Blue	Х		0.09	0.14	0.19		
	Diue	Y		0.05	0.10	0.15		
Average Brightness Pattern=white display I (With B/L)		IV	IF=20mA	200	310	_	cd/m2	Note1
Uniformity (With B/L)		∆B	IF=20mA	80	-	-	%	Note1

Note1:

- $1 : \triangle B = B(min) / B(max) \times 100\%$ .
- 2 : Measurement Condition for Optical Characteristics:
  - a : Environment: 25℃ ±5℃ / 60±20%R.H, no wind, dark room below 10 Lux at typical lamp current and typical operating frequency.
  - b : Measurement Distance: 500 ± 50 mm, ( $\theta$ = 0°).
  - c : Equipment: TOPCON BM-7 fast, (field 1°), after 10 minutes operation.
  - d: The uncertainty of the C.I.E coordinate measurement ±0.01, Average Brightness ± 4%.





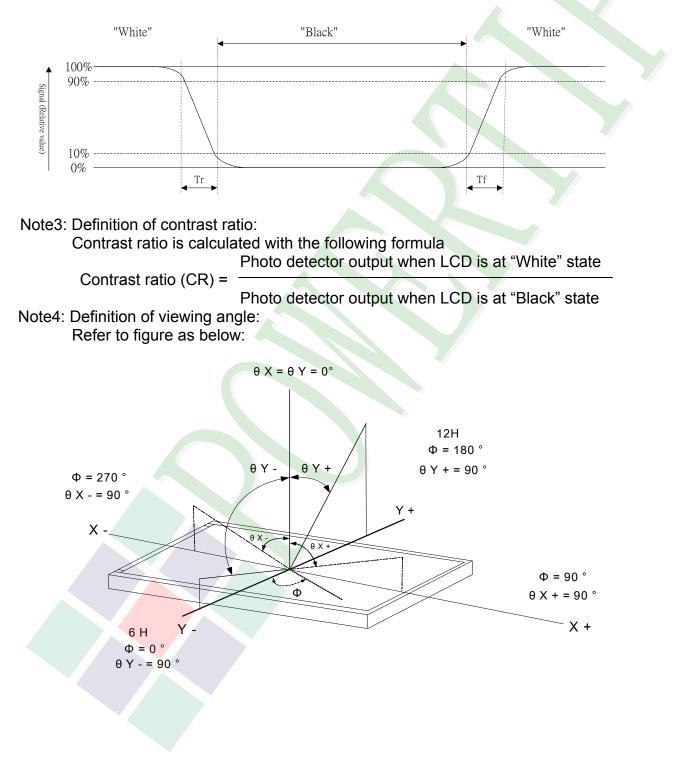
Colorimeter=BM-7 fast



#### Note2: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time), respectively. The response time is defined as the time interval between the 10% and 90% of Amplitudes.

Refer to figure as below:





# **1.6 Backlight Characteristics**

#### Maximum Ratings

Item	Symbol	Conditions	Min.	Max.	Unit
Forward Current	IF	<b>Ta =25</b> ℃	-	30	mA
Reverse Voltage	VR	Ta =25℃	-	5.0	V
Power Dissipation	PD	<b>Ta =25</b> ℃	-	90	mW

#### Electrical / Optical Characteristics

Item	Symbol	Conditions	Min.	Тур.	Max.	Unit
Forward Voltage	VF	IF= 20mA	2.8	3.2	3.6	V
Average Brightness	IV	IF= 20mA	4000	5000	<b>Y</b> -	cd/m <sup>2</sup>
Color of CIE Coordinate	Х	IF= 20mA	0.27	0.30	0.33	
	Y	IF- 2011A	0.27	0.30	0.33	-
Color			White			

Circuit diagram

LED+ o LED-

#### Other Description

Item	Conditions	Description
Life Time	Ta =25℃ IF= 20mA	20000 hrs



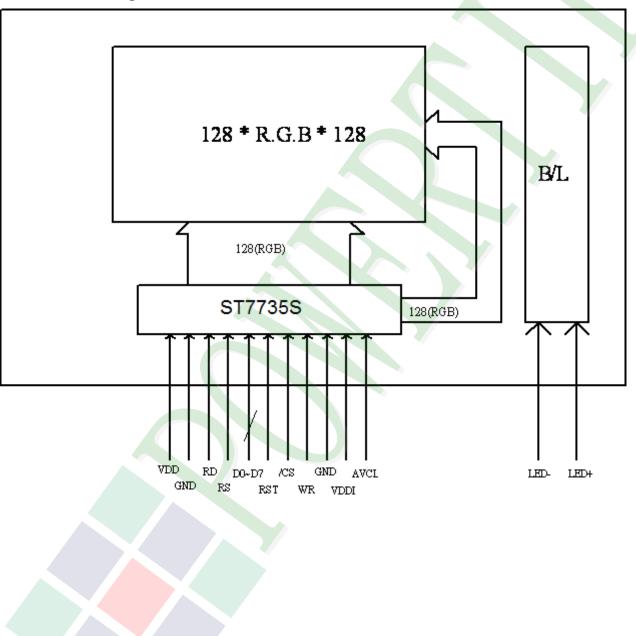
# 2. MODULE STRUCTURE

#### **2.1 Counter Drawing**

#### 2.1.1 LCM Mechanical Diagram

\* See Appendix

#### 2.1.2 Block Diagram





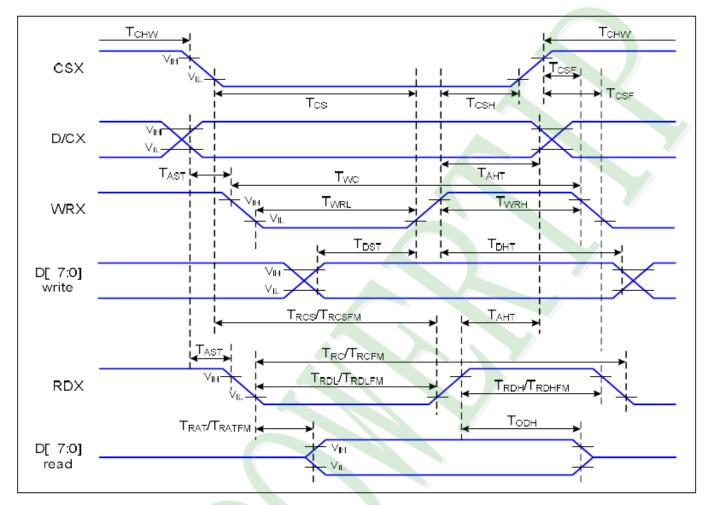
# 2.2 Interface Pin Description

Pin NO	Symbol	Function			
1	LED+	Backlight LED anode input pin.			
2	LED-	Backlight LED cathode input pin.			
3	GND	System ground			
4	VDD				
5	VDD	Power supply for analog, digital system and booster circuit.			
6	RD	Read enable in 8080 MCU parallel interface.			
7	RS	Display data/command selection pin in MCU interface. RS ='1': display data or parameter. RS ='0': command data.			
8	D1				
9	D3				
10	D5	D[7:0] are used as MCU parallel interface data bus.			
11	D7				
12	RST	This signal will reset the device and it must be applied to properly initialize the chip. Signal is active low.			
13	/CS	Chip selection pin, Low enable			
14	D6				
15	D4	DI7:01 are used as MCLL percelled interface data hus			
16	D2	-D[7:0] are used as MCU parallel interface data bus.			
17	D0				
18	WR	Write enable in MCU parallel interface.			
19	GND	Sustem ground			
20	GND	System ground			
21	VDDI	Power supply for I/O system.			
22	AVCL	A power supply pin for generating GVCL.Connect a capacitor for stabilization.			



# 2.3 Timing Characteristics

#### Parallel interface characteristics: 8-bit bus (8080 series MCU interface)



Parallel Interface Timing Characteristics (8080 Ceries MCU Interface)

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#### Ta=25 °C, VDDI=1.65~3.7V, VDD=2.5~4.8V

Signal	Symbol	Parameter	Min	Max	Unit	Description
D/CX	TAST	Address Setup Ttime	0		ns	
DICA	TAHT	Address Hold Time (Write/Read)	10		ns	
	TCHW	Chip Select "H" Pulse Width	0		ns	
	TCS	Chip Select Setup Time (Write)	15		ns	
csx	TRCS	Chip Select Setup Time (Read ID)	45		ns	
057	TRCSFM	Chip Select Setup time (Read FM)	355		ns	
	TCSF	Chip Select Wait Time (Write/Read)	10		ns	
	TCSH	Chip Select Hold Time	10		ns	
	TWC	Write Cycle	66		ns	
WRX	TWRH	Control Pulse "H" Duration	15		ns	
	TWRL	Control Pulse "L" Duration	15		ns	
	TRC	Read Cycle (ID)	160		ns	
RDX (ID)	TRDH	Control Pulse "H" Duration (ID)	90		ns	When Read ID Data
	TRDL	Control Pulse "L" Duration (ID)	45		ns	

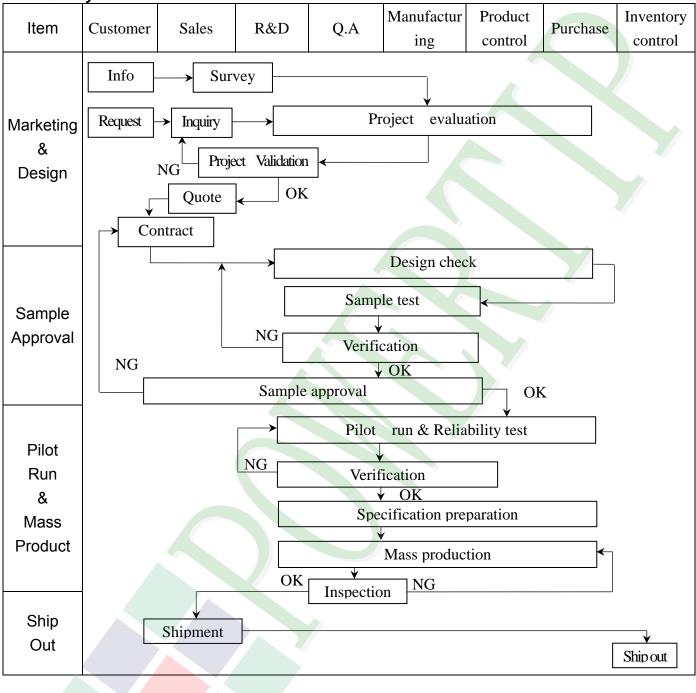
_						
RDX	TRCFM	M Read Cycle (FM)			ns	When Read from
	TRDHFM Control Pulse "H" Duration (FM) 9		90		ns	Frame Memory
(FM)	TRDLFM	Control Pulse "L" Duration (FM)	355		ns	Traine Memory
	TDST	Data Setup Time	10		ns	
	TDHT     Data Hold Time       TRAT     Read Access Time (ID)       TRATFM     Read Access Time (FM)		10		ns	
D[ 7:0]				40	ns	For CL=30pF
				340	ns	
	TODH	Output Disable Time	20	80	ns	

8080 Parallel Interface Characteristics



# **3. QUALITY ASSURANCE SYSTEM**

#### 3.1 Quality Assurance Flow Chart



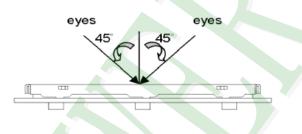


						- · ·		
Item	Customer	Sales	R&D	Q.A	Manufact	Product	Purchase	Inventory
nem	Oustonier	Gales	Rub	Q./ (	uring	control	i uronasc	control
Sales Service	Info Analys	→ Claim sis report	[	Trackin	Failure ar			
Q.A Activity	1. ISO 9001 Maintenance Activities2. Process improvement proposal3. Equipment calibration4. Education And Training Activities5. Standardization Management5. Standardization Management							es

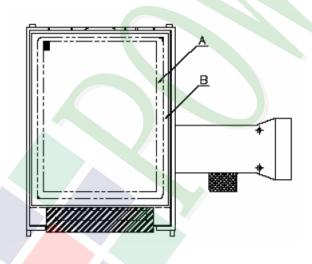
# POWERTIP

# **3.2. Inspection Specification**

- ◆Scope ∶ The document shall be applied to TFT-LCD Module for less than 3.5″ (Ver.B01).
- $\bullet$ Inspection Standard : MIL-STD-105E Table Normal Inspection Single Sampling Level II.
- ◆Equipment:Gauge、MIL-STD、Powertip Tester、Sample
- ◆Defect Level:Major Defect AQL: 0.4 ; Minor Defect AQL: 1.5
- ♦OUT Going Defect Level:Sampling.
- $\clubsuit$ Standard of the product appearance test :
  - a. Manner of appearance test :
  - (1). The test best be under  $20W \times 2$  fluorescent light , and distance of view must be at 30 cm.
  - (2). The test direction is base on about around  $45^\circ$  of vertical line.



(3). Definition of area.



A area : viewing area

**B** area : Outside of viewing area

(4). Standard of inspection : (Unit : mm)



#### $\clubsuit$ Specification For TFT-LCD Module Less Than 3. 5" :

NO 01	Item Product condition	1. 1The part nu production	Criterie umber is inconsistent		Level			
01	Product condition	-		with work order of				
01	Product condition		1.		Major			
		1.2 Mixed proc	duct types.		Major			
		1. 3 Assembled	in inverse direction.		Major			
02	Quantity	2. 1The quantit	2. 1The quantity is inconsistent with work order of production.					
03	Outline dimension	3. 1 Product di diagram.	3. 1 Product dimension and structure must conform to structure diagram.					
		4.1 Missing lin	e character and icon		Major			
		4. 2 No function or no display.						
04	Electrical Testing	4. 3 Display malfunction.						
		4. 4 LCD viewing angle defect.						
		4. 5 Current consumption exceeds product specifications.						
			Item	Acceptance (Q'ty)				
	Dot defect		Bright Dot	$\leq 2$				
	200 00000	Dot	Dark Dot	≦ 3				
05	(Bright dot 、	Defect	Joint Dot	≦ 2				
05	Dark dot)		Total	≦ 3	Minor			
	On -display	5. 1 Inspection pattern : full white , full black , Red , Green and blue screens.						
		5. 2 It is defined as dot defect if defect area $>1/2$ dot. 5. 3 The distance between two dot defect $\ge 5$ mm.						

(Ver.B01)



speci	ication For TFT-LCD	Module Less Than 3.5″ :			(Ver.B0	
NO	Item	Crite	erion		Level	
		6. 1 Round type ( Non-display or display) :				
		Dimension	Acceptance	(Q'ty)		
		(diameter ÷ Φ)	A area	B area		
	Black or white dot、scratch、	$\Phi \leq 0.15$	Ignore			
	contamination	$0.15 < \Phi \leq 0.20$	2			
	Round type	$0.20 \ < \ \Phi \leq 0.30$	2	Ignore		
		$\Phi > 0.30$	0			
06	● Y	Total	3		Mino	
	$\Phi = (x+y)/2$	6. 2 Line type( Non-display or display) :				
	Line type	Dimension	Acceptar	nce (Q'ty)		
	Line type ⊥	Length (L) Width (W)	A area	B area		
	∼ J <sup>†</sup> <sup>w</sup>	$$ $W \leq 0.$	.03 Ignore			
		$L \le 5.0$ 0.03 $< W \le 0.0$	05 3	-		
		W >0.	.05 As round type	I Ignore		
		Total	3			
				1		
		Dimension	Acceptance	(Q'ty)		
		(diameter ÷ Φ)	A area	B area		
	Polarizer	$\Phi \leq 0.20$	Ignore			
07	Bubble	$0.20 < \Phi \leq 0.50$	3	T	Mino	
		$\Phi > 0.50$	0	Ignore		

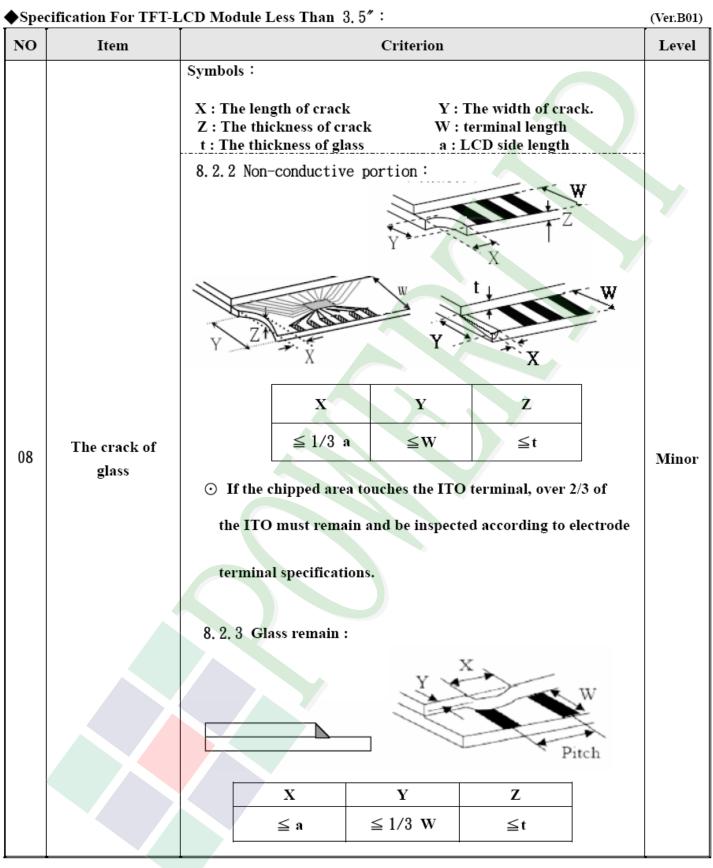


♦Spec	ification For TFT-LCD N	Iodule Less Than 3.5″ :		(Ver.B01)
NO	Item	Criterion	Level	
		Z : The thickness of crack	Y : The width of crack. W : terminal length a : LCD side length	
		8.1 General glass chip: 8.1.1 Chip on panel surface and cra	ack between panels:	
		Y Z Z	Z X X X	
08	The crack of glass	SP Y [OK]	ING]	Minor
		Seal width	Y	
		X Y	Z	
		≤ a Crack can't enter viewing area	$\leq 1/2 t$	
		≤ a Crack can't exceed the half of SP width.	$1/2 t < Z \leq 2 t$	



♦Spec	cification For TFT-LCD	Module Less Than 3.5":	Ver.B01)				
NO	Item	Criterion	Level				
		Symbols :X : The length of crackY : The width of crack.Z : The thickness of crackW : terminal lengtht : The thickness of glassa : LCD side length8. 1. 2 Corner crack :					
8. 1. 2 Corner Crack .							
		X Y Z					
		$ \leq 1/5 \text{ a} \qquad \begin{array}{c} \text{Crack can't enter} \\ \text{viewing area} \end{array} \qquad \textbf{Z}  \leq 1/2 \text{ t} \end{aligned} $					
		$\leq 1/5 \text{ a}  \begin{array}{c} \text{Crack can't exceed the} \\ \text{half of SP width.} \end{array}  1/2 \text{ t} < \text{Z}  \leq 2 \text{ t} \end{array}$					
08	The crack of glass		Minor				
		8.2 Protrusion over terminal:					
		8.2.1 Chip on electrode pad:					
		v Y I-					
		WYY					
		W					
		A CARACTER AND A CARACTER ANTE ANTE ANOCTER ANTE ANOCTER ANTE ANTE ANOCTER ANTE ANOCTER ANTE ANTE ANTE ANTE ANTE ANTE ANTE ANTE					
		AILFront $\leq a$ $\leq 1/2 W$ $\leq t$					
		Profit $\leq a$ $\leq 1/2 t$ Back $\leq a$ $\leq W$ $\leq 1/2 t$					







#### ◆Specification For TFT-LCD Module Less Than 3.5″:

♦Spec	ification For TFT-	LCD Module Less Than 3.5″ :	(Ver.B01)
NO	Item	Criterion	Level
		9. 1 Backlight can't work normally.	Major
09	Backlight elements	9. 2 Backlight doesn't light or color is wrong.	Major
		9. 3 Illumination source flickers when lit.	Major
		10. 1 Pin type 、 quantity 、 dimension must match type in structure diagram.	Major
		10. 2 No short circuits in components on PCB or FPC .	Major
10	General	10.3 Parts on PCB or FPC must be the same as on the production characteristic chart .There should be no wrong parts , missing parts or excess parts.	Major
10	арреагапсе	10. 4 Product packaging must the same as specified on packaging specification sheet.	Minor
		10. 5 The folding and peeled off in polarizer are not acceptable.	Minor
		10. 6 The PCB or FPC between B/L assembled distance(PCB or FPC ) is ≤1.5 mm.	Minor



# **4. RELIABILITY TEST**

#### 4.1 Reliability Test Condition

(Ver.B01)

4.1							
NO.	TEST ITEM	TEST CO	ONDITION				
1	High Temperature Storage Test	Keep in $+80^{\circ}$ $\pm 2^{\circ}$ 240hrs Surrounding temperature, then storage at normal condition 4hrs.					
2	Low Temperature Storage Test	Keep in −30°C ±2°C 240hrs Surrounding temperature, then st	orage at normal condition 4hrs.				
3	High Temperature / High Humidity Storage Test	Keep in +60 °C / 90% R.H duration for 240hrs Surrounding temperature, then storage at normal condition 4hrs. (Excluding the polarizer)					
4	Temperature Cycling Storage Test	$-30^{\circ}C \rightarrow +25^{\circ}C \rightarrow +80^{\circ}C \rightarrow +25^{\circ}C$ $(30 \text{mins})  (5 \text{mins})  (30 \text{mins})  (5 \text{mins})$ $10 \text{ Cycle}$ Surrounding temperature, then storage at normal condition 4hrs.					
5	ESD Test	Air Discharge: Apply 2 KV with 5 times Discharge for each polarity +/- 1. Temperature ambiance : 15°C - 2. Humidity relative : 30% ~60% 3. Energy Storage Capacitance(C 4. Discharge Resistance(Rd) : 330 5. Discharge, mode of operation :	Contact Discharge: Apply 250 V with 5 times discharge for each polarity +/- ~35°C s+Cd) : 150pF±10% Ω±10% uccessive discharges at least 1 sec)				
6	Vibration Test (Packaged)	<ol> <li>Sine wave 10~55 Hz frequency (1 min/sweep)</li> <li>The amplitude of vibration :1.5 mm</li> <li>Each direction (X \ Y \ Z) duration for 2 Hrs</li> </ol>					
7	Drop Test (Packaged)	Packing Weight (Kg           0 ~ 45.4           45.4 ~ 90.8           90.8 ~ 454           Over 454	122       76       61       46				
		Drop Direction : <b>%1</b> corner / 3 edg	es / 6 sides each 1time				



# **5. PRECAUTION RELATING PRODUCT HANDLING**

#### 5.1 SAFETY

- 5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes , please wash it off immediately by using soap and water.

#### 5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module , be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So , please handle it very carefully, do not touch , push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth , as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands , this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is  $320 \pm 10^{\circ}$ C and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM
- 5.2.10 Caution!( LCM products with Capacitive Touch Panel)

Strong EMI-sources such as switch-mode power supplies (SMPS) can lead to touch malfunction (e.g. ghost-touches).

Therefore, the touch needs to be thoroughly tested inside the target application.

#### 5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is  $25^{\circ}C \pm 5^{\circ}C$  and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush , shake , or jolt the module.

#### 5.4 TERMS OF WARRANTY

- 5.4.1 Applicable warrant period The period is within thirteen months since the date of shipping out under normal using and storage conditions.
- 5.4.2 Unaccepted responsibility This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.

REV	001	003	004	006	Z ZO							
	zlo	_			TE: 1.DRIVING 2.THE TOL 3.DRIVER 4.a:PO.8X: 5.Top: -2		9.7±0.5-		-36.9±0.2(0 32.24(L 27.49( 26.49(	CD) 6(V.A)	2.352±0.3 2.852±0.3	A
REV BY	NEW DRAWING				4G ELEMENT : 65K COLOR a-Si TFT OLERANCE UNLESS CLASSIFIED±0.2mm R IC:ST7735S 3X21=16.8±0.1 -20~70°C Tst:-30~80°C	2.2±0.3-			VIEWING	128x3(R,G,B)x128		B
REVISER	Air Air	>			TFT							С
DATE	2018/03/01	2010/02/02				Č				PULL TAPE	±0.3 -	
			DRAWING NAME :	PH128128T041-LAA07		0.13±0.05			0.3			D
VING Approve		I-LAAU7 Check	1 I A AOZ	707		1.9±0.3				-2-Double Tape(T=0.1)		m
Ryan	iony	Terrv	Air	久止光 POWERTIP T					2	•(T = 0.1 	1.1ΰ	
Page	Scale	Unit	•	電股 衍 TECHNOLOGY					35.9-		0.5	
1/1	3:5	MM		LOGY					AIIIIIII			
Quantity	Thickness	Material	Surface	有限公司 CORPORATION					-			G
63 ~ 250 250 ~ 1000	+ - 10 16 ~ 63	1 ~ 4		ATION					22			T
$\vdash$	1 1	11	精級					D	71111111		1	1

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