OriginGPS
mini+mighty

ORG4572-AG05

GPS / GNSS MODULE

Datasheet

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ABBREVIATIONS

Abbreviation	Description
A-GPS	Assisted GPS
AC	Alternating Current
ADC	Analog to Digital Converter
AGC	Automatic Gain Control
BPF	Band Pass Filter
C/N_0	Carrier-to-Noise density ratio [dB-Hz]
CDM	Charged Device Model
CE	European Community conformity mark
CEP	Circular Error Probability
CMOS	Complementary Metal-Oxide Semiconductor
CPU	Central Processing Unit
CTS	Clear-to-Send
CW	Continuous Wave
DC	Direct Current
DOP	Dilution of Precision
DR	Dead Reckoning
DSP	Digital Signal Processor
ECEF	Earth-Centered Earth-Fixed
ECHA	European Chemical Agency
EGNOS	European Geostationary Navigation Overlay Service
EIA	Electronic Industries Alliance
EMC	Electro-Magnetic Compatibility
EMI	Electro-Magnetic Interference
ENIG	Electroless Nickel Immersion Gold
ESD	Electro-Static Discharge
ESR	Equivalent Series Resistance
EU	European Union
EVB	Evaluation Board

Abbreviation	Description
EVK	Evaluation Kit
FCC	Federal Communications Commission
FSM	Finite State Machine
GAGAN	GPS Aided Geo-Augmented Navigation
GNSS	Global Navigation Satellite System
GPIO	General Purpose Input or Output
GPS	Global Positioning System
HBM	Human Body Model
HDOP	Horizontal Dilution of Precision
I^2C	Inter-Integrated Circuit
I/O	Input or Output
IC	Integrated Circuit
ICD	Interface Control Document
IF	Intermediate Frequency
ISO	International Organization for Standardization
JEDEC	Joint Electron Device Engineering Council
KA	Keep Alive
KF	Kalman Filter
LDO	Low Dropout regulator
LGA	Land Grid Array
LNA	Low Noise Amplifier
LP	Low Power
LS	Least Squares
LSB	Least Significant Bit
MID	Message Identifier
MM	Machine Model
MSAS	Multi-functional Satellite Augmentation System
MSB	Most Significant Bit
MSL	Moisture Sensitivity Level
NFZ™	Noise-Free Zones System

Abbreviation	Description
NMEA	National Marine Electronics Association
NVM	Non-Volatile Memory
PCB	Printed Circuit Board
PLL	Phase Lock Loop
PMU	Power Management Unit
POR	Power-On Reset
PPS	Pulse per Second
PRN	Pseudo-Random Noise
PSRR	Power Supply Rejection Ratio
PTF™	Push-to-Fix
QZSS	Quasi-Zenith Satellite System
RAM	Random Access Memory
REACH	Registration, Evaluation, Authorization, and Restriction of Chemical substances
RF	Radio Frequency
RHCP	Right-Hand Circular Polarized
RMS	Root Mean Square
RoHS	Restriction of Hazardous Substances directive
ROM	Read-Only Memory
RTC	Real-Time Clock
RTS	Ready-to-Send
SAW	Surface Acoustic Wave
SBAS	Satellite-Based Augmentation Systems
SID	Sub-Identifier
SIP	System in Package
SMD	Surface-Mounted Device
SMPS	Switched-Mode Power Supply
SMT	Surface-Mount Technology
SOC	System-On Chip
SPI	Serial Peripheral Interface
SV	Satellite Vehicle

Abbreviation	Description
TCXO	Temperature-Compensated Crystal Oscillator
TTFF	Time to First Fix
TTL	Transistor-Transistor Logic
UART	Universal Asynchronous Receiver/Transmitter
VCCI	Voluntary Control Council for Interference by information technology equipment
VEP	Vertical-Error Probability
VGA	Variable-Gain Amplifier
WAAS	Wide Area Augmentation System

Related Documents

	Description
1	ORG4572-AG EVK Datasheet
2	AG-L1-GNSS SW Manual

Revision History

Ver. #	Description	Author/s	Date
1.0	First release	Mark Rimer, Igor Mindel	August 7, 2023

SCOPE

This document describes the features and specifications of the ORG4572-AG05 GNSS module.

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SAFETY INFORMATION

Improper handling and use can cause permanent damage to the product.

ESD SENSITIVITY

This product is ESD sensitive device and must be handled with care.

CONTACT INFORMATION

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1. About Spider Modules

OriginGPS GNSS receiver modules prioritize size, weight, standalone operation, integration, power consumption, and design flexibility—making them ideal for various markets. The Spider family stands out with its industry-leading small size, full integration, and exceptional sensitivity. Equipped with OriginGPS' NFZ™ technology, these modules maintain high sensitivity and noise immunity even in challenging signal conditions, such as urban canyons, dense foliage, or rapid changes in the receiver's position. Achieve the shortest TTM and minimal design risks by simply connecting an antenna and power supply on a 2-layer PCB.

2. About the ORG4572-AG05 Module

The ORG4572-AG module is a compact SiP with an LGA SMT footprint, offering unique integration features for high volume, cost-sensitive applications. Its versatility suits smartwatches, wearable devices, and asset trackers, functioning as a miniature multi-channel receiver for GPS, Galileo, GLONASS, BeiDou, and SBAS, QZSS. It continuously tracks all visible satellites, delivering real-time positioning data in standard NMEA format.

Remarkably small at 7mm x 7mm, the ORG4572-AG module stands as the industry's tiniest solution. It boasts the lowest energy-per-fix ratio, exceptional accuracy, and rapid fixes, even in challenging signal conditions like built-up urban areas, dense foliage, or indoor environments.

Embedded with an integrated GNSS SoC, this module houses a high-performance microprocessor and advanced firmware. This architecture efficiently manages positioning payload, allowing seamless integration in low-computing-resource embedded solutions.

Notably innovative, the module can detect context, temperature changes, and satellite signals, maintaining near-continuous availability. It achieves this by opportunistically updating its internal fine time, frequency, and satellite ephemeris data while consuming mere microwatts of battery power.

3. ABOUT ORIGINGPS

OriginGPS is a renowned global leader in the design, manufacturing, and supply of miniature positioning modules, and cellular IoT systems and products. Our expertise lies in creating fully integrated, compact GPS/GNSS and IoT solutions to empower developers and facilitate their product development.

At the core of OriginGPS modules is our groundbreaking Noise-Free-Zone system (NFZ™) proprietary technology, which sets new benchmarks for sensitivity and noise immunity. This enables faster position fixing and ensures stable navigation even in challenging satellite signal conditions.

Established in 2006, OriginGPS has been at the forefront of developing cutting-edge technologies that miniaturize RF modules, catering to the increasing demand for smaller wireless solutions in the market. With over a decade of experience, our team of experts has been dedicated to producing ultra-sensitive, dependable, and high-performance modules with the smallest footprint available.

Our versatile range of products supports various sectors, including asset tracking, fleet management, industrial IoT, law enforcement, pet and people tracking, precise agriculture, smart cities, sports, and wearables. OriginGPS remains committed to innovation and delivering top-notch solutions for all our customers' positioning and IoT needs.

4. MODULE DESCRIPTION

The following section provides comprehensive details on the ORG4572-AG05, and includes the module's highlights, features and integrated parts, and provides a presentation of its architecture.

4.1. Module Highlights

The following section provides comprehensive details on the ORG4572-AG05, and includes the module's highlights, features and integrated parts, and provides a presentation of its architecture.

- Autonomous operation
- OriginGPS Noise Free Zone System technology
- Active or passive antenna support
- Fully integrated: dual-stage LNA, SAW filter, TCXO, RTC crystal, GNSS SoC, RF shield, and PMU.
- Concurrent tracking of multiple constellations: GPS, Galileo and GLONASS, BeiDou, and QZSS.
    - Supports GPS & Galileo L1 1575.42 frequency, C/A code.
    - Supports GLONASS G1 FDMA 1598-1606MHz frequency band, SP signal.
    - Supports BEIDOU B1I (1561.098MHz) and B1C (1575.42MHz) frequency bands.
    - DGPS capability supports SBAS (WAAS, EGNOS, MSAS and GAGAN).
- Sensitivity down to -167dBm, enabling indoor tracking.
- TTFF of < 1s in 50% of trials under hot start conditions
- Low power consumption of ≤ 54mW
- Accuracy of 2@CEP
- Accuracy of 1.7m@CEP in open sky conditions over 24 hours
- AGPS support: Embedded Assist System (EASY) and Extended Prediction Orbit (EPO) and Hot Still
- Indoor and outdoor multipath and cross-correlation mitigation
- Jamming Rejection – 12 multi-tone Active Interference Cancellation (AIC)
- 2MB built-in flash
- Power management modes: ALP, SW RTC, BackUp, and HW RTC
- NMEA, RTCM, and raw data with PAIR commands over UART and I2C
- Update message rate of 1-10Hz
- 1PPS output
- Voltage supply of 1.8V and backup input
- LGA footprint of 7mm x 7mm
- Weight of 0.2g

- Surface Mount Device (SMD)
- Optimized for automatic assembly and reflow equipment.
- Operating from -40°C to +85°C
- FCC, CE, VCCI, RoHS II/REACH compliant

4.2. Architecture

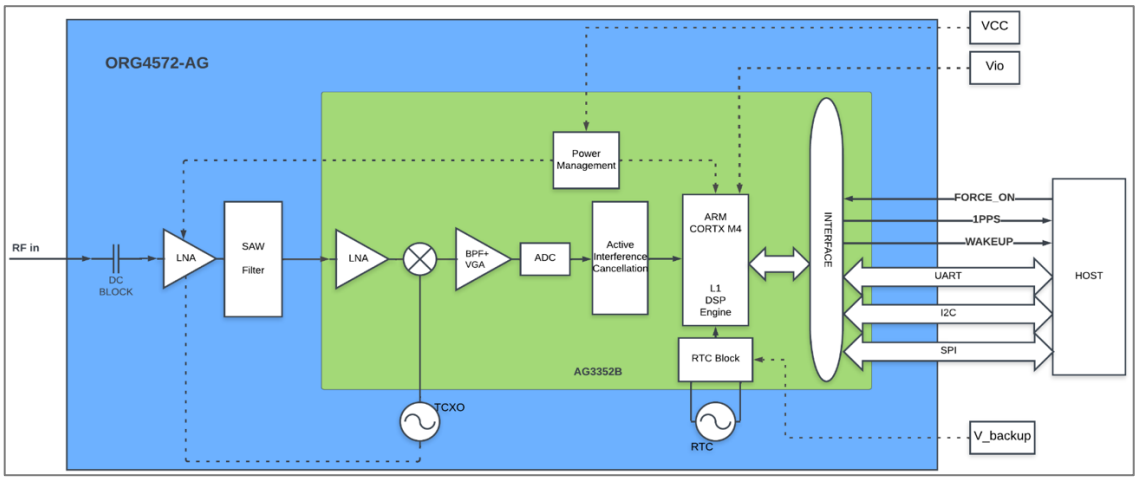

Figure 1. ORG4572-AG05 Architecture

The ORG4572-AG05 module includes the following main components:

- **GNSS SAW Filter**

  The band-pass SAW filter eliminates out-of-band signals that may interfere with GNSS reception. The GNSS SAW filter is optimized for low Insertion Loss in the GNSS band and low Return Loss outside of it.

- **GNSS LNA**

  The dual stage cascaded LNAs amplify GNSS signals to meet RF down converter input threshold.

  The noise figure (NF) optimized design provides maximum sensitivity.

- **TCXO**

  The 26MHz oscillator serves as the clock source for the down conversion process in the RF block. Its stability is of paramount importance because it directly affects the performance of the GNSS module in several ways; shortening the TTFF and improving the navigation stability.

- **RTC Crystal**

  The RTC (Real-Time Clock) in the GNSS SoC plays a crucial role in maintaining the Hot Start and Warm Start capabilities of the module. To achieve these capabilities, a high-precision 32.768 kHz quartz crystal is utilized as the timekeeping reference for the RTC. This crystal has very tight specifications, which means it exhibits highly accurate and stable frequency characteristics.

- **RF Shield**

  The RF enclosure serves two critical purposes in the operation of the GNSS module: protection from external interference and containment of internal emissions. The RF enclosure prevents external RF signals from entering the module and interfering with the GNSS receiver's operation.

- **Flash**

  The Flash has a capacity of 2MB (megabytes). During boot-up, the GNSS SoC reads the firmware from the Flash memory and loads it into its internal processing units. The firmware stored in the built-in Flash enables the GNSS module to operate independently. This is particularly useful in applications where the GNSS module must function in remote or resource-constrained environments.

- **AG3352B GNSS SoC**

  The AG3352B, AIROHA's multi-GNSS System on Chip (SoC), combines signals from GPS, Galileo, GLONASS, BeiDou, and QZSS in a hybrid positioning processor, delivering high-performance navigation.

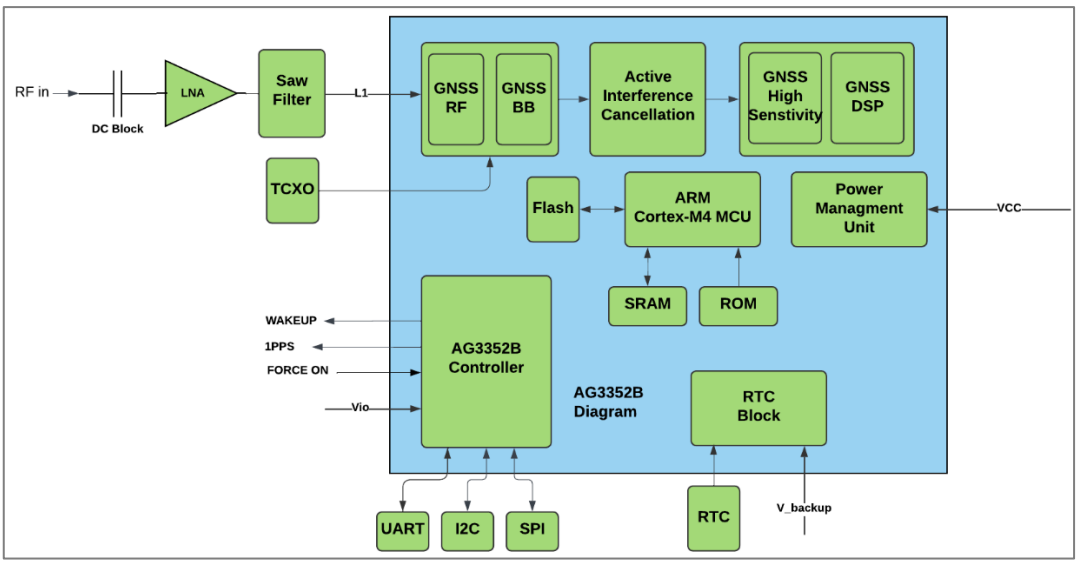

Figure 2. AG3352B System Block Diagram and Peripheral Components

The AIROHA AG3352B is a feature-rich multi-GNSS SoC and includes the following units:

- **GNSS radio subsystem** enables concurrent multi-channel reception (GPS, Galileo, GLONASS, BeiDou, SBAS, QZSS) in L1/B1/E1, with mixer, current mode interface, fractional-N synthesizer, self-calibrating filters, IF VGA with AGC, and high-sample rate ADCs with adaptive dynamic range.
- **Measurement subsystem** includes DSP core for GNSS signal acquisition and tracking, interference scanner and detector, removers, multipath mitigation, dedicated DSP code ROM, and DSP cache SRAM, interfacing seamlessly with the GNSS radio subsystem.
- **Navigation subsystem** integrates an ARM Cortex-M4 microprocessor system for precise position, velocity, and time solutions, along with program ROM, data SRAM, and flash memory.
- **Peripheral Controller subsystem (a)** facilitates UART Host interface, I2C, RTC block, and wake-up signal option for efficient communication.
- **Peripheral Controller subsystem (b)** interfaces with navigation, PLL, and PMU subsystems, ensuring smooth data flow and control.
- **Navigation subsystem** efficiently communicates with the measurement subsystem, enhancing overall navigation performance.
- **PMU subsystem** features voltage regulators for RF and baseband domains, optimizing power management and performance.

4.3. ORG4572-AG05 Feature Description

The ORG4572-AG05 is a feature-rich module, encompassing an array of advanced capabilities, including:

4.3.1. Constellation Configuration

The ORG4572-AG05 module supports GPS, Galileo GLONASS, BeiDou.

4.3.2. 1PPS

1PPS (Pulse per Second) signal output may be configured at the following points:

- At 2D Fix only
- At 3D Fix only
- After the first Fix
- Always on (Default)

The pulse is configurable for required duration, frequency, and active high/low via command. The pulse may vary 10nS (1σ). The relationship between the PPS signal and UTC is unspecified.

4.3.3. Assisted GPS (AGPS)

Assisted GPS (A-GPS) is a technique that reduces Time To First Fix (TTFF) by using data from sources other than broadcast GPS signals. The receiver can calculate necessary ephemeris data locally (locally generated ephemeris) or obtain it from a server (server-generated ephemeris), storing it in the module. The ORG4572-AG05 incorporates EPO and HotStill technologies, enabling Hot Starts even in weak signal conditions and while on the move. EPO (Extended Prediction Orbit) is Airoha's proprietary off-line server based AGPS solution. By utilizing an application to store and load EPO files into the device, multi-constellation EPO enhances user experience with improved TTFF and better first fix accuracy.

4.3.4. Differential GNSS (DGNSS)

Differential GNSS applications leverage data from GNSS augmented systems or ground station networks to enhance the performance of primary GNSS constellations. By collecting correction information from the broadcast navigation messages of augmented systems or ground stations, the receiver integrates this data using estimation methodology to improve the accuracy of position-related information. The ORG4572-AG05 module fully supports differential GNSS applications, including Satellite Based Augmentation System (SBAS), Radio Technical Commission for Maritime Services (RTCM), and Sub-meter Level Augmentation Service (SLAS). The following sections provide detailed descriptions of the DGNSS (differential GNSS) applications implemented by the module.

4.3.4.1. QZSS (Quasi-Zenith Satellite System0

The Japanese SBAS (Satellite-Based Augmentation System) is comprised of three satellites positioned in a highly inclined elliptical orbit that is geosynchronous (not geostationary). These satellites follow analemma-like ground tracks, allowing them to provide continuous coverage over Japan with just three satellites. The main function of the Japanese SBAS is to augment the GPS system, enhancing its accuracy and reliability. However, the signals from these satellites can also be utilized for ranging purposes. Users have the flexibility to enable or disable NMEA reporting for QZSS (Quasi-Zenith Satellite System) as per their requirements.

4.3.4.2. Satellite-Based Augmentation System (SBAS)

The ORG4572-AG05 module can effectively utilize Satellite-Based Augmentation System (SBAS) satellites for two purposes: obtaining differential corrections and satellite range measurements. Systems such as WAAS, EGNOS, MSAS, and GAGAN employ geostationary satellites to transmit regional differential corrections via GNSS-compatible signals. By integrating SBAS corrections, the module can greatly enhance position accuracy by compensating for significant error sources, such as the ionospheric delay and satellite time/clock errors. This enhancement ensures more precise and reliable GNSS-based positioning information for various applications.

4.3.5. Power Management Modes

The ORG4572-AG05 module offers different operational modes to cater to various requirements, allowing it to provide positioning information while minimizing overall current consumption. The choice of power management modes considers the availability of GNSS signals in the module's operating environment, allowing the designer to strike the right balance between performance and power consumption.

The power management modes available are described below and can be enabled using specific commands:

Full Power Continuous: This mode ensures optimal GNSS performance, providing accurate positioning information without compromising on the power consumption. It is suitable for scenarios where precise positioning is of utmost importance, and power efficiency is not a primary concern.

Power Save Mode (range of options): The module offers a range of power-saving modes designed to optimize power consumption while still providing positioning information. These modes are ideal for situations where power efficiency is crucial, and the level of positioning accuracy can be adjusted based on the specific application's needs.

By offering a variety of power management modes, the ORG4572-AG05 module enables designers to find the best trade-off between performance and power consumption.

4.3.5.1. Full Power Continuous Mode

The ORG4572-AG05 module initiates its operation in the full power continuous mode by default, as the FORCE_ON pin is internally set to HIGH. In this mode, the acquisition engine operates at its maximum performance, leading to the shortest Time To First Fix (TTFF) and highest sensitivity. The module actively searches for signals from all available satellites during this phase.

Upon meeting the following conditions, the module transitions from the acquisition engine to the tracking engine, which results in reduced power consumption:

Valid GPS/GNSS position obtained: Once the module successfully determines a valid position using the acquired satellite signals, it switches to the tracking engine to maintain the accuracy of the position information.

Valid ephemeris for each satellite in view: Ephemeris data contains essential information about the satellite's orbital parameters. When the module receives valid ephemeris data for all the satellites in view, it shifts to the tracking engine to optimize power consumption while continuously tracking the satellites' movements for consistent positioning updates.

By intelligently switching to the tracking engine when necessary, the module achieves a balance between performance and power consumption, ensuring accurate positioning while efficiently managing power resources.

4.3.5.2. RTC Mode

The RTC (Real-Time Clock) mode is an additional power-saving feature of the ORG4572-AG05 module. In this mode, all systems, including the GNSS engine and internal processing units, are shut down, except for a dedicated low-power RTC block. The GNSS engine no longer provides position-related information, and PAIR commands cannot be sent during this mode.

The navigation data, which includes critical information like ephemeris, almanac, location, and time, used to facilitate TTFF performance, is saved to RTCRAM for future use upon exiting RTC mode.

There are three ways to enter RTC mode:

1. Running a PAIR command (SW RTC Mode): A specific PAIR command, which can be time-specific, triggers the module to enter RTC mode.
2. Disconnecting the module from the main power while keeping "Vbackup" at 1.8V. This method allows the module to enter RTC mode while maintaining a low-power state.
3. Setting the FORCE_ON pin to LOW state (HW RTC Mode): Configuring the FORCE_ON pin in this way causes the module to enter RTC mode.

To wake the module from RTC mode, there are two options:

1. Setting the RTC timer by a PAIR650 command: The RTC timer can be set to wake the module from RTC mode at a specific time.
2. Pulling the FORCE_ON pin HIGH: Changing the state of the FORCE_ON pin to HIGH will wake the module from RTC mode.

Upon exiting RTC mode, all system resources are re-initialized, allowing the module to resume normal operation. The RTC mode provides an effective means to save power when continuous GNSS operation is not required, ensuring efficient power management in various scenarios.

(*)- Note that Vbackup must be connected to a power source at all times to ensure the module's functionality.

4.3.5.3. Periodic Mode

This mode enables autonomous power on/off with reduced fix rate to reduce average power consumption. In periodic mode, the main power supply Vcc is still powered, but power distribution to internal circuits is controlled by the receiver. The periodic mode consists of the running and sleeping stages. In the running stage, the GNSS module provides the position-related information while staying in the lower power consumption status in the sleeping stage.

The periodic mode in the ORG4572-AG05 module enables autonomous power on/off with a reduced fix rate, effectively reducing average power consumption. In this mode, the main power supply Vcc remains powered, but power distribution to internal circuits is controlled by the receiver.

The periodic mode operates in two stages:

1. Running Stage: During the running stage, the GNSS module provides position-related information. However, it does so while maintaining a lower power consumption status, optimizing energy usage.
2. Sleeping Stage: In the sleeping stage, the GNSS module goes into a low-power state, conserving energy and reducing overall power consumption. During this period, the module is not actively providing position-related information.

By cycling between the running and sleeping stages, the module achieves a balance between periodically providing position information and conserving power during intervals when positioning updates are not required frequently. This approach ensures efficient power management, making it suitable for applications where lower average power consumption is desired without compromising essential positioning functionality.

Figure 3. Periodic Mode - Power Consumption

The time span of the running stage can be changed dynamically and also strictly limited. To improve the TTFF performance after waking up from the sleeping stage, the module will change the time span of the running stage dynamically to obtain more navigation information.

In the periodic mode, the time span of the running stage can be dynamically adjusted and strictly limited. This flexibility allows the module to optimize the Time To First Fix performance after waking up from the sleeping stage.

Upon waking up from the sleeping stage, the module adopts a method to dynamically change the time span of the running stage. By extending the duration of the running stage, the module can gather more navigation information, this additional information helps improve the TTFF performance as the module has a better chance of acquiring and processing sufficient data to determine an accurate position fix in a shorter time.

To activate the periodic mode, issue the following command:

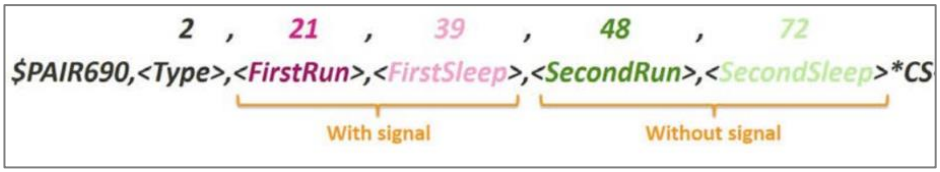

Figure 4. Periodic Mode - Command Structure

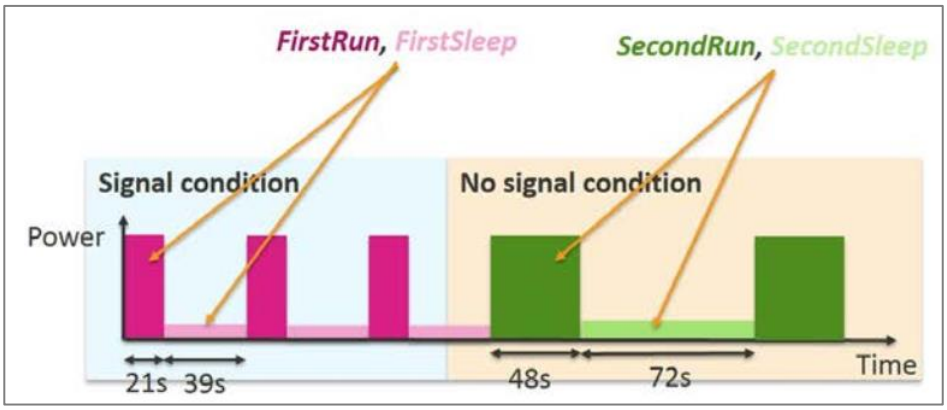

Figure 5. Periodic Mode - Time Parameters

To exit the periodic mode and return to Full Power Continuous Mode, send the Pair690,0-disable command immediately after the module wakes up from a sleep cycle. To return the module to its regular operating mode, a restart can be performed.

4.3.5.4. ALP Mode

The Adaptive Low Power mode (ALP) utilizes duty cycles to conserve power but may have an impact on GNSS performance. The receiver provides positioning solutions at each epoch during ALP operation. This mode is limited to the "Normal" and "Fitness" navigation modes and is not supported at high fixing rates (greater than 1 Hz). Additionally, certain module features, such as SBAS, SLAS, low power periodic mode, and GLP mode, will be automatically disabled while ALP is active.

4.3.6. Configuration Settings

When the power is turned off, the configuration settings in the ORG4572-AG05 module are erased and reset to their default values. However, to retain the desired configuration settings for future navigation sessions, users can save them to the internal flash memory using the PAIR513 command. By using this command, the configuration settings will be stored in the internal flash, ensuring that they are preserved and loaded for the next navigation session, even after a power cycle or system restart. Detailed instructions on how to use the PAIR513 command can be found in the *AG-L1-GNSS SW Manual*".

4.4. Pad Assignment

Table 1. Pin-Out

Pad	Name	Function	Direction
1	GND	System ground	Power
2	RF IN	RF input	Input
3	GND	System ground	Power
4	WAKEUP	Power state signal	Output
5	MISO	SPI slave output	Output
6	Vio	IO level voltage	Output
7	SCK	SPI clock	Input
8	SCS	SPI chip select	Input
9	RX	UART receive (serial Input)	Input
10	MOSI	SPI slave input	Input
11	FORCE ON	power mode signal	Input
12	1PPS	UTC Time Mark	Output
13	TX	UART transmit (serial output)	Output
14	Vcc	System power	Power
15	Vbackup	Input for battery backup	Power
16	TX2	UART2 transmit (serial output)	Output
17	GND	System ground	Power
18	I2C - SCL	I2C interface - clock	Bi-directional
19	I2C - SDA	I2C interface - data	Bi-directional
20	RX2	UART2 receive (serial input)	Input

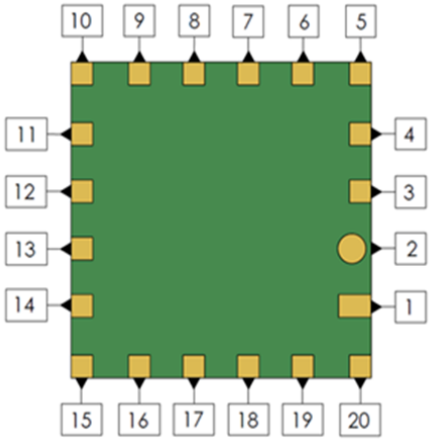

Figure 6. ORG4572-AG05 Top View

5. MECHANICAL SPECIFICATIONS

The ORG4572-AG05 module features the following mechanical specifications:

- Packaging: The module comes in a miniature LGA (Land Grid Array) SMD (Surface Mount Device) package, measuring 10mm x 10mm in size.
- Enclosure: The module is built on a PCB (Printed Circuit Board) assembly enclosed within a metallic RF shield box. The Patch antenna element is positioned on top of this shield box.
- SMT Pads: On the bottom side of the module, there are 10 SMT (Surface Mount Technology) pads with a base and ENIG (Electroless Nickel Immersion Gold) plating. These pads facilitate the connection and mounting of the module on the PCB.
- Assembly Compatibility: The ORG4572-AG05 module supports automated pick and place assembly, enabling efficient mass production processes. It is also compatible with reflow soldering processes, ensuring reliable and consistent solder connections during production.

  These mechanical specifications make the ORG4572-AG05 module suitable for integration into various electronic devices and systems, providing reliable and precise GNSS positioning capabilities.

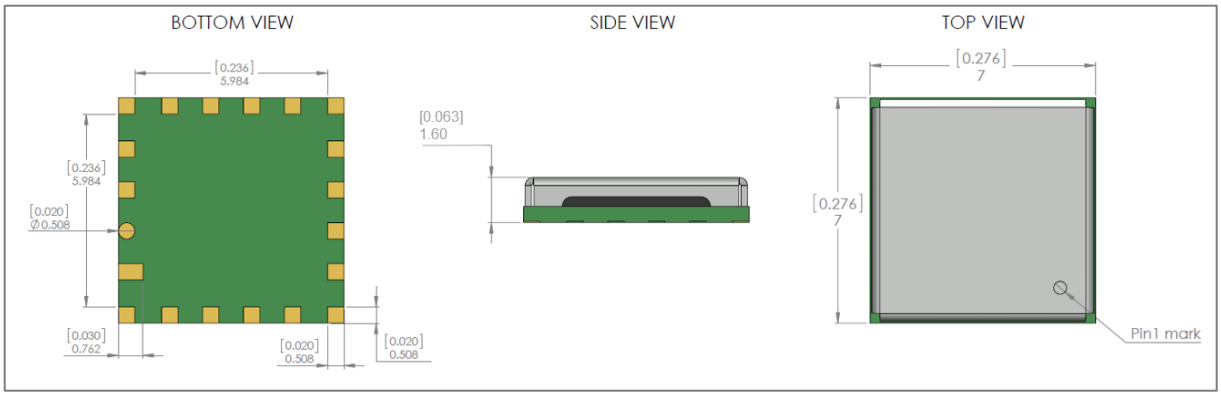

Figure 7. Mechanical Drawing

Table 2. Mechanical Summary

Dimensions	Length	Width	Height	Weight	
mm	7.0 + 0.2 / -0.05	7.0 + 0.2 / -0.05	1.6 + 0.2 / -0.05	gr	0.2
inch	0.276 + 0.008 / -0.002	0.276 + 0.008 / -0.002	0.063 + 0.008/ -0.02	oz	0.01

6. ELECTRICAL SPECIFICATIONS

6.1. Absolute Maximum Ratings

Stresses exceeding Absolute Maximum Ratings may damage the device.

Table 3. Absolute Maximum Ratings

PARAMETER		SYMBOL	MIN	MAX	UNIT
Power Supply Voltage		V_{CC}	0	1.93	V
Backup Supply Voltage		V_{backup}	0	3.63	V
Power Supply Current[1]		I_{CC}		500	mA
RF Input Voltage[2]		V_{RF}	-0.30	+3.6	V
Digital I/O Voltage		V_{IO}	-0.30	3.63	V
I/O Source/Sink Current		I_{IO}		12	mA
ESD Voltage		$V_{IO/RF}$, HBM[2]	-2000	2000	V
		$V_{IO/RF}$, MM[3]	-500	500	V
RF Power[5]	f_{IN} = 1560MHz÷1630MHz	P_{RF}		+10	dBm
	f_{IN} <1560MHz, >1630MHz			+30	dBm
Operating Temperature		T_{AMB}	-45	+85	°C
Storage Temperature		T_{ST}	-50	+125	°C
Lead Temperature[6]		T_{LEAD}	-5	+260	°C

Notes:

1. Inrush current for ~20µs duration
2. Human Body Model (HBM) contact discharge per EIA/JEDEC JESD22-A114D. Step: 500V (+/-)
3. Machine Model (MM) contact discharge per EIA/JEDEC JESD22-A115C. Step: 50V (+/-)
4. Power delivered to antenna element.
5. Lead temperature at 1mm from case for a 10s duration.

6.2. Recommended Operating Conditions

Exposure to stresses above the Recommended Operating Conditions may affect device reliability.

Table 4. Recommended Operating Conditions

Parameter	Symbol	Mode/Pad	Test CONDITIONS	Min	Typ	Max	Unit
Power supply voltage	Vcc	Vcc		1.735	1.8	1.98	V
Backup Supply Voltage	Vbackup	Vbackup		1.62		3.63	V
Digital IO Pin Low level input	Vil	IO Level 1.8V		-0.3	0	+0.45	V
Digital IO Pin High level input	Vih			+1.35	+1.8	+2.1	V
Digital IO Pin Low level output	Vol				0	0.27	V
Digital IO Pin High level output	Voh			+1.53	1.8		V
Power Supply Current[1]	I_{CC}	Acquisition			32		mA
		Tracking			30		mA
		Acquisition ALP Mode			28		mA
		Tracking ALP Mode			22		mA
		SW RTC	PAIR command		16		µA
		BackUp	Vcc = OFF Vbackup = ON		33		µA
		HW RTC	FORCE_ON = Low level		210		µA
Input Impedance	Z_{IN}	RF Input	f_{IN} = 1575.5MHz		50		Ω
Input Return Loss	R_{LIN}			-7			dB
Input Power Range	P_{IN}		GPS or GLONASS	-167		-110	dBm
Input Frequency Range	f_{IN}			1560		1620	MHz
Operating Temperature	T_{AMB}			-40	+25	+85	°C
Storage Temperature[2]	T_{ST}			-50	+25	+125	°C
Relative Humidity[3]	R_H		T_{AMB}	5		95	%

Notes:

1. Typical values under signal conditions of -130dBm and ambient temperature of +25°C.

   Tested on the EVB with 12x12mm passive antenna.
2. A longer TTFF is expected while operating below -30°C to -40°C.
3. Relative Humidity is within Operating Temperature range.

7. PERFORMANCE

7.1. Acquisition Time

TTFF (Time to First Fix) refers to the duration between the module's power-up and the acquisition of a valid position estimation.

7.1.1. Hot Start

Hot Start occurs when a module undergoes a software reset after a continuous navigation period or when it returns from a short idle period that followed continuous navigation. During a hot start, all essential data, such as position, velocity, time, and satellite ephemeris, are stored in RAM and remain relevant with the specified accuracy and availability.

7.1.2. Signal Reacquisition

Reacquisition is the process that follows a temporary interruption or blocking of GNSS signals. A common example of a reacquisition scenario is driving through a tunnel, where the GPS signals are temporarily lost due to the tunnel's obstruction.

7.1.3. Aided Start

Aided Start is a technique used to decrease TTFF by providing the receiver with valid satellite ephemeris data, which is crucial for precise positioning. This aiding process can be implemented using Extended Prediction Orbit (EPO) data.

7.1.4. Warm Start

Warm Start is a state that typically occurs when a user provides position and time initialization data or when a receiver maintains continuous Real-Time Clock (RTC) operation with an accurate last known position stored in RAM. In the warm start state, the position and time data are present and valid, but the satellite ephemeris data, which is essential for precise satellite tracking, is no longer valid.

7.1.5. Cold Start

Cold Start is a state that occurs when the satellite ephemeris data, as well as the position and time data, are unknown to the receiver. This typically happens during the initial power application or when the receiver has been off for an extended period, and it needs to start the satellite acquisition process from scratch. In a cold start scenario, the receiver has no prior information to rely on, so it needs to search and acquire satellite signals, determine its position, and establish accurate time information, which can take a longer time compared to warm or hot start scenarios.

Table 5. Acquisition Time

Operation[1]	Value	Unit
Hot Start	< 2	s
Aided Start[2]	< 4	s
Warm Start	< 23	s
Cold Start	< 25	s
Signal Reacquisition[3]	< 3	s

Notes:

1. It is static under signal conditions of -130dBm and ambient temperature of +25°C during 24 hours test.
2. Tested on the evaluation board with a 12x12mm passive antenna.
3. Outage duration ≤ 30s for reacquisition.

7.2. Sensitivity

7.2.1. Tracking

Tracking refers to the receiver's ability to maintain valid satellite ephemeris data and lock onto the signals from multiple satellites to calculate a position solution. During tracking, there may be instance when the receiver temporarily stops outputting valid position solutions, especially if there is a loss of signal or interference.

Tracking sensitivity is defined as the minimum GNSS signal power required for tracking.

7.2.2. Reacquisition

Reacquisition occurs after a temporary loss or blocking of GNSS signals. Reacquisition sensitivity refers to the minimum power level of GNSS signals required for the receiver to successfully reacquire and track the satellites after a temporary signal loss.

7.2.3. Navigation

During navigation, the receiver consistently outputs valid positioning information, allowing users to determine their accurate position, velocity, and time.

Navigation sensitivity refers to the minimum GNSS signal power required for the receiver to maintain reliable navigation.

7.2.4. Hot Start

Hot start sensitivity is defined as the minimum GNSS signal power required for a receiver to obtain a valid position solution under hot start conditions.

7.2.5. Aided Start

Aided start sensitivity is defined as the minimum GNSS signal power required for a receiver to obtain a valid position solution following the aiding process.

7.2.6. Cold Start

Cold start sensitivity is defined as the minimum GNSS signal power required for a receiver to obtain a valid position solution under cold start conditions.

The cold start sensitivity, also known as the ephemeris decode threshold, represents the receiver's ability to acquire and decode weak GNSS signals to determine the satellite positions and obtain a valid position solution.

Table 6. Sensitivity

OPERATION	MODE	VALUE	UNIT
Tracking	GNSS	-167	dBm
Acquisition	GNSS	-150	dBm

7.3. Received Signal Strength

Table 7. Received Signal Strength

Parameter	Value	Unit
C/N_0	48	dB-Hz

7.4. Position Accuracy

Table 8. ORG4572-AG05 Position Accuracy

Parameter	CEP (m)
Horizontal Position Accuracy	2

7.5. Dynamic Constraints

Table 9. Dynamic Constraints

Parameter	Metric	Imperial
Velocity	514m/s	1000Knots
Altitude	18288m	60000 ft
Acceleration	4g	

Note: Standard dynamic constraints according to regulatory limitations.

8. CONTROL INTERFACE

8.1. Power Supply

For optimal receiver performance, maintain continuous power supply to keep the RTC block active and retain satellite data in RAM for the fastest TTFF. Removing Vcc resets settings to factory defaults, triggering a cold start on the next power-up, erasing stored data such as ephemeris and time info.

8.1.1. Power Supply Design

Here are some key points to consider for the power supply design for the ORG4572-AG05 module:

1. **Voltage Requirement**: The module requires a regulated power supply providing 1.8V DC.
2. **Tracking and Processing Power Consumption**: During tracking, the processing is less intense compared to acquisition, resulting in lower power consumption.
3. **Filtering**: To manage high alternating current flows on the power input connection, an additional LC filter on the power input may be necessary. This filter helps reduce system noise.
4. **Input Current Rate of Change**: The ORG4572-AG05 module has a high rate of input current change. Therefore, low Equivalent Series Resistance (ESR) bypass capacitors are required to handle this.
5. **Output Capacitors**: Additional output capacitors with a higher ESR can provide input stability damping. The ESR and size of these output capacitors directly impact the output ripple voltage in relation to the selected inductor size. Large, low ESR output capacitors are beneficial for achieving low noise.

8.1.2. Ground

To ensure optimal performance and reduce potential interference, it is imperative to establish a direct connection between the ground pad of the ORG4572-AG05 module and the host PCB ground. This can be achieved either by utilizing the shortest possible trace or employing multiple VIAs.

8.1.3. Vbackup

In the ORG4572-AG05 module, the Vbackup line is connected to the RTC domain. Connect 1.8V external regulated power to Vbackup for faster TTFF in RTC domain. In this way, satellite and navigation data is stored in RAM, in case of a sudden power when in RTC mode.

Optionally, shorten the Vbackup to the Vcc.

8.2. Interfaces

8.2.1. UART- Host Interface

The ORG4572-AG05 module features standard UART ports with the following specifications:

Supported Baud Rates: The module supports various baud rates, including:

- 9600 bps (default)
- 19200 bps
- 38400 bps
- 57600 bps
- 115200 bps
- 230400 bps
- 460800 bps
- 921600 bps
- 3000000 bps

Users can select the desired baud rate based on their application requirements and communication needs. However, they should be aware that hardware flow control is not an option and should use alternative methods.

8.2.1.1. TX

In the ORG4572-AG05 module, the TX serial data line is used for sending GPS data reports. The data sent through the TX line can be in different formats, including NMEA, RTCM, raw data, and potentially other formats.

When the module is not actively transmitting data, the TX data line idles high.

8.2.1.2. RX

The RX (Receive) data line is utilized for receiver control and firmware upgrades. It is versatile, capable of receiving information in various formats like PAIR, RTCM, among others.

8.2.2. I2C – Host Interface

The ORG4572-AG05 module is equipped with a standard I2C host interface, offering the following features:

- I2C slave mode: In this mode, the host initiates the clock and data transmission.
- 7-bit I2C address support: The module supports 7-bit I2C address, allowing for communication and interaction with other I2C devices.

8.2.3. Data Interface

8.2.3.1. 1PPS

Pulse-per-Second (PPS) output provides a pulse signal for timing purposes.

The pulse may be configured for duration, frequency, and phase via a command.

The pulse varies 10ns (1σ). The relationship between the PPS signal and UTC is unspecified.

The proprietary PAIR command enables configuring or disabling this functionality.

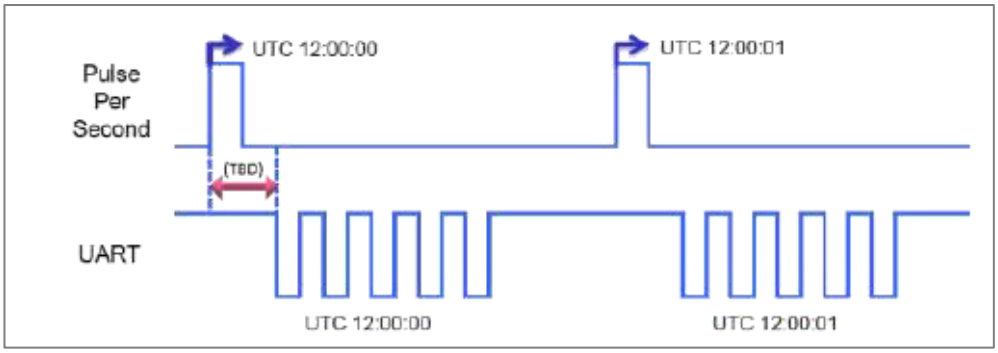

Figure 8. Mechanical Layout

Once a 3D fix is achieved, 1PPS is enabled. To modify the 1PPS settings, refer to the SW manual called “AG-L1-GNSS SW Manual”.

8.2.3.2. Force-On

The FORCE-ON is an input pin which controls the power state of the module. The FORCE-ON line is configured in a high-level state due to an internal 10KΩ pull-up resistor.

There are two possible states for this pin: LOW and HIGH.

Low State: The module enters low power mode (RTC).

High State: The module exits low power mode (RTC).

8.2.3.3. Wakeup

When the ORG4572-AG05 module is on (full power), the output is high. In RTC or periodic mode, the output is low. Wakeup output is only for probing the module’s active/non-active state, with the probe type potentially affecting the high voltage level.

9. TYPICAL APPLICATION CIRCUIT

The following diagram depicts the schematics of the ORG4572-AG05 module.

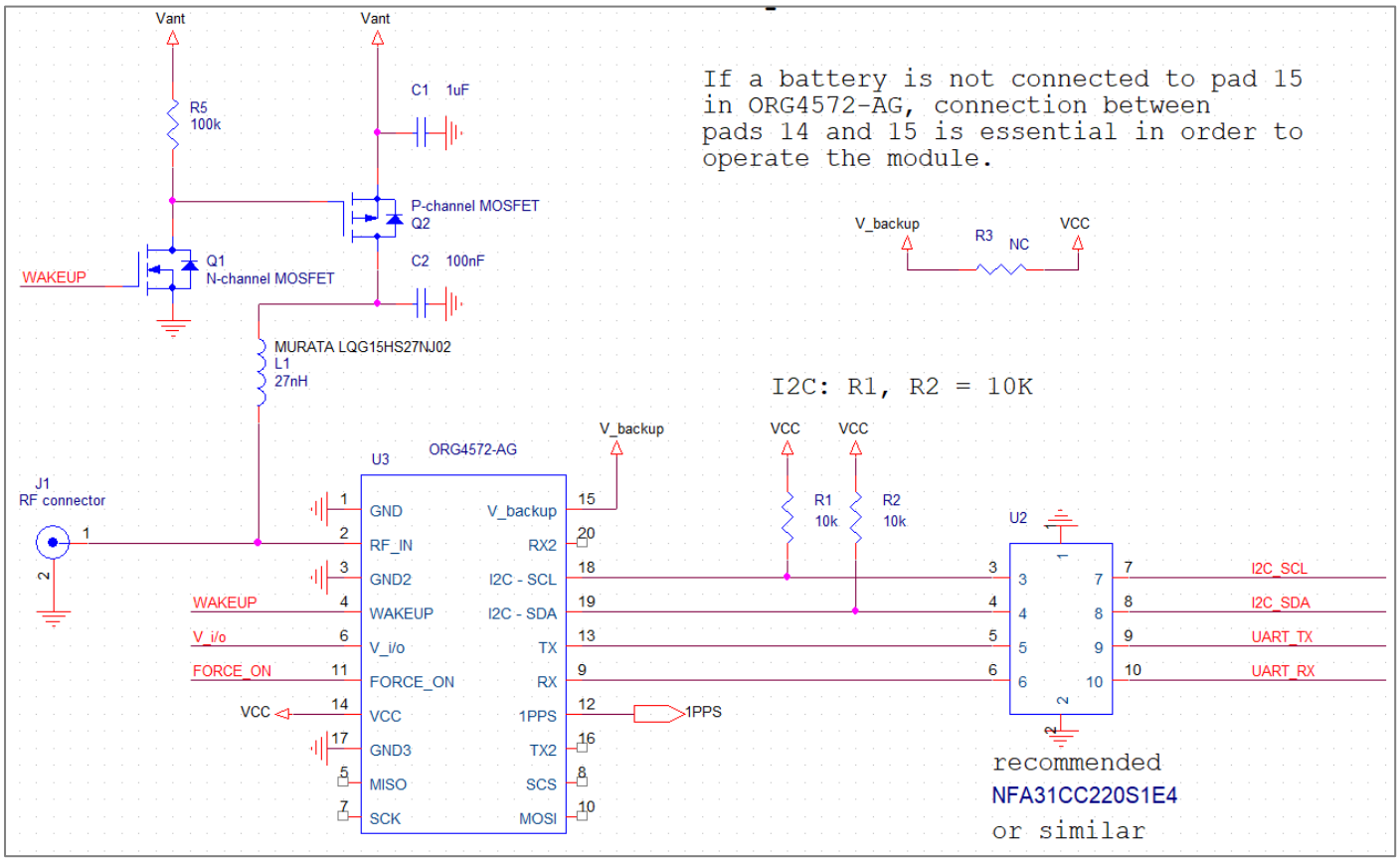

Figure 9. Reference Schematic Diagram

10. RECOMMENDED PCB LAYOUT

10.1. Footprint

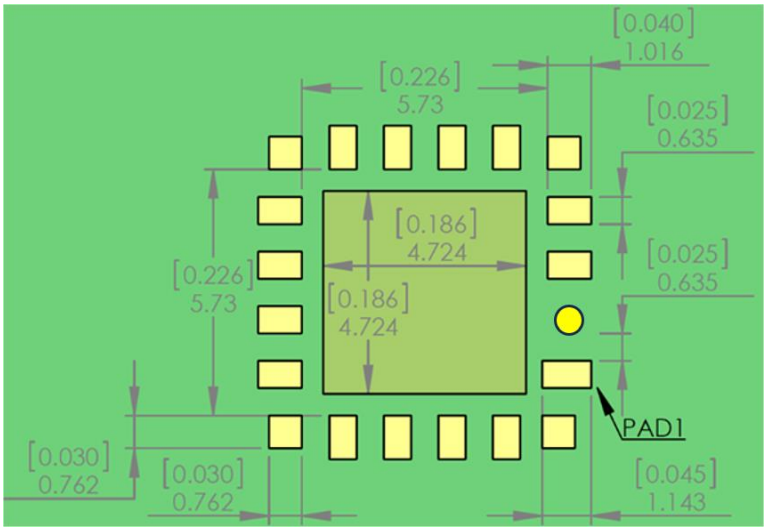

Figure 10. Footprint

Connect the middle ground paddle to the main ground plane using multiple VIAs. Solder-mask the middle ground paddle. The silk print of the module's outline is recommended for SMT visual inspection.

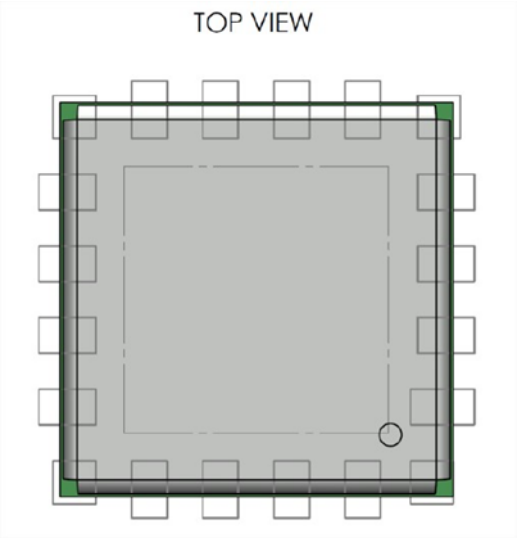

Figure 11. Module Hosted on Footprint

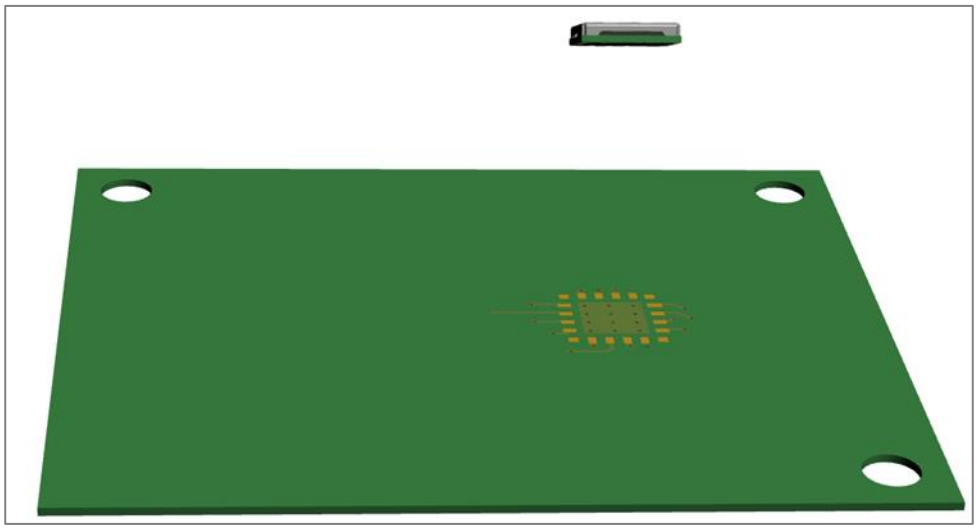

Figure 12. Host PCB

10.2. RF Trace

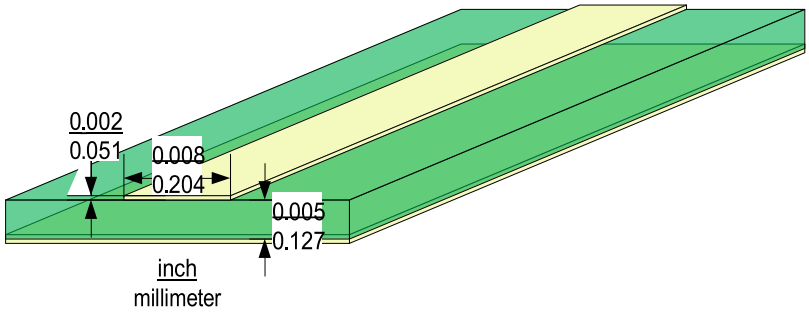

Figure 13. Typical Microstrip PCB Trace on Fr-4 Substrate

10.3. PCB Stack-Up

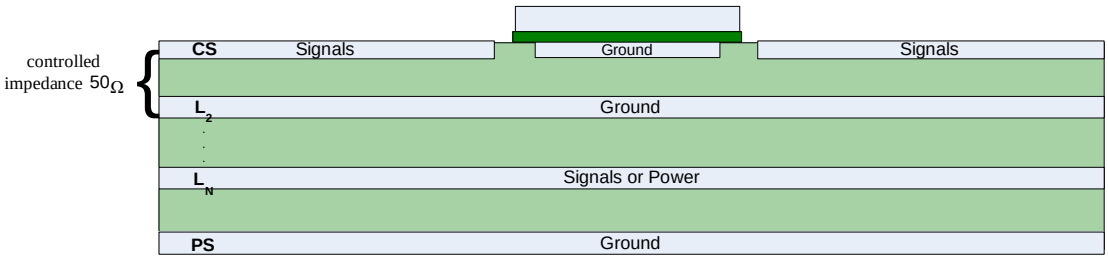

Figure 14. Typical PCB Stack-Up

10.4. PCB Layout Restrictions

Switching and high-speed components, traces, and VIAs must be kept away from the ORG4572-AG05 module.

Signal traces to/from module must have minimum length.

The recommended minimal distance from adjacent active components is 3mm.

Ground pads must be connected to the host PCB Ground with the shortest possible traces or VIAs.

In the event of a tight integration constraint or co-location with adjacent high-speed components like CPU or memory, high frequency components like transmitters, clock resonators or oscillators, LCD panels or CMOS image sensors, contact OriginGPS for application-specific recommendations.

11. DESIGN CONSIDERATIONS

11.1. Antenna

Antennas for GPS, Galileo, and GLONASS have a wider bandwidth than pure GPS antennas.

Some wideband antennas may not have a good axial ratio to block reflections of RHCP GPS, Galileo, and GLONASS signals. These antennas have lower rejection of multipath reflections and tend to degrade the overall performance of the receiver.

11.1.1. Passive Antenna

A design with a passive antenna requires RF layout skills and can be challenging.

11.1.2. Active Antenna

While designing with active antenna, consider using WAKEUP output to control auxiliary DC bias.

11.2. RF

The ORG4572-AG05 Spider module operates with received signal levels down to -167dBm and can be affected by high absolute levels of RF signals, moderate levels of RF interference near the GNSS bands, and by low levels of RF noise in GNSS band.

RF interference from nearby electronic circuits or radio transmitters can contain enough energy to desensitize ORG4572-AG05. These systems may also produce levels of energy outside of GNSS band, high enough to leak through RF filters and degrade the operation of the radios in ORG4572-AG05.

This issue becomes more critical in small products, where there are industrial design constraints. In that environment, transmitters for Wi-Fi, Bluetooth, RFID, cellular and other radios may have antennas physically close to the GNSS receiver antenna.

To prevent degraded performance of ORG4572-AG05, OriginGPS recommends performing EMI/jamming susceptibility tests for radiated and conducted noise on prototypes and assessing risks of other factors.

Contact OriginGPS for application-specific recommendations and design review services.

12. FIRMWARE UPDATES

Configuration details of the firmware are listed below:

- Normal mode is enabled.
- L1 full satellite is enabled.
- Constellations - GPS, Galileo, GLONASS, and BeiDou.
- Additional constellations - QZSS and SBAS.
- PPS is enabled
    - Always ON mode.
- The firmware is stored in the internal Flash memory and is upgradeable.

13. HANDLING INFORMATION

Follow these recommended steps to ensure maximum performance and proper care of your GNSS modules.

13.1. Moisture Sensitivity

The ORG4572-AG05 modules are classified as MSL (Moisture Sensitivity Level) 3 designated devices in accordance with the IPC/JEDEC J-STD-033B standard.

For modules that come in sample or bulk packaging, it is essential to perform a baking process before assembly. The recommended baking conditions involve subjecting the modules to a temperature of 125°C for a duration of 48 hours.

13.2. Assembly

The ORG4572-AG05 module is compatible with automatic pick-and-place assembly and reflow soldering processes.

To achieve reliable and consistent soldering results, it is recommended to use a solder paste stencil with a thickness of 5 mil.

13.3. Soldering

When reflow soldering the ORG4572-AG05 module, it should always be placed on the component side (top side) of the host PCB, following the guidelines specified in the standard IPC/JEDEC J-STD-020D for LGA SMD (Land Grid Array Surface Mount Devices).

It is crucial to avoid exposing the ORG4572-AG05 module to a face-down orientation during the reflow soldering process.

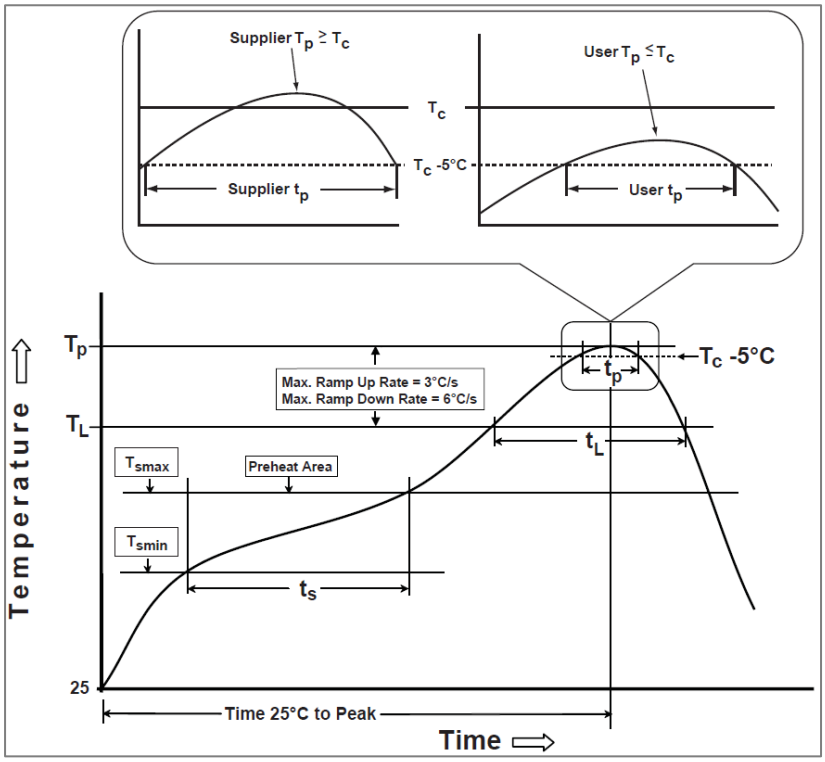

Figure 15. Recommended Soldering Profile

Throughout the soldering process, temperature measurement is conducted on the top surface of the ORG4572-AG05 module's package. To ensure proper soldering, the recommended peak reflow temperature is 250°C for 30 seconds when using Pb-free solder paste.

However, it's important to note that the actual board assembly reflow profile must be tailored individually based on the specific characteristics of the furnace utilized. This customization allows for optimal performance and adherence to quality standards.

Various factors influence the reflow furnace settings, including the number of heating and cooling zones, the type of solder paste and flux utilized, the board design, component density, and the types of packages used in the assembly.

Table 10. Soldering Profile Parameters

Symbol	Parameter	Min	Typical	Max	Unit
T_C	Classification Temperature		245		°C
T_P	Package Temperature			245	°C
T_L	Liquidous Temperature		217		°C
T_S	Soak/Preheat Temperature	150		200	°C
t_S	Soak/Preheat Time	60		120	s
t_L	Liquidous Time	60		150	s
t_P	Peak Time		30		s

13.4. Cleaning

In instances where flux cleaning is necessary, the ORG4572-AG05 module is designed to withstand a standard cleaning process using a vapor degreaser with Solvon® n-Propyl Bromide (NPB) solvent. Additionally, the module can be safely washed in DI (Deionized) water.

However, it is crucial to avoid using an ultrasonic degreaser for the cleaning process. The vibrations produced by the ultrasonic cleaning method may lead to performance degradation or, in extreme cases, damage the internal circuitry of the module.

13.5. Rework

Absolutely, when localized heating is needed for reworking or repairing the ORG4572-AG05 module, precautionary measures must be taken to prevent exposure to solder reflow temperatures that could cause irreversible damage to the device.

13.6. Safety Information

Improper handling and usage of the product can lead to permanent damage.

The ORG4572-AG05 module is an ESD (Electrostatic Discharge) sensitive device, making it vital to exercise caution during its handling.

13.7. Disposal Information

The ORG4572-AG05 module should never be treated as household waste.

Due to its electronic nature, it requires specialized disposal and recycling procedures to minimize environmental impact and recover valuable resources.

To properly dispose of or recycle electronic components like the ORG4572-AG05 module, contact your local waste management authority.

14. COMPLIANCE

The production of ORG4572-AG05 modules adheres to the following standards:

- IPC-6011/6012 Class2 for PCB manufacturing
- IPC-A-600 Class2 for PCB inspection
- IPC-A-610D Class2 for SMT acceptability

The production of ORG4572-AG05 modules takes place in facilities that have achieved accreditation in various internationally recognized management standards:

- ISO 9001:2008: The facilities where ORG4572-AG05 modules are manufactured have attained certification under ISO 9001:2008. This standard focuses on quality management systems and ensures that the manufacturing processes consistently meet customer requirements and deliver high-quality products.
- ISO 14001:2004: The facilities hold accreditation in ISO 14001:2004, which emphasizes environmental management systems. This standard ensures that the manufacturing practices consider environmental impacts and strive to minimize any negative effects, promoting sustainability and eco-friendly approaches.
- OHSAS 18001:2007: The facilities are accredited under OHSAS 18001:2007, which pertains to occupational health and safety management systems. This standard prioritizes the health and safety of employees and visitors within the manufacturing environment, fostering a safe working environment and risk reduction.

The ORG4572-AG05 modules are designed, manufactured, and handled in strict compliance with the following European Union directives and regulations:

- RoHS III (Directive 2015/65/EU): The modules adhere to RoHS III, which restricts the use of specific hazardous substances in electrical and electronic equipment to safeguard human health and the environment.
- REACH (Commission Regulation EU 2018/1881): The modules are manufactured and handled in accordance with the substance bans specified in Annex XVII of Commission Regulation EU 2018/1881 on Registration, Evaluation, Authorization, and Restriction of Chemicals. This includes adherence to all amendments and the candidate list issued by the European Chemicals Agency (ECHA).
- Radio Equipment Directive (Directive 2014/53/EU): The handling of ORG4572-AG05 modules complies with the EU directive 2014/53/EU, which concerns the placing of radio-electric equipment on the market. The compliance ensures that the modules meet the necessary requirements for radio equipment in the European market, as per the directive issued on 13 June 2017.

15. PACKAGING AND DELIVERY

15.1. Appearance

ORG4572-AG05 modules are delivered in reeled tapes for an automatic pick-and-place assembly process.

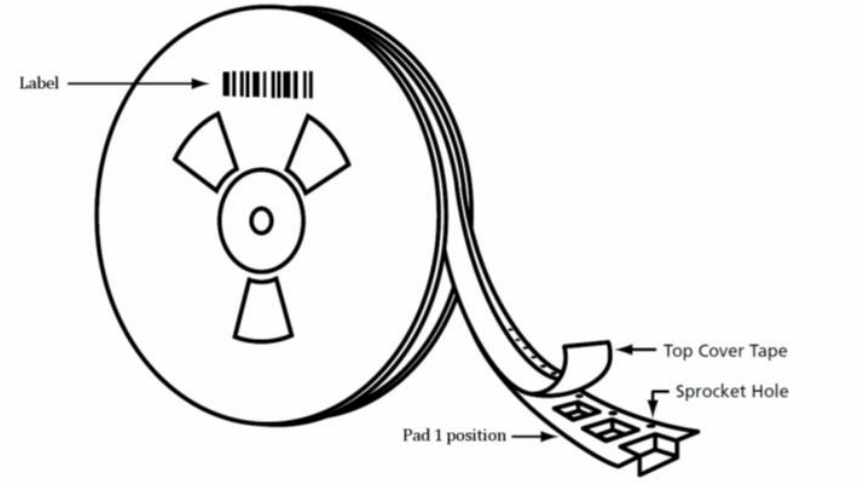

Figure 16. Module Position

ORG4572-AG05 modules are packed in two different tape reel quantities (TR1 and TR2).

Table 11. Reel Quantity

Suffix	Tape Reel 1 (TR1)	Tape Reel 2 (TR2)
Quantity	500	2000

Reels are dry-packed with a humidity indicator card and desiccant bag according to IPC/JEDEC J-STD-033B standard for MSL 3 devices.

Reels are vacuum sealed inside anti-static moisture barrier bags.

Sealed reels are labeled with an MSD sticker providing information about:

- MSL
- Shelf life
- Reflow soldering peak temperature
- Seal date

Sealed reels are packed inside cartons.

Reels, reel packs, and cartons are labeled with a sticker providing information about:

- Product description
- Part number
- Lot number
- Customer PO number
- Quantity
- Date code

15.2. Carrier Tape

The carrier tape is made of polystyrene with carbon (PS+C).

The cover tape is made of a polyester-based film with a heat-activated adhesive coating layer.

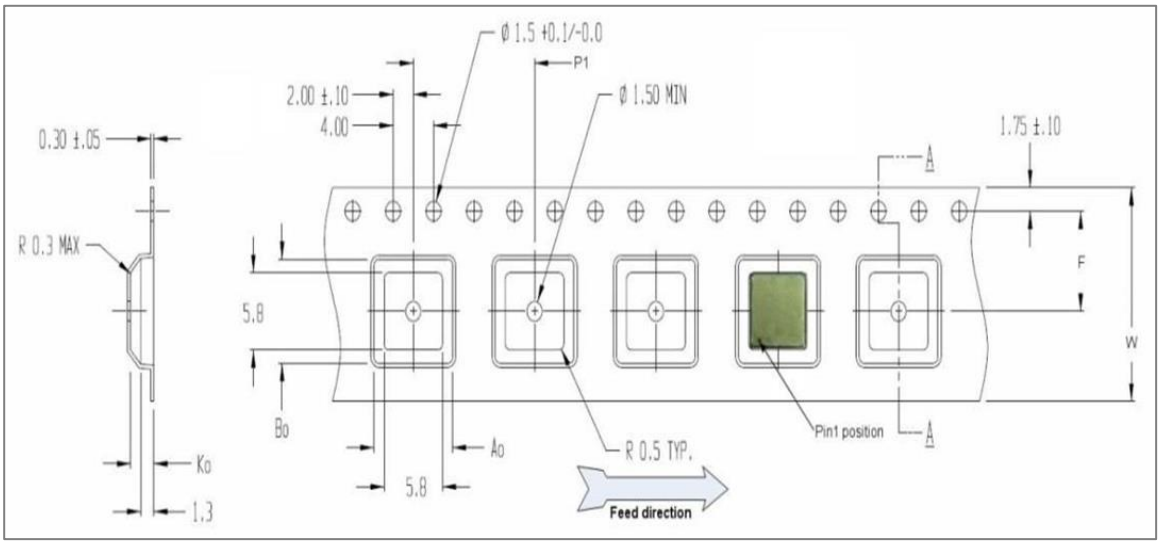

Figure 17. Carrier Tape

Table 12. Carrier Tape Dimensions

	MM	Inch
A0	8.3 ± 0.1	0.327 ± 0.004
B0	8.3 ± 0.1	0.327 ± 0.004
K0	2.7 ± 0.1	0.106 ± 0.004
F	7.5 ± 0.1	0.295 ± 0.004
P1	12.0 ± 0.1	0.472 ± 0.004
W	16.0 ± 0.3	0.630 ± 0.012

15.3. Reel

The product reel is made of anti-static plastic.

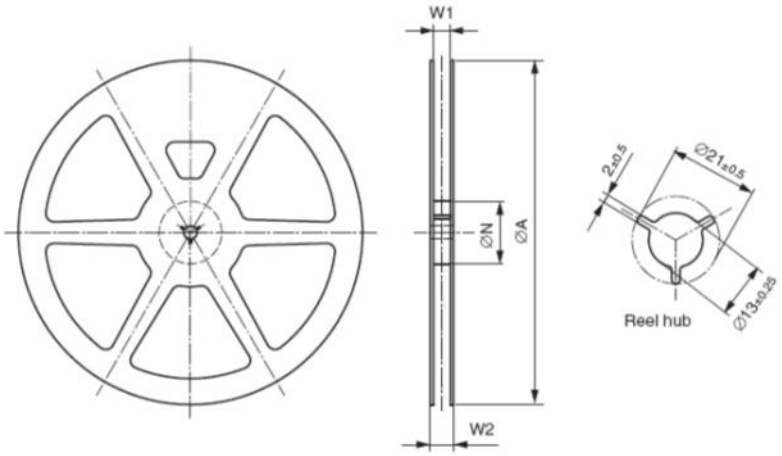

Figure 18. Product Reel

Table 13. Reel Dimensions

Suffix	TR1		TR2	
	mm	Inches	mm	Inches
ØA	178.0 ± 1.0	7.00 ± 0.04	330.0 ± 2.0	13.00 ± 0.08
ØN	60.0 ± 1.0	2.36 ± 0.04	102.0 ± 2.0	4.02 ± 0.08
W1	16.7 ± 0.5	0.66 ± 0.02	16.7 ± 0.5	0.66 ± 0.02
W2	19.8 ± 0.5	0.78 ± 0.02	22.2 ± 0.5	0.87 ± 0.02

16. ORDERING INFORMATION

The ORG4572-AG05 module may be ordered in accordance with the following methodology.

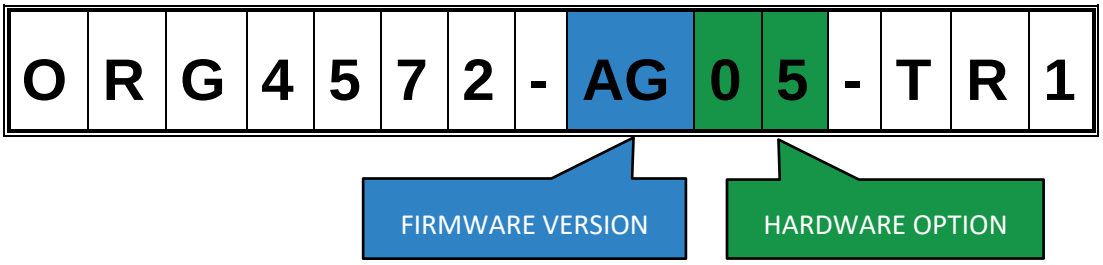

Figure 19. Ordering Options

Table 14. Orderable Devices

Part Number	FW Version	HW Option	Vcc Range	Packaging	SPQ
ORG4572-AG05-TR1	AG	05	1.8V	REELED TAPE	500
ORG4572-AG05-TR2	AG	05	1.8V	REELED TAPE	2000
ORG4572-AG05-UAR	AG	05	5V USB	EVALUATION KIT	1
ORG4572-AG05-USB	AG	05	5V USB	GNSS ON A STICK KIT	1