SPECIFICATIONS

CUSTOMER . PTC

SAMPLE CODE . NSC1602LRS-FWB-H

MASS PRODUCTION CODE . NPC1602LRS-FWB-H

SAMPLE VERSION . 01

SPECIFICATIONS EDITION . 002

DRAWING NO. (Ver.) · JLMD- NPC1602LRS-FWB-H_001

PACKAGING NO. (Ver.) : JPKG- NPC1602LRS-FWB-H_001

Customer Approved

Date:

POWERTIP 2014.05.26

Approved	Checked	Designer
閆偉	劉進	徐明菲

- □ Preliminary specification for design input
- Specification for sample approval

POWERTIP TECH. CORP.

Headquarters: No.8, 6th Road, Taichung Industrial Park,

Taichung, Taiwan

台中市 407 工業區六路 8號

TEL: 886-4-2355-8168

FAX: 886-4-2355-8166

E-mail: sales@powertip.com.tw

Http://www.powertip.com.tw



History of Version

Date (mm / dd / yyyy)	Ver.	Edi.	Description	Page	Design by
05/07/2014	01	001	First Sample		徐明菲
05/20/2014	01	002	Update Specification	-	徐明菲

Total: 30 Pages



Contents

1. SPECIFICATIONS

- 1.1 Features
- 1.2 Mechanical Specifications
- 1.3 Absolute Maximum Ratings
- 1.4 DC Electrical Characteristics
- 1.5 Optical Characteristics
- 1.6 Backlight Characteristics

2. MODULE STRUCTURE

- 2.1 Counter Drawing
- 2.2 Interface Pin Description
- 2.3 Timing Characteristics
- 2.4 Display Command
- 2.5 Character Pattern
- 2.6 Jumper (Setting different use)

3. QUALITY ASSURANCE SYSTEM

- 3.1 Quality Assurance Flow Chart
- 3.2 Inspection Specification

4. RELIABILITY TEST

4.1 Reliability Test Condition

5. PRECAUTION RELATING PRODUCT HANDLING

- 5.1 Safety
- 5.2 Handling
- 5.3 Storage
- 5.4 Terms of Warranty

Appendix :

- 1. LCM Drawing
- 2. LCM Packaging

Note: For detailed information please refer to IC data sheet: SITRONIX---ST7066U-0B



1. SPECIFICATIONS

1.1 Features

Item	Standard Value
Display Type	16*2 Characters
LCD Type	STN Gray , Positive , Transflective , Extended Temp.
Driver Condition	LCD Module : 1/16 Duty , 1/5 Bias
Viewing Direction	6 O'clock
Weight	40g
Interface	6800-series 8-bit parallel
Driver IC	ST7066U-0B
	THIS PRODUCT CONFORMS THE ROHS OF PTC
ROHS	Detail information please refer web site :
	http://www.powertip.com.tw/news.php?area_id_view=1085560481/

1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	84.0 (L) * 44.0 (W) *12.7 (H)	mm
Viewing Area	66.0 (L) * 16.0 (W)	mm
Active Area	56.2 (L) * 11.5 (W)	mm
Character Size	2.95(L)* 5.55 (W)	mm
Character Pitch	3.55(L)* 5.95 (W)	mm

Note: For detailed information please refer to LCM drawing

1.3 Absolute Maximum Ratings

Item	Symbol	Condition	Min.	Max.	Unit
Power Supply Voltage	V_{DD}	-	-0.3	7.0	V
LCD Driver Supply Voltage	V_{LCD}	-	VDD -10.0	V _{DD} +0.3	
Input Voltage	V _{IN}	-	-0.3	V _{DD} +0.3	V
Operating Temperature	T _{OP}	-	-20	70	$^{\circ}\! \mathbb{C}$
Storage Temperature	T _{ST}	-	-30	80	$^{\circ}\! \mathbb{C}$
Storage Humidity	H _D	Ta<60 °C	-	90	%RH



1.4 DC Electrical Characteristics

 V_{DD} =5.0 \pm 0.5V, V_{SS} =0V, Ta = 25 $^{\circ}$ C

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Logic Supply Voltage	V_{DD}	-	4.5	5.0	5.5	V
"H" Input Voltage	V _{IH}	-	0.7 V _{DD}	-	VDD	V
"L" Input Voltage	V _{IL}	-	-0.3	-	0.6	V
"H" Output Voltage	V _{OH}	IOH=-0.1mA	3.9	-	V _{DD}	V
"L" Output Voltage	V _{OL}	IOL=0.1mA	-	-	0.4	٧
Supply Current	I _{DD}	V _{DD} = 5.0,Vop= 4.5 V Pattern= Horizontal Line *1	-	2	3	mA
		-20 ℃	4.4	4.6	4.8	
LCM Driver Voltage	V _{OP} *2	25℃	4.3	4.5	4.7	V
		70℃	3.8	4.0	4.2	

NOTE: *1 The Maximum current display

*2 The V_{OP} test point is $(V_{DD} - V_0)$





1.5 Optical Characteristics

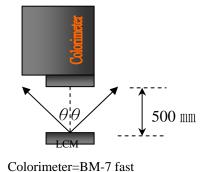
LCD Panel : 1/16 Duty , 1/5 Bias , V_{LCD} = 4.5 V , Ta =25 $^{\circ}$ C

				17 10 Bat		i LCD		
Item		Symbol	Conditions	Min.	Тур.	Max.	Unit	Reference
Paspansa Tima	Rise	tr		-	80	120	me	Note 2
Response Time	Fall	tf	-	_	150	225	ms	NOIE 2
	Тор	θ+		-	40			
Viewing angle	Bottom	θ-	C>2.0	-	40	-	Dog	Note 1
range	Left	θL	C <u>></u> 2.0	-	45	-	Deg.	Note 1
	Right	θR		-	45	-		
Contrast Ra	tio	С	θ = 0°	-	5	•	-	Note 3
Average Bright (with LCD)		IV		30	35	-	cd/m ²	
Wavelengt (with LCD)		λр	IE-100 mA	568	571	574	nm	Note 4
Uniformity *1		ΔΒ	IF=100 mA	70	-	-	%	Note 4

Note 4:

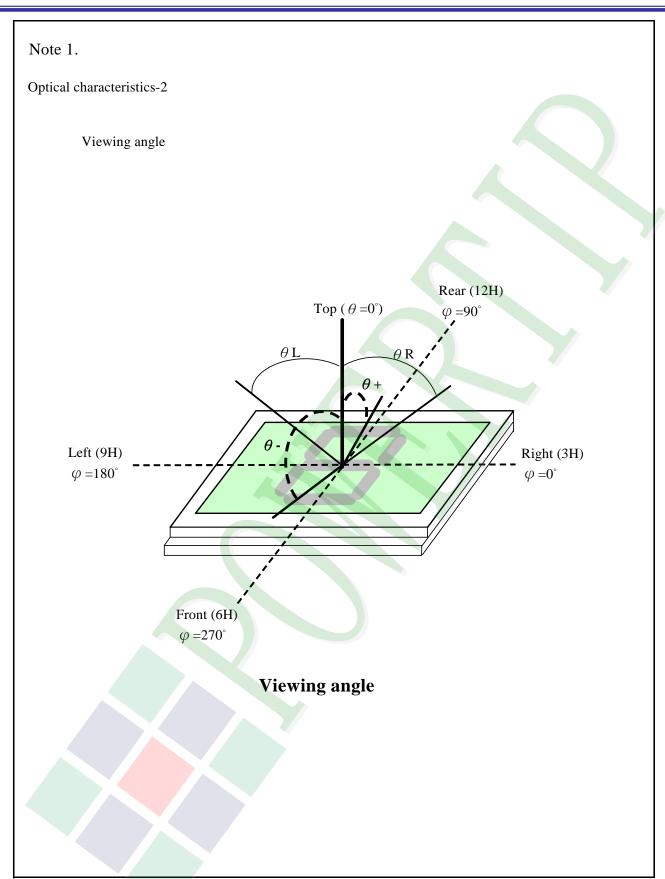
- 1 : △B=B(min) / B(max) * 100%
- 2 : Measurement Condition for Optical Characteristics:
 - a : Environment: 25°C±5°C / 60±20%R.H → no wind → dark room below 10 Lux at typical lamp current and typical operating frequency.
 - b : Measurement Distance: $500 \pm 50 \text{ mm}$, $(\theta = 0^{\circ})$
 - c: Equipment: TOPCON BM-7 fast, (field 1°), after 10 minutes operation.
 - d: The uncertainty of the C.I.E coordinate measurement ±0.01, Average Brightness ± 4%



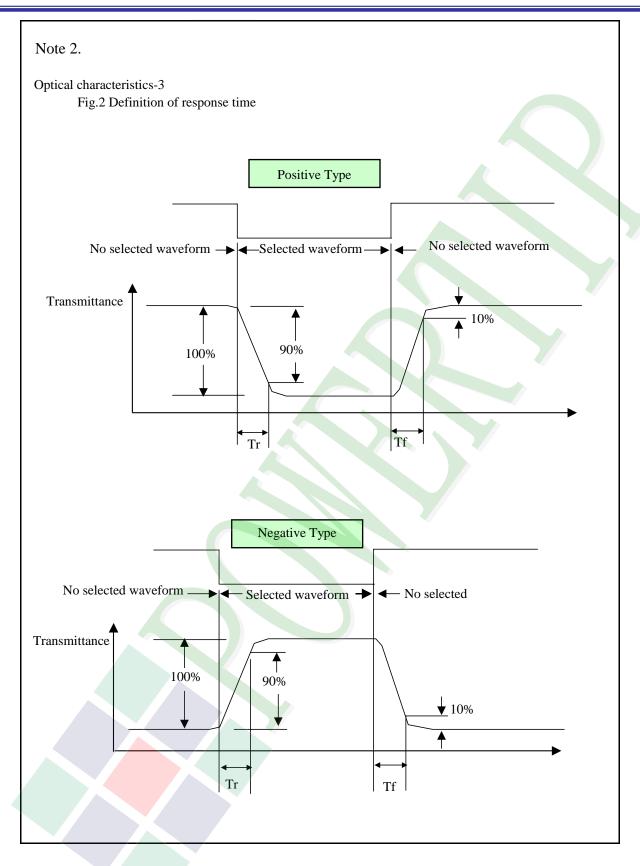


SPEC Edi.002











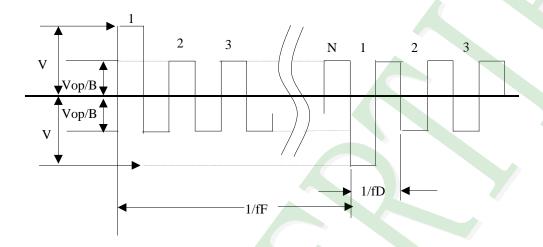
Electrical characteristics-2

[™] 2 Drive waveform

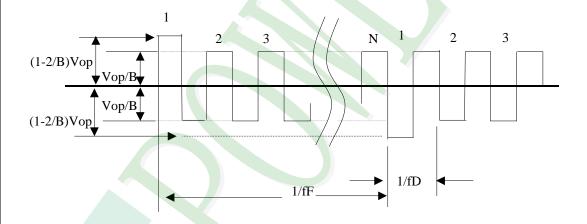
Vop: Drive voltage fF: Frame frequency 1/B: Bias fD: Drive frequency

N: Duty

(1) Selected waveform



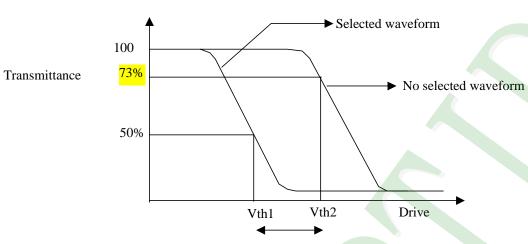
(2) Non- Selected wave form



Note:

Frame frequency is defined as follows: Common side supply voltage peak - to - peak /2 = 1 period

Note 3.: Definition of Vth



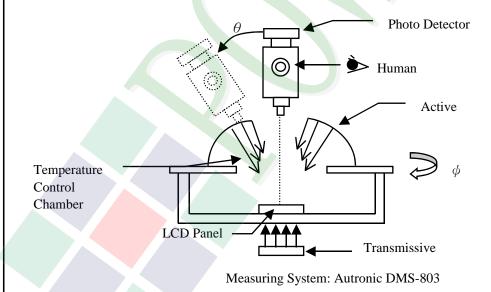
Active voltage range

	Vth1	Vth2
View direction	10°	40°
Drive waveform	(Selected waveform)	(No selected waveform)
Transmittance	50%	73%

※1 Contrast ratio

= (Brightness in OFF state) / (Brightness in ON state)

Outline of Electro-Optical Characteristics Measuring System





1.6 Backlight Characteristics

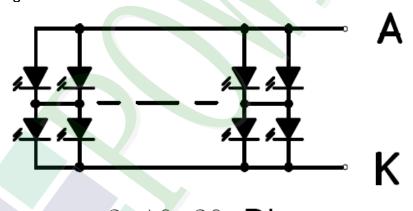
Maximum Ratings

Item	Symbol	Conditions	Min.	Max.	Unit
Forward Current	IF	Ta =25°ℂ	-	150	mA
Reverse Voltage	VR	Ta =25°ℂ	- 🛕	8	V
Power Dissipation	PD	Ta =25°ℂ	- ^	660	mW

Electrical / Optical Characteristics

Item	Symbol	Conditions	Min.	Тур.	Max.	Unit
Forward Voltage	VF	IF= 100 mA	4.0	4.2	4.4	V
Reverse Current	IR	VR=8V		-	100	uA
Average Brightness	IV	IF= 100 mA	160	190	-	cd/m ²
Wavelength	λр	IF= 100 mA	569	572	575	nm
Color	Yellow/Green					

Internal Circuit Diagram:





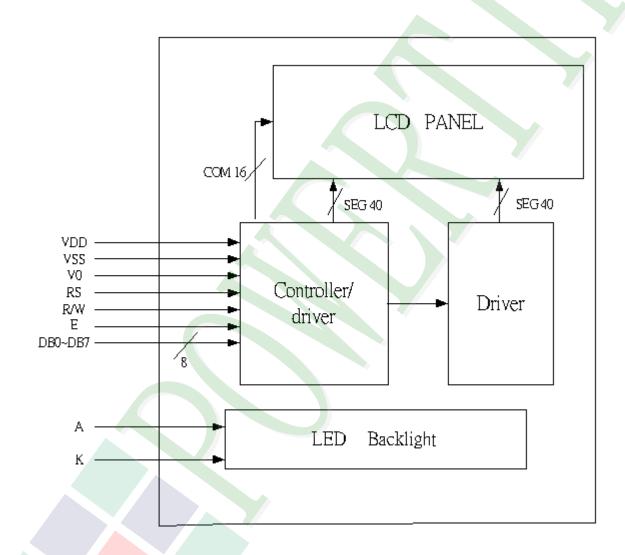
2. MODULE STRUCTURE

2.1 Counter Drawing

2.1.1 LCM Mechanical Diagram

* See Appendix

2.1.2 Block Diagram





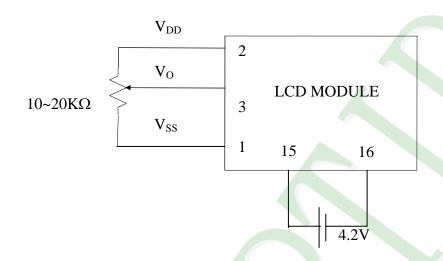
2.2 Interface Pin Description

Pin No.	Symbol	Signal Description
1	V _{SS}	Power Supply (Vss=0)
2	V_{DD}	Power Supply (5V)
3	Vo	Operating voltage for LCD
4	RS	Register Selection input High = Data register Low = Instruction register (for write) Busy flag address counter (for read)
5	R/W	Read/Write signal input is used to select the read/write mode High = Read mode, Low = Write mode
6	E	Start enable signal to read or write the data
7	DB0	Four low order bi-directional three-state data bus lines. Use for
8	DB1	data transfer between the MPU and the LCD module.
9	DB2	These four are not used during 4-bit operation.
10	DB3	These rour are not used during 4 bit operation.
11	DB4	
12	DB5	Four high order bi-directional three-state data bus lines. Used for data transfer between the MPU and the LCD module.
13	DB6	DB7 can be used as a busy flag.
14	DB7	DDT call be ased as a basy mag.
15	Α	LED+
16	K	LED-



2.2.1 Application Notes

Contrast Adjust



2.2.2 Refer Initial code

```
void initial()
{
    delay(40);
    write_com(0x01);
```

delay(5);
write_com(0x38);

delay(5);

write_com(0x0c);

delay(5);

write_com(0x06)

delay(5);

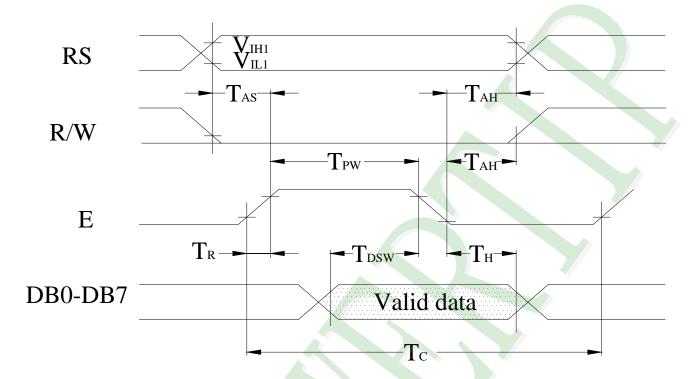
}

NPC1602LRS-FWB-H Page14 SAMPLE Ver.01 SPEC Edi.002

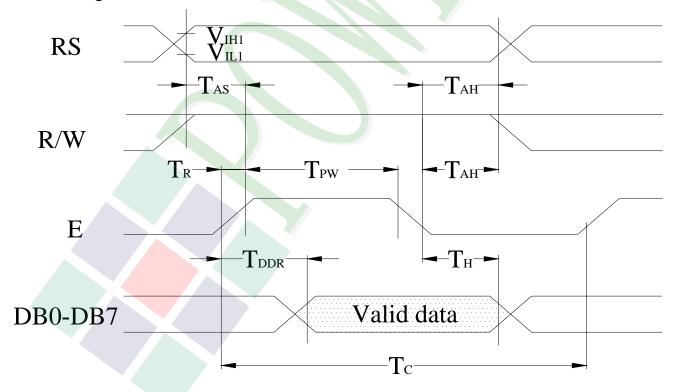


2.3 Timing Characteristics

Writing data from MPU to ST7066U



Reading data from ST7066U to MPU





• Write Mode (Writing data from MPU to ST7066U)

 $(VDD = +5V,Ta=25^{\circ}C)$

Symbol	Characteristics	Test Condition	Min.	Тур.	Max.	Unit
T _C	Enable Cycle Time	Pin E	1200	ı	-	ns
T_PW	Enable Pulse Width	Pin E	140	ı		ns
T_R , T_F	Enable Rise / Fall Time	Pin E	ı	-	25	ns
T _{AS}	Address Setup Time	Pins: RS , RW,E	0	-	-	ns
T _{AH}	Address Hold Time	Pins :RS,RW,E	10	1	-	ns
T _{DSW}	Data Setup Time	Pins:DB0~DB7	40		-	ns
T _H	Data Hold Time	Pins:DB0~DB7	10	-	-	ns

• Read Mode (Reading data from ST7066U to MPU)

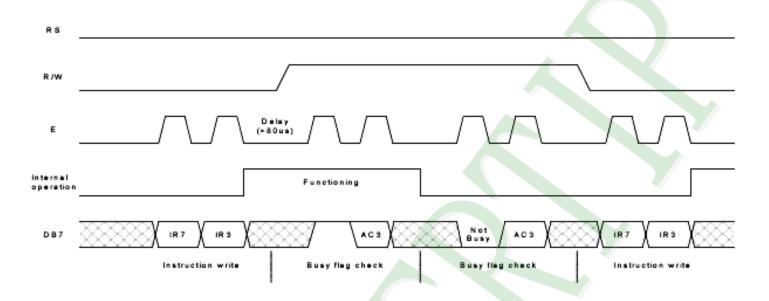
 $(VDD = +5V,Ta=25^{\circ}C)$

Symbol	Characteristics	Test Condition	Min.	Тур.	Max.	Unit
T _C	Enable Cycle Time	Pin E	1200	1	-	ns
T _{PW}	Enable Pulse Width	Pin E	140	1	-	ns
T_R , T_F	Enable Rise / Fall Time	Pin E	-	1	25	ns
T _{AS}	Address Setup Time	Pins: RS , RW,E	0	-	-	ns
T _{AH}	Address Hold Time	Pins :RS,RW,E	10	-	-	ns
T_{DDR}	Data Setup Time	Pins:DB0~DB7		-	100	ns
T _H	Data Hold Time	Pins:DB0~DB7	10	-	-	ns



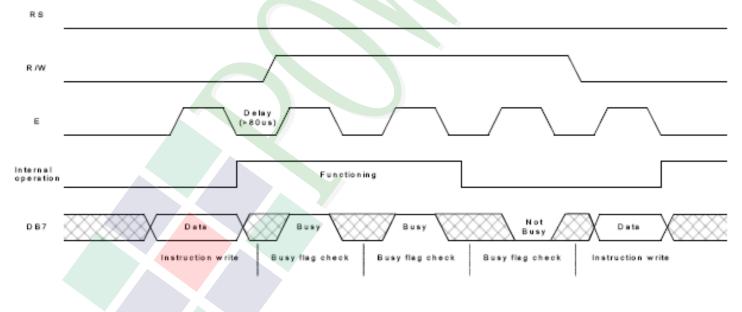
For 4-bit interface date, only four bus lines (DB4 to DB7) are used for transfer.

Example of busy flag check timing sequence



For 8-bit interface date, all eight bus lines (DB0 to DB7) are used .

Example of busy flag check timing sequence





2.4 Display Command

2.4 Dispi	ay '	OU		IIaii	<i>A</i>							
				I	nstru	ction	Code	e				Description
Instructions		R/	DB	DB	DB	DB	DB	DB	DB	DB	Description	Time
	RS	W	7	6	5	4	3	2	1	0		(270KHz)
Clear Display	0	0	0	0	0	0	0	0	0	1	Write "20H" to DDRAM. and set DDRAM address to "00H" from AC.	1.52ms
Return Home	0	0	0	0	0	0	0	0	1	×	Set DDRAM address to "00H" from AC and return cursor to it's original position if shifted. The contents of DDRAM are not changed.	1.52ms
Entry Mode Set	0	0	0	0	0	0	0	1	I/D	S	Sets cursor move direction and specifies display shift. These operations are performed during data write and read.	37μs
Display ON/OFF	0	0	0	0	0	0	1	D	С	В	D=1 : entire display on C=1 : cursor on B=1 : cursor position on	37µs
Cursor or Display Shift	0	0	0	0	0	1	S/C	R/L	×	×	Set cursor moving and display shift control bit, and the the direction, without changing of DDRAM data.	37µs
Function Set	0	0	0	0	1	DL	N	F	×	×	DL: interface data is 8/4 bits NL: number of line is 2/1 F: font size is 5×11/5×8	37µs
Set CGRAM Address	0	0	0	1	AC 5	AC 4	AC 3	AC 2	AC 1	AC 0	Set CGRAM address in address counter.	37µs
Set DDRAM Address	0	0	1	AC 6	AC 5	AC 4	AC 3	AC 2	AC 1	AC 0	Set DDRAM address in address counter.	37µs
Read Busy Flag and Address	0	1	B F	AC 6	AC 5	AC 4	AC 3	AC 2	AC 1	AC 0	Whether during internal operation or not can be known by reading BF. The contents of address counter can also be read.	0μs



Write Data	1	0	D	D6	D5	D4	D3	D2	D1	DO	Write data into internal RAM	37us	
to RAM	1	U	7	פט סט		D4 D3				DU	(DDRAM/CGRAM).	37 μ3	
Read Data	1	1	D	D6	D5	D4	D3	D3	D1	DO	Read data from internal RAM	37us	
from RAM	ı	ı	7	סט	טט	D4	טט	DZ	וטו	טט	(DDRAM/CGRAM).	<i>31</i> μ 5	

Note:

Be sure the ST7066U is not in the busy state (BF=0) before sending an instruction from the MPU to the ST7066.

If an instruction is sent without checking the busy flag, the time between the first instruction and next instruction will take much longer than the instruction time itself.

Before checking BF, be sure to wait at least 80us.. Do not keep "E" always "High" for checking BF Refer to Instruction Table for the list of each instruction execution time.





2.5 Character Pattern

NO.7066-0B

<u> 10.7</u>	000-	00														
67-64 63-60	0000	0001	0010	0011	0100	0101	0110	0111	1000	1001	1010	1011	1100	1101	1110	1111
0000	CG RAM (1)															
0001	(2)															
0010	(3)															
0011	(4)															
0100	(5)															
0101	(6)															
0110	(7)															
0111	(8)															
1000	(1)															
1001	(2)															
1010	(3)															
1011	(4)															
1100	(5)															
1101	(6)															
1110	(7)															
1111	(8)															

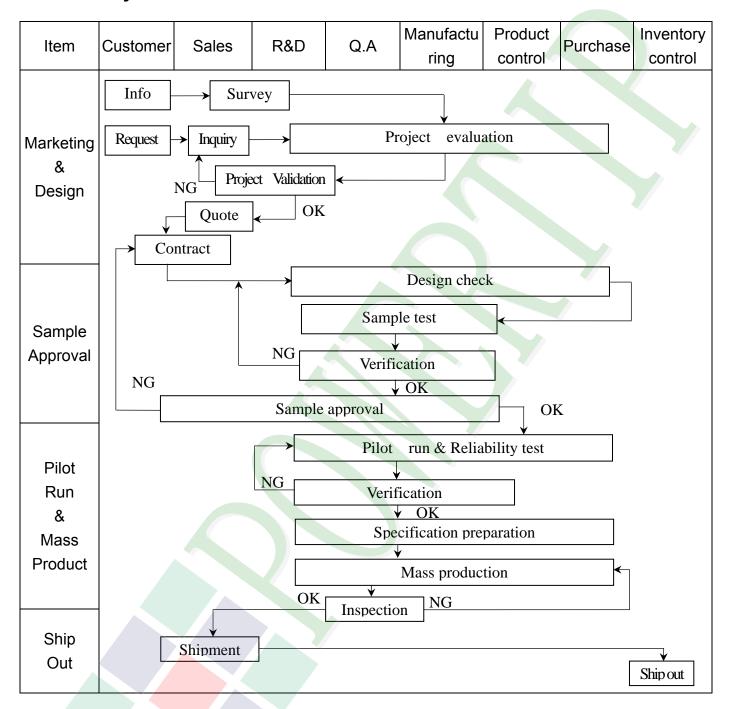
2.6 JUMPER(Setting different use)

J1/J4:SHORT; Others :open

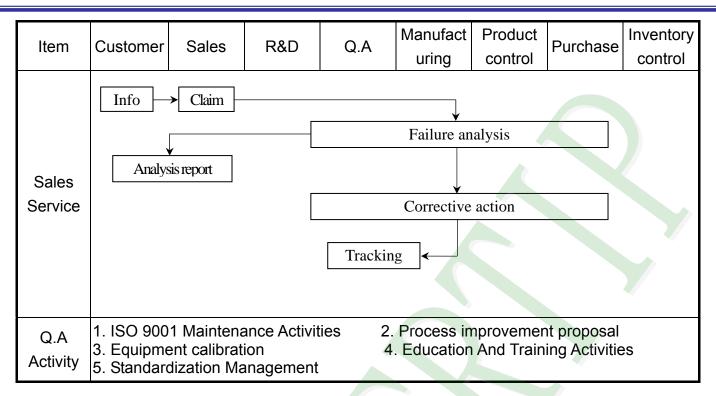


3. QUALITY ASSURANCE SYSTEM

3.1 Quality Assurance Flow Chart









3.2 Inspection Specification

- ◆Scope: The document shall be applied to LCD Module for Monotype and Color STN(Ver. B01).
- ◆Inspection Standard: MIL-STD-105E Table Normal Inspection Single Sampling Level Ⅱ.
- ◆Equipment : Gauge \ MIL-STD \ Powertip Tester \ Sample
- ◆Defect Level: Major Defect AQL: 0.4; Minor Defect: AQL: 1.5.
- **♦**OUT Going Defect Level : Sampling .
- ◆Manner of appearance test :
 - (1). The test be under 20W×2 fluorescent light 'and distance of view must be at 30 cm.
 - (2). Standard of inspection: (Unit: mm)
 - (3). The test direction is base on about around 45° of vertical line. (Fig. 1)
 - (4). Definition of area . (Fig. 2)

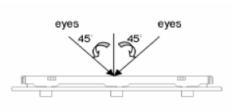


Fig.1

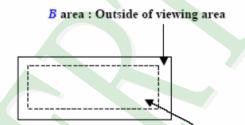


Fig. 2 A area: viewing area

Specification:

NO	Item	Criterion	Level
		1. 1 The part number is inconsistent with work order of Production.	Major
01	Product condition	1. 2 Mixed production types.	Major
		1. 3 Assembled in inverse direction.	Major
02	Quantity	2. 1 The quantity is inconsistent with work order of production.	Major
03	Outline dimension	3. 1 Product dimension and structure must conform to Structure diagram.	Major
		4. 1 Missing line character and icon.	Major
		4. 2 No function or no display.	Major
04	Electrical Testing	4. 3 Output data is error.	Major
		4. 4 LCD viewing angle defect.	Major
		4. 5 Current consumption exceeds product specifications.	Major



NO NO	Item	Criterion					Level	
	Black or white dot \ scratch \ contamination	 5. 1 Round type: 5. 1. 1 display only: • White and black spots on display ≤ 0, 30 mm, no more than 4 white or black spots present. • Densely spaced: NO more than two spots or lines within 3 mm. 						
	Round type	5. 1. 2 Non-display : Dimension (diameter : Φ)	Acceptance (Q'ty) A area B area					
	→ <u>x</u> ← _↓	$\Phi \leq 0.10$		ept no dense				
05	● Y	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$		3	I	Ignore		Minor
	$\Phi = (x+y)/2$	Total quantity 4		4				
	Line type	5. 1. 3 Line type: Dimension Length (L) Width (W) W \leq 0 L \leq 3. 0 0. 03 < W \leq 0 L \leq 2. 5 0. 05 < W \leq 0.	0. 03	Accept A area Accept no des		e (Q'ty) B area Ignore		
		$L \equiv 2.0 0.00 \forall W \equiv 0.$ $W > 0$		Ası	roun	d type		
06	Polarizer Bubble	$\begin{array}{c} \textbf{Dimension} \\ (\textbf{diameter}:\Phi) \\ \hline \Phi \leq 0, 20 \\ \hline 0, 20 < \Phi \leq 0, 50 \\ \hline 0, 50 < \Phi \leq 1, 00 \\ \hline \Phi > 1, 00 \\ \hline \textbf{Total quantity} \end{array}$		Acceptance (A area Accept no dense 3 2 0 4		Q'ty) B area Ignore		Minor



NO	Item	Criterion					
		Symbols: X: The length of crack Z: The thickness of crack t: The thickness of glass Y: The width of crack W: terminal length a: LCD side length					
		7. 1 General glass chip: 7. 1. 1 Chip on panel surface and crack between panels:					
		Z Z Y					
07	The crack of glass	SP SP [NG]	Minor				
		Seal width					
		X Y Z					
		≤ a Crack can't enter viewing area ≤1/2 t					
		≤ a Crack can't exceed the half of SP width. 1/2 t < Z ≤2 t					



NO	Item	Criterion	Level					
		Symbols: X: The length of crack Z: The thickness of crack t: The thickness of glass X: The width of crack W: terminal length a: LCD side length						
		7. 1. 2 Corner crack:						
		X Y Z						
		$\leq 1/5$ a Crack can't enter viewing area $Z \leq 1/2$ t						
	The crack of	$\leq 1/5$ a Crack can't exceed the half of SP width. $1/2$ t $<$ Z ≤ 2 t						
07	glass	7.2 Protrusion over terminal:	Minor					
		7. 2.1 Chip on electrode pad:						
		W Y W W						
		A V V Z						
		$\begin{array}{c cccc} X & Y & Z \\ \hline Front & \leq a & \leq 1/2 \text{ W} & \leq t \end{array}$						
		Back Neglect						
		Dack						



NO	Item	Criterion	Level
		Symbols: X: The length of crack Y: The width of crack. Z: The thickness of crack W: terminal length t: The thickness of glass a: LCD side length 7. 2. 2 Non-conductive portion:	
	The crack of glass	X Y Z	
07		≦1/3 a ≦W ≦t ⊙ If the chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode	Minor
		terminal specifications. 7. 2. 3 Glass remain:	
		Y X W Pitch	
		$\begin{array}{c cccc} X & Y & Z \\ & \leq a & \leq 1/3 \text{ W} & \leq t \end{array}$	



NO	Item	Criterion	Level
		8. 1 Backlight can't work normally.	Major
08	Backlight elements	8. 2 Backlight doesn't light or color is wrong.	Major
		8. 3 Illumination source flickers when lit.	Major
		9. 1 Pin type must match type in specification sheet.	Major
		9. 2 No short circuits in components on PCB or FPC.	Major
09	General appearance	9. 3 Product packaging must the same as specified on packaging specification sheet.	Minor
		9. 4 The folding and peeled off in polarizer are not acceptable.	Minor
		9. 5 The PCB or FPC between B/L assembled distance (PCB or FPC) is ≤1. 5 mm.	Minor



4. RELIABILITY TEST

4.1 Reliability Test Condition

NO.	TEST ITEM	TEST C	ONDITION			
1	High Temperature Storage Test	Keep in +80°C±2°C 96 hrs Surrounding temperature, ther 4hrs.	storage at normal condition			
2	Low Temperature Storage Test	Keep in -30℃ ±2℃ 96 hrs Surrounding temperature, then storage at normal condition 4hrs.				
3	High Temperature / High Humidity Storage Test	Keep in +60°C / 90% R.H duration for 96 hrs Surrounding temperature, then storage at normal condition 4hrs. (Excluding the polarizer)				
4	Temperature Cycling Storage Test	-30(min) → +25°C →+80°C (max) → +25°C (30mins) (5mins) (5mins) (5mins) 10 Cycle Surrounding temperature, then storage at normal condition 4hrs.				
5	ESD Test	Air Discharge: Apply 2 KV with 5 times Discharge for each polarity +/- 1. Temperature ambiance : 15°C ~35°C 2. Humidity relative : 30%~60% 3. Energy Storage Capacitance(Cs+Cd) : 150pF±10% 4. Discharge Resistance(Rd) : 330Ω±10% 5. Discharge, mode of operation : Single Discharge (time between successive discharges at least				
6	Vibration Test (Packaged)	 Sine wave 10~55 Hz frequence The amplitude of vibration : Each direction (X ⋅ Y ⋅ Z) d 	1.5 mm			
7	Drop Test (Packaged)	Packing Weight (Kg 0 ~ 45.4 45.4 ~ 90.8 90.8 ~ 454 Over 454 Drop Direction : %1 corner / 3 e	122 76 61 46			



5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

- 5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module, be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So , please handle it very carefully ,do not touch , push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands, this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is 320±10°C and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM.

5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is 25°C ±5°C and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush, shake, or jolt the module.

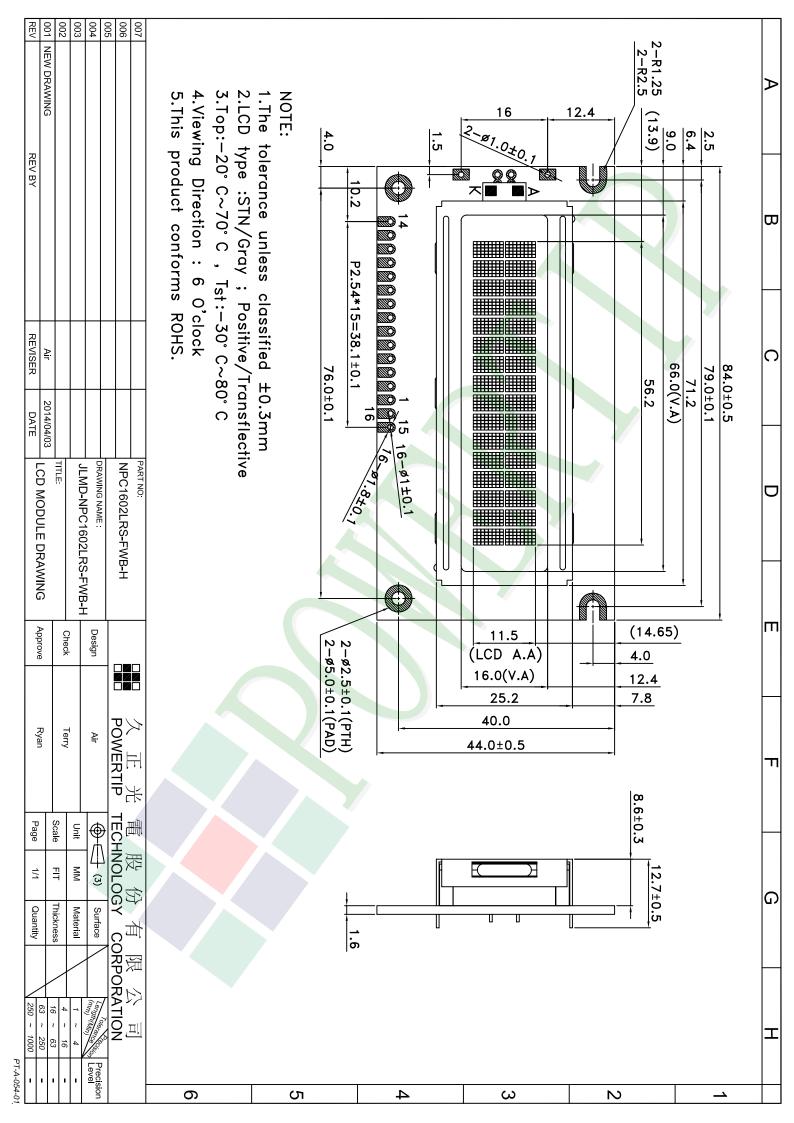
5.4 TERMS OF WARRANTY

5.4.1 Applicable warrant period

The period is within thirteen months since the date of shipping out under normal using and storage conditions.

5.4.2 Unaccepted responsibility

This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required



Check Contact Approve Ver.001 LCM包裝規格書 JPKG-NPC1602LRS-FWB-H Ryan Terry Air Documents NO. LCM Packaging Specifications 1. 包裝材料規格表 (Packaging Material): (per carton) 1Pcs Weight No. Item Model Dimensions (mm) Quantity Total Weight 成品 (LCM) NPC1602LRS-FWB-H 84.0 X 44.0 X 12.7 0.04 468 18.72 2 0.5148 靜電袋(1)Antistatic Bag BAG100100ARABA 100 X 100 0.0011 468 3 A1-1隔板(2)A1-1 Partition 295 X 47 X 3 0.0078 1.3104 BX29500047BZBA 168 4 B1-1隔板(3)B1-1 Partition BX24500047BZBA 245 X 47 X 3 0.0065 48 0.312 氣泡紙(4)Bubble Sheet 280 X 240 0.006 24 BAG280240BWABA 0.144 6 C1內盒(5)Product Box BX31025555AABA 310 X 255 X 55 0.13 12 1.56 7 外紙箱(6)Carton BX52732536CCBA 527 X 325 X 360 0.83 1 0.83 8 9 一整箱總重量 (Total LCD Weight in carton): 23.39 Kg±10% 3.單箱數量規格表 (Packaging Specifications and Quantity): (1)Quantity Of Spacer: A1-1隔板 X 14 , B1-1隔板 (2)Total LCM quantity in carton: quantity per box x no of boxes 12 468 (4) 氣泡紙 **Bubble Sheet** (1)靜電袋+LCM Antistatic Bag+LCM (3) B1-1隔板 B1-1 Partition (2) A1-1隔板 ₩, A1-1 Partition (4) 氣泡紙 Bubble Sheet (6)外紙箱 Carton (5) C1內盒 Product Box 特 記 事 項 (REMARK) 4. Label Specifications: 5. LCM排放示意圖(前後間隔不放置): 5. LCM placed as figure showing: 標籤依廠內標準作業 (First and last slot should be empty)