

## CLE90UH1200TLB

advanced

3~ Rectifier					
$V_{RRM}$	=	1200 V			
IDAV	=	90 A			
I <sub>FSM</sub>	=	350 A			

# **High Efficiency Thyristor**

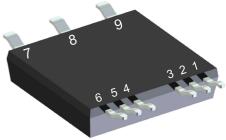
### SemiFast

3~ Rectifier Bridge, half-controlled (high-side)

#### Part number

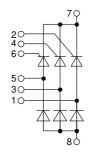
### CLE90UH1200TLB

Marking on Product: CLE90UH1200TLB



Backside: isolated







#### Features / Advantages:

- Thyristor for line and moderate frequencies
- Short turn-off time
- Planar passivated chip
- Long-term stability

#### **Applications:**

- Line rectifying 50/60 Hz
- Drives
- SMPS
- UPS

### Package: SMPD

- Isolation Voltage: 3000 V~
- Industry convenient outline
- RoHS compliant
- Epoxy meets UL 94V-0
- Soldering pins for PCB mounting
- Backside: DCB ceramic
- Reduced weight
- Advanced power cycling

#### **Disclaimer Notice**

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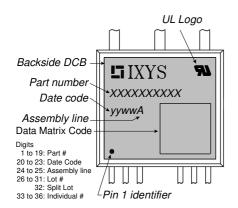
Rectifier					Ratings		1 -
Symbol	Definition	Conditions		min.	typ.	max.	Unit
V <sub>RSM/DSM</sub>	max. non-repetitive reverse/forwa	rd blocking voltage	$T_{VJ} = 25^{\circ}C$			1200	٧
V <sub>RRM/DRM</sub>	max. repetitive reverse/forward bl		$T_{VJ} = 25^{\circ}C$			1200	٧
I <sub>R/D</sub>	reverse current, drain current	$V_{R/D} = 1200 \text{ V}$	$T_{VJ} = 25^{\circ}C$			10	μΑ
		$V_{R/D} = 1200 \text{ V}$	$T_{VJ} = 125^{\circ}C$			2	mA
$V_{T}$	forward voltage drop	$I_T = 30 A$	$T_{VJ} = 25^{\circ}C$			1.30	٧
		I <sub>T</sub> = 90 A				1.80	٧
		$I_T = 30 A$	$T_{VJ} = 125$ °C			1.28	٧
		I <sub>T</sub> = 90 A				1.95	٧
I DAV	bridge output current	$T_C = 90^{\circ}C$	$T_{VJ} = 150$ °C			90	Α
		120° sine					
$V_{T0}$	threshold voltage	oss calculation only	$T_{VJ} = 150$ °C			0.92	٧
r <sub>T</sub>	slope resistance	oss calculation only				13	mΩ
R <sub>thJC</sub>	thermal resistance junction to cas	e				0.9	K/W
R <sub>thCH</sub>	thermal resistance case to heatsi	nk			0.40		K/W
P <sub>tot</sub>	total power dissipation		$T_{C} = 25^{\circ}C$			140	W
I <sub>TSM</sub>	max. forward surge current	t = 10 ms; (50 Hz), sine	$T_{VJ} = 45^{\circ}C$			350	Α
		t = 8.3  ms; (60 Hz), sine	$V_R = 0 V$			380	Α
		t = 10 ms; (50 Hz), sine	T <sub>vJ</sub> = 150°C			300	А
		t = 8,3  ms; (60 Hz), sine	$V_R = 0 V$			320	Α
l²t	value for fusing	t = 10 ms; (50 Hz), sine	$T_{VJ} = 45^{\circ}C$			615	A <sup>2</sup> s
		t = 8.3  ms; (60 Hz), sine	$V_R = 0 V$			600	A <sup>2</sup> s
		t = 10 ms; (50 Hz), sine	T <sub>vJ</sub> = 150°C			450	A <sup>2</sup> s
		t = 8,3  ms; (60 Hz), sine	$V_R = 0 V$			425	A <sup>2</sup> s
C <sub>J</sub>	junction capacitance	$V_R = 400 V$ f = 1 MHz	$T_{VJ} = 25^{\circ}C$		13		рF
$P_{GM}$	max. gate power dissipation	t <sub>P</sub> = 30 μs	T <sub>C</sub> = 150°C			10	W
	- , ,	t <sub>P</sub> = 300 μs				5	W
$P_{GAV}$	average gate power dissipation					0.5	W
(di/dt) <sub>cr</sub>	critical rate of rise of current	T <sub>v,l</sub> = 150°C; f = 50 Hz re	epetitive, $I_{\tau} = 90 \text{ A}$			150	A/μs
701		$t_P = 200 \mu s; di_G/dt = 0.3 A/\mu s; -$	•				<u> </u>
		· · · · · · · · · · · · · · · · · · ·	on-repet., $I_T = 30 \text{ A}$			500	A/μs
(dv/dt) <sub>cr</sub>	critical rate of rise of voltage	V = <sup>2</sup> / <sub>3</sub> V <sub>DRM</sub>	T <sub>v.i</sub> = 150°C				V/µs
(	· ·	R <sub>GK</sub> = ∞; method 1 (linear volta	· ·				
V <sub>GT</sub>	gate trigger voltage	V <sub>D</sub> = 6 V	$T_{VJ} = 25^{\circ}C$			1.4	٧
- 61			$T_{VJ} = -40$ °C			1.7	٧
I <sub>GT</sub>	gate trigger current	$V_D = 6 \text{ V}$	$T_{VJ} = 25^{\circ}C$			30	mA
•G1	gate trigger carron.	<b>v</b> <sub>D</sub> − <b>3 v</b>	$T_{VJ} = -40$ °C			50	mA
V <sub>GD</sub>	gate non-trigger voltage	$V_D = \frac{2}{3} V_{DRM}$	$T_{VJ} = 150^{\circ}C$			0.2	V
	gate non-trigger current	V D − /3 V DRM	1// = 100 0			1	mA
I <sub>GD</sub>	latching current	t 10 up	T <sub>vJ</sub> = 25°C			90	
I <sub>L</sub>	raterning current	$t_p = 10 \mu s$ $I_G = 0.3 A; di_G/dt = 0.3 A/\mu s$				90	mA
I <sub>H</sub>	holding current	$V_D = 6 V R_{GK} = \infty$	$T_{VJ} = 25$ °C			60	mΑ
t <sub>gd</sub>	gate controlled delay time	$V_D = \frac{1}{2} V_{DRM}$	$T_{VJ} = 25$ °C			2	μs
•		$I_{G} = 0.3  A;  di_{G}/dt = 0.3  A/\mu s$	5				
tq	turn-off time	$V_{R} = 100 \text{ V}; I_{T} = 30 \text{ A}; V = \frac{2}{3}$			50		μs
ч		$di/dt = 10 A/\mu s dv/dt = 20 V$					



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Package	SMPD				Ratings			
Symbol	Definition	Conditions		min.	typ.	max.	Unit	
I <sub>RMS</sub>	RMS current	per terminal				100	Α	
T <sub>VJ</sub>	virtual junction temperature			-55		150	°C	
T <sub>op</sub>	operation temperature			-55		125	°C	
T <sub>stg</sub>	storage temperature			-55		150	°C	
Weight					8.5		g	
<b>F</b> <sub>c</sub>	mounting force with clip			40		130	N	
d <sub>Spp/App</sub>	creepage distance on surface   striking distance through air		terminal to terminal	1.6			mm	
$d_{\text{Spb/Apb}}$	creepage distance on surrac	ce   striking distance through an	terminal to backside	4.0			mm	
V <sub>ISOL</sub>	isolation voltage	t = 1 second	50/00 II	3000			٧	
		t = 1 minute	50/60 Hz, RMS; IISOL ≤ 1 mA				٧	



### Part description

C = Thyristor(SCR)

L = High Efficiency Thyristor

E = Semifast (up to 1200V)

90 = Current Rating [A]

UH = 3~ Rectifier Bridge, half-controlled (high-side)

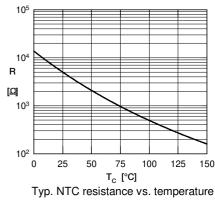
1200 = Reverse Voltage [V]

T = Thermistor \ Temperature sensor

LB = SMPD-B

Ordering	Ordering Number	Marking on Product	Delivery Mode	Quantity	Code No.
Standard	CLE90UH1200TLB-TUB	CLE90UH1200TLB	Tube	20	517456
Alternative	CLE90UH1200TLB-TRR	CLE90UH1200TLB	Tape & Reel	200	517463

ymbol	Definition		Condi	itions	min.	typ.	max.	Unit
<b>R</b> <sub>25</sub>	resistance		$T_{VJ} =$	25°	4.75	5	5.25	kΩ
B <sub>25/50</sub>	temperature coeffic	cient				3375		K
<b>→</b> (V <sub>0</sub> )	lent Circuits for	Thyristor	<b>7</b> 11	* on die l	ever		$T_{VJ} = 1$	
V <sub>0 max</sub>	threshold voltage	0.92						٧
R <sub>0 max</sub>	slope resistance *	10.5						mΩ







#### **Outlines SMPD**

