600V 75A Insulated Gate Bipolar Transistors

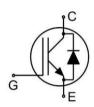
FEATURES

- V_{CES}=600V,I_C=75A(T_C=100°C)
- · Saturation pressure is reduced and the switching speed is fast
- Saturation pressure drops to a positive temperature coefficient
- High reliability and thermal stability, good parameter consistency

APPLICATIONS

- UPS
- Frequency transformer
- · Industrial power supply
- Inverter welder

SYMBOL





TO-247

ASSEMBLY MESSAGE

Product Name	Package	Packaging
BXE75T60HFHD	TO-247	Tube

ABSOLUTE MAXIMUM RATINGS (T_J=25°C unless otherwise noted)

Symbol	Parameter	Value	Units
Vces	Collector-Emitter Voltage	600	V
V _{GES}	Gate- Emitter Voltage	±20	V
,	Collector Current@Tc = 25 ° C	150	Α
lc	Collector Current @T _C = 100 ° C	75	Α
I _{Cplus}	Pulsed Collector Current, tp limited by Tjmax	300	А
l _F	Diode Continuous Forward Current @T _C = 25 °C	150	Α
	Diode Continuous Forward Current @T _C = 100 °C	75	Α
lғм	Diode Maximum Forward Current	300	Α
<u> </u>	Power Dissipation @ T _C = 25°C	390	W
P_D	Power Dissipation @ T _C = 100°C	156	W
T_J, T_{stg}	Operating Junction and Storage Temperature Range -55 to +150		°C
TL	Maximum Temperature for Soldering 270		°C

THERMAL CHARACTERISTICS

Parameter	Symbol	Max.	Units		
Thermal Resistance, Junction to case for IGBT	Rejc	0.32	°C/W		
Thermal Resistance, Junction to case for Diode	Rejc	0.8	°C/W		
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	45	°C/W		



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ELECTRICAL CHARACTERISTICS (T_J=25°C,unless otherwise Noted)

0	Dans western	-	Value				
Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units	
tatic Char	racteristics						
V _{(BR)CES}	Collector-Emitter Breakdown Voltage	V _{GE} =0V,I _{CE} =250uA	600			V	
I _{CES}	Collector-Emitter Leakage Current	V _{GE} =0V,V _{CE} =600V			1	mA	
I _{GES(F)}	Gate to Emitter Forward Leakage	V _{GE} =+20V,V _{CE} =0V			+250	nA	
I _{GES(R)}	Gate to Source Reverse Leakage	V _{GE} =-20V,V _{CE} =0V			-250	nA	
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C =75A,V _{GE} =15V		1.75	2.4	V	
$V_{\text{GE(th)}}$	Gate Threshold Voltage	Ic=250uA,VcE=VGE	4	5.5	7	V	
ynamic C	haracteristics						
Cies	Input Capacitance			11780			
Coes	Output Capacitance	V _{CE} =30V,V _{GE} =0V,		253		pF	
Cres	Reverse Transfer Capacitance	⊢f=1MHz		198			
Qg	Total Gate Charge			430			
Q _{ge}	Gate to Emitter Charge	V _{CE} =480V,I _C =75A,		180		nC	
Q _{gc}	Gate to Collector Charge	─V _{GE} =15V		150			
witching	Characteristics		•		'		
t _{d(ON)}	Turn-on Delay Time			96			
tr	Rise Time			92			
t _{d(OFF)}	Turn-Off Delay Time	V _{CE} =400V,I _C =75A,		308		ns	
t _f	Fall Time	–V _{GE} =15V, R _g =7Ω, _Inductive Load,		59			
Eon	Turn-On Switching Loss	T _J =25°C		4.6			
E _{off}	Turn-Off Switching Loss			2.1		mJ	
Ets	Total Switching Loss			6.7			
t _{d(ON)}	Turn-on Delay Time			90			
tr	Rise Time			95			
$t_{\text{d(OFF)}}$	Turn-Off Delay Time			336		ns	
t _f	Fall Time			55			
Eon	Turn-On Switching Loss	T _J =150°C		4.3			
E _{off}	Turn-Off Switching Loss			2.3		mJ	
Ets	Total Switching Loss			6.6			

ELECTRICAL CHARACTERISTICS OF THE DIODE(TJ=25°C, unless otherwise Noted)

Symbol	Parameter	Test Conditions	Rating			
			Min.	Тур.	Max.	Units
VF	Diode Forward Voltage	I _F =75A		1.8	2.4	٧
Trr	Reverse Recovery Time	I _F =75A,		65		ns
I _{RRM}	Diode Peak Reverse Recovery Current			5		Α
Q _{rr}	Reverse Recovery Charge	di/dt=200A/us		195		nC

Note: Pulse width ≤ 300µs, Duty cycle ≤ 2%



TYPICAL PERFORMANCE CHARACTERISTICS

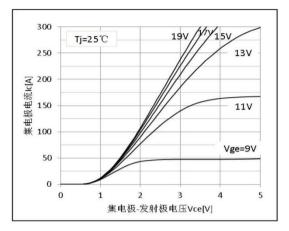


图 1 输出特性曲线

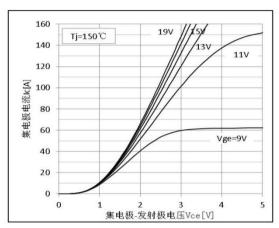


图 2 输出特性曲线

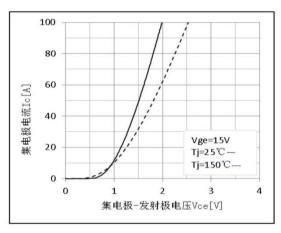


图 3 饱和压降特性

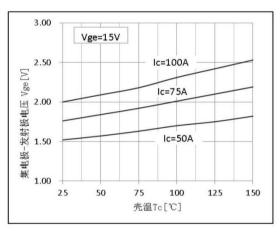


图 4 饱和压降温度特性

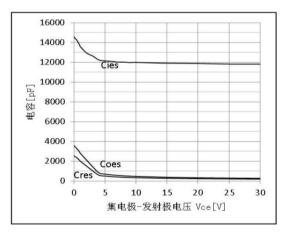


图 5 电容特性

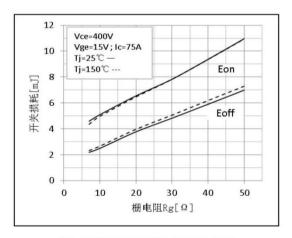


图 6 开关损耗-栅电阻特性曲线

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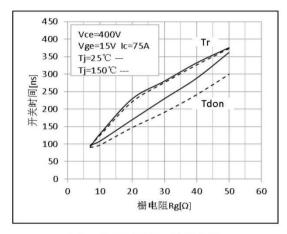


图 7 开通-栅电阻特性曲线

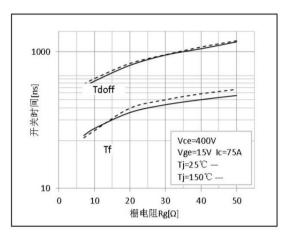


图 8 关断-栅电阻特性曲线

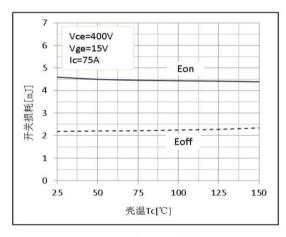


图 9 开关损耗温度特性

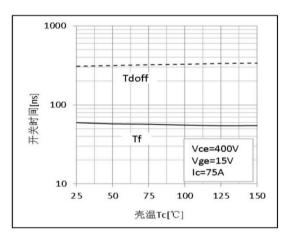


图 10 关断温度特性

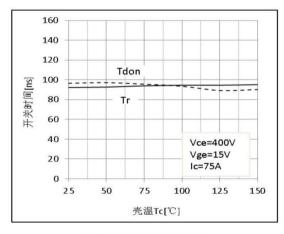


图 11 开通的温度特性

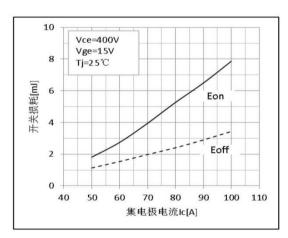


图 12 开关损耗的电流特性



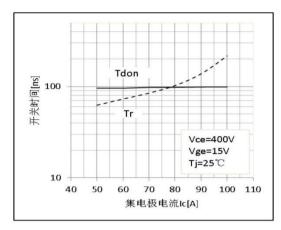


图 13 开通的电流特性

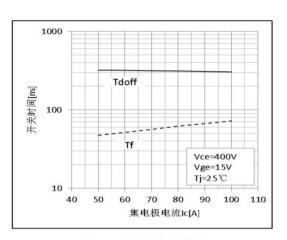


图 14 关断的电流特性

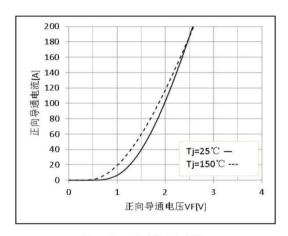


图 15 二极管正向特性

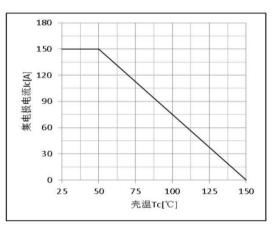


图 16 集电极电流温度特性

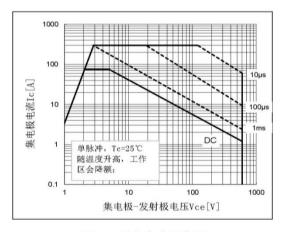


图 17 正向安全工作区

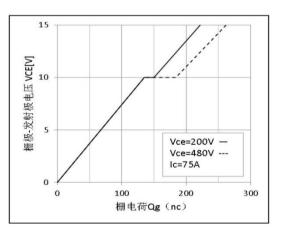
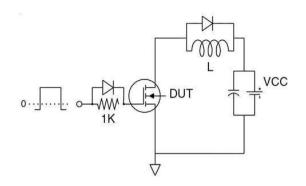


图 18 栅电荷特性



TEST CIRCUIT

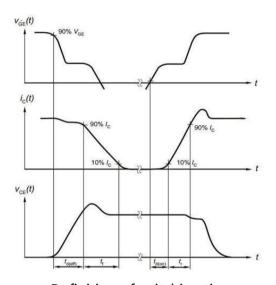


Gate Charge Test Circuit

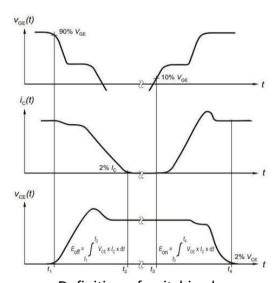
V_{DC} DUT (Diode) L C_{σ} C_{r} DUT (IGBT)

Switch Time Test Circuit

SWITCHING CHARACTERISTICS

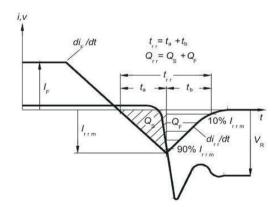


Definition of switching times



Definition of switching losses

Version: 1.0



Definition of diode switching characteristics



Revision history

Document revision history

Date	Revision	Changes
20-Oct-2021	1.0	First release



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