HV582
96-Channel High-Voltage Driver IC Evaluation Board
User's Guide

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USA

This declaration of conformity is issued by the manufacturer.

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Signed for and on behalf of Microchip Technology Inc. at Chandler, Arizona, USA

Derek Carlson
VP Development Tools

12-Sep-14
Date

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Preface

NOTICE TO CUSTOMERS

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Documents are identified with a "DS" number. This number is located on the bottom of each page, in front of the page number. The numbering convention for the DS number is "DSXXXXXXXXA", where "XXXXXXXX" is the document number and "A" is the revision level of the document.

For the most up-to-date information on development tools, see the MPLAB® IDE online help. Select the Help menu, and then Topics to open a list of available online help files.

INTRODUCTION

This chapter contains general information that will be useful to know before using the HV582 96-Channel High-Voltage Driver IC Evaluation Board. Items discussed in this chapter include:

- Document Layout
- Conventions Used in this Guide
- Warranty Registration
- Recommended Reading
- The Microchip Web Site
- Development Systems Customer Change Notification Service
- Customer Support
- Document Revision History

DOCUMENT LAYOUT

This document describes how to use the HV582 96-Channel High-Voltage Driver IC Evaluation Board as a development tool. The manual layout is as follows:

- **Chapter 1. "Product Overview"** – Important information about the HV582 96-Channel High-Voltage Driver IC Evaluation Board.
- **Chapter 2. "Installation and Operation"** – This chapter includes a detailed description of each function of the HV582 96-Channel High-Voltage Driver IC Evaluation Board and instructions on how to use it.
- **Appendix A. "Schematic and Layouts"** – Shows the schematic and layout diagrams for the HV582 96-Channel High-Voltage Driver IC Evaluation Board.
- **Appendix B. "Bill of Materials (BOM)"** – Lists the parts used to build the HV582 96-Channel High-Voltage Driver IC Evaluation Board.
- **Appendix C. "HV582 Typical Waveforms"** – Describes the various plots and waveforms for the HV582 96-Channel High-Voltage Driver IC Evaluation Board.

CONVENTIONS USED IN THIS GUIDE

This manual uses the following documentation conventions:

DOCUMENTATION CONVENTIONS

Description	Represents	Examples
Arial font:		
Italic characters	Referenced books	*MPLAB® IDE User's Guide*
	Emphasized text	...is the *only* compiler...
Initial caps	A window	the Output window
	A dialog	the Settings dialog
	A menu selection	select Enable Programmer
Quotes	A field name in a window or dialog	"Save project before build"
Underlined, italic text with right angle bracket	A menu path	*File>Save*
Bold characters	A dialog button	Click **OK**
	A tab	Click the **Power** tab
N'Rnnnn	A number in verilog format, where N is the total number of digits, R is the radix and n is a digit.	4'b0010, 2'hF1
Text in angle brackets < >	A key on the keyboard	Press <Enter>, <F1>
Courier New font:		
Plain Courier New	Sample source code	`#define START`
	Filenames	`autoexec.bat`
	File paths	`c:\mcc18\h`
	Keywords	`_asm, _endasm, static`
	Command-line options	`-Opa+, -Opa-`
	Bit values	`0, 1`
	Constants	`0xFF, 'A'`
Italic Courier New	A variable argument	*`file`*`.o`, where *`file`* can be any valid filename
Square brackets [ ]	Optional arguments	`mcc18 [options]` *`file`* `[options]`
Curly brackets and pipe character: { | }	Choice of mutually exclusive arguments; an OR selection	`errorlevel {0|1}`
Ellipses...	Replaces repeated text	`var_name [, var_name...]`
	Represents code supplied by user	`void main (void) { ... }`

WARRANTY REGISTRATION

Please complete the enclosed Warranty Registration Card and mail it promptly. Sending in the Warranty Registration Card entitles users to receive new product updates. Interim software releases are available at the Microchip web site.

RECOMMENDED READING

This user's guide describes how to use the HV582 96-Channel High-Voltage Driver IC Evaluation Board. Other useful documents are listed below. The following Microchip documents are available and recommended as supplemental reference resources.

- **HV582 Data Sheet – *"96-Channel Serial to Parallel Converter with Push-Pull Outputs"* (DS20005455)**.

Additional documentation (including schematic and code samples) is available under the PIC32 Starter Kit (DM320001) section on the website.

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- **Emulators** – the latest information on Microchip in-circuit emulators. This includes the MPLAB REAL ICE™ and MPLAB ICE 2000 in-circuit emulators.
- **In-Circuit Debuggers** – the latest information on the Microchip in-circuit debuggers. This includes MPLAB ICD 3 in-circuit debuggers and PICkit™ 3 debug express.
- **MPLAB IDE** – the latest information on Microchip MPLAB IDE, the Windows Integrated Development Environment for development systems tools. This is focused on the MPLAB IDE, MPLAB IDE Project Manager, MPLAB Editor, and MPLAB SIM simulator, as well as general editing and debugging features.
- **Programmers** – the latest information on Microchip programmers. These include production programmers such as MPLAB REAL ICE in-circuit emulator, MPLAB ICD 3 in-circuit debugger, and MPLAB PM3 device programmers. Also included are nonproduction development programmers such as PICSTART Plus and PICkit 2 and 3.

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- Distributor or Representative
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- Field Application Engineer (FAE)
- Technical Support

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Technical support is available through the web site at:
http://www.microchip.com/support

DOCUMENT REVISION HISTORY

Revision A (December 2015)

- Initial release of this document.

Chapter 1. Product Overview

1.1 INTRODUCTION

This chapter covers the following topics:

- HV582 Device Overview
- HV582 Evaluation Board Overview
- HV582 Evaluation Board Kit Contents

1.2 HV582 DEVICE OVERVIEW

The HV582 is a unipolar 96-channel, low-voltage serial to high-voltage parallel converter with push-pull outputs, dedicated to printer driver and plasma display applications. The device has been designed for applications requiring high channel count and high output voltage swing (0-80V) with current sinking and sourcing capabilities of ±75 mA.

The device consists of six parallel 16-bit shift registers, a 96-bit latch, and 96 high-voltage outputs. The 16-bit shift registers can operate up to a 30 MHz speed rate, allowing 180 MHz data rates due to the parallel arrangement. The parallel arrangement of the registers permits six times the speed of a single register (30 MHz x 6 16-bit shift registers), providing a fast update rate for the 96 output channels. Data flow can be shifted from a clockwise to a counterclockwise direction via the DIR pin. All high-voltage outputs can be forced to a low-level, high-level or high-impedance state (high Z) or to alter their polarity state through the $\overline{OL}$, $\overline{OH}$, OE and POL pins, respectively.

1.3 HV582 EVALUATION BOARD OVERVIEW

The HV582 96-Channel High-Voltage Drive IC Evaluation Board facilitates quick implementation for display and printer driver applications due to its flexible input/output connection interface.

The evaluation board is designed to be operated with the Microchip PIC32 Starter Kit (DM320001), or with a generic signal pattern generator via the dedicated J5 pin header connector (see Figure 1-1).

There are 32 test-point pads corresponding to the first and the last 16-bit registers (D1A/D1B and D6A/D6B) that control the high-voltage outputs: $HV_{OUT}1$ to 16 and $HV_{OUT}81$ to 96. Ground pads are provided along with the test-point pads to facilitate the analysis of the high-voltage output channels. Refer to Figure 1-1 and to the HV582 Data Sheet.

All output channels are available via a 160-position, high-density array, female connector, where only 96 positions are used (refer to Evaluation Board – Schematic in Appendix A. "Schematic and Layouts" for more details).

The HV582 contains six parallel 16-bit shift registers. Each 16-bit shift register features two ports that can be set as either inputs or outputs by controlling the DIR pin, as shown in the following list:

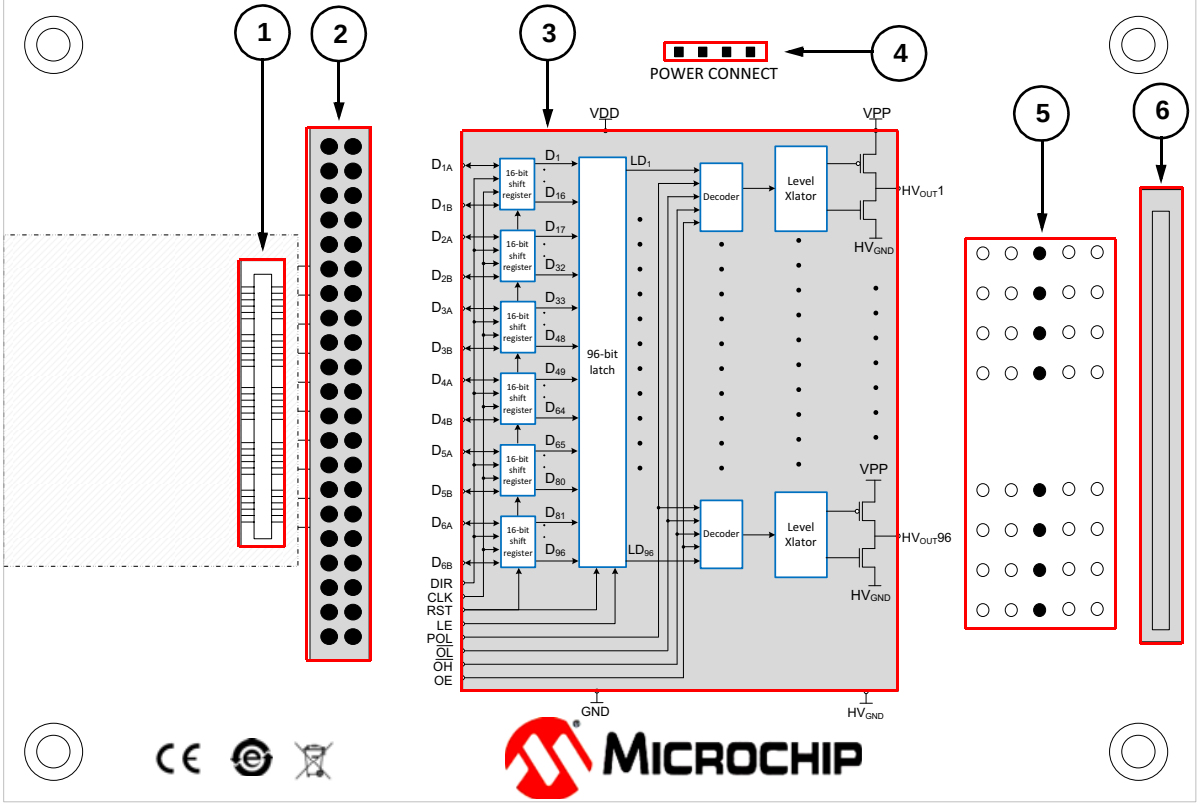
- setting the DIR pin high configures DnB pins as inputs and DnA pins as outputs (counterclockwise input data)
- setting the DIR pin low enables DnA pins as inputs and DnB pins as outputs (clockwise input data)

Shunt (zero Ohm) resistors are placed right after the pin header connectors (D1A to D6A) to facilitate the adjustment of the input registers in any desired manner.

Note: The HV582 96-Channel High-Voltage Drive IC Evaluation Board comes equipped with shunt resistors only on the DnA pins. Shunt resistors are not installed on the DnB pins.

1.3.1 HV582 Evaluation Board Block Diagram

Figure 1-1 presents the HV582 96-Channel High-Voltage Drive IC Evaluation Board block diagram with the main sections labeled and explained.



Legend:

1 = FX10A-120S/12-SV(71) connector (J2) for PIC32 Starter Kit (DM320001, not included)

2 = Header connector for generic signal pattern generator (J5, not installed)

3 = HV582 169-Ball TFBGA package

4 = Power connection pin header.

5 = 32 $HV_{OUT}n$ test points and ground pads.

6 = 160-position, high-density array, female connector.

FIGURE 1-1: *HV582 96-Channel High-Voltage Drive IC Evaluation Board Block Diagram.*

1.4 HV582 EVALUATION BOARD KIT CONTENTS

The HV582 96-Channel High-Voltage Drive IC Evaluation Board Kit includes:

- HV582 96-Channel High-Voltage Drive IC Evaluation Board (ADM00697)
- Important Information Sheet

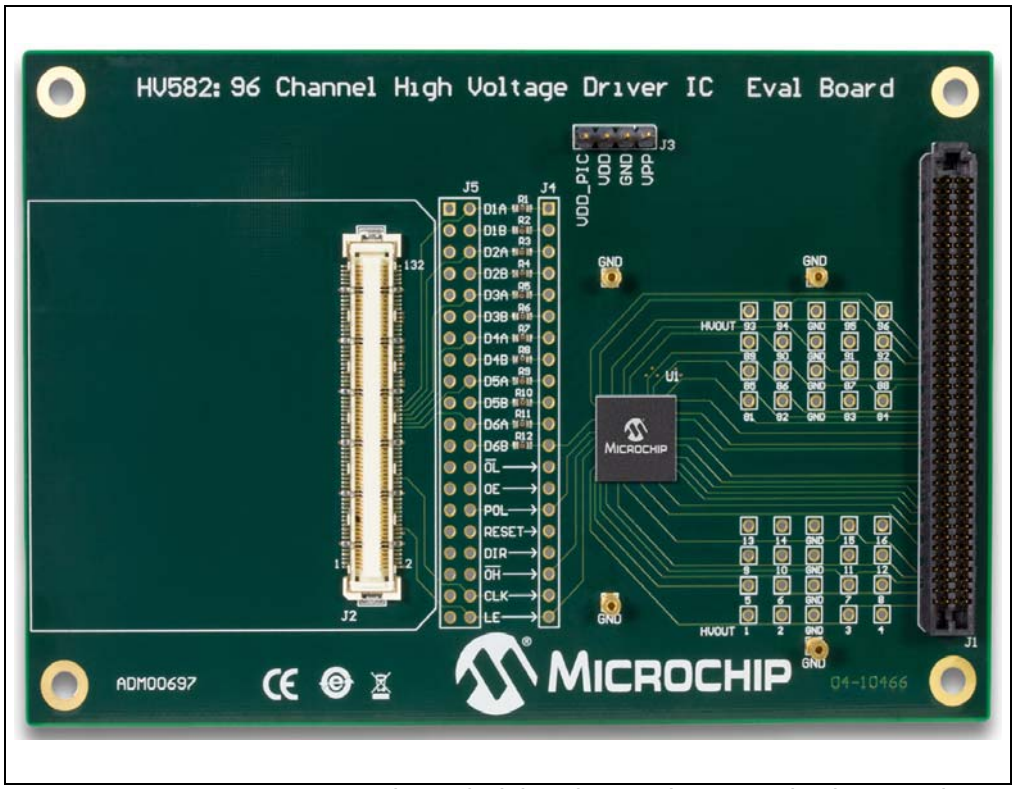


FIGURE 1-2: *HV582 96-Channel High-Voltage Drive IC Evaluation Board – Top View.*

NOTES:

Chapter 2. Installation and Operation

2.1 GETTING STARTED

The HV582 96-Channel High-Voltage Driver IC Evaluation Board is fully assembled and tested.

2.1.1 Tools Required for Operation

- A low DC power supply for V_{DD} (and VDD_PIC) that can produce 5V
- A high DC power supply for V_{PP} with a voltage range up to +80V
- A logic signal driver: PIC32 Starter Kit (DM320001) or a generic signal pattern generator
- An oscilloscope and/or a multimeter to observe waveforms and measure electrical parameters

2.2 SETUP PROCEDURE

To prepare the HV582 96-Channel High-Voltage Driver IC Evaluation Board for operation, the following steps must be followed:

WARNING

Read the *HV582 96-Channel High-Voltage Driver IC Evaluation Board User's Guide* (this document) fully before proceeding to board setup.

1. Connect the power supplies by following the steps indicated by this power-up sequence:
    a) Connect GND
    b) Apply V_{DD}
    c) Connect VDD_PIC (if PIC32 Starter Kit is mounted and used) (1)
    d) Set logic input signals to a known state
    e) Apply VPP

Note 1: If the PIC32 Starter Kit is used and connected to the USB debug cable there is no need to power the VDD_PIC pin.

Note: To power down the board, follow the reverse order of the power-up sequence.

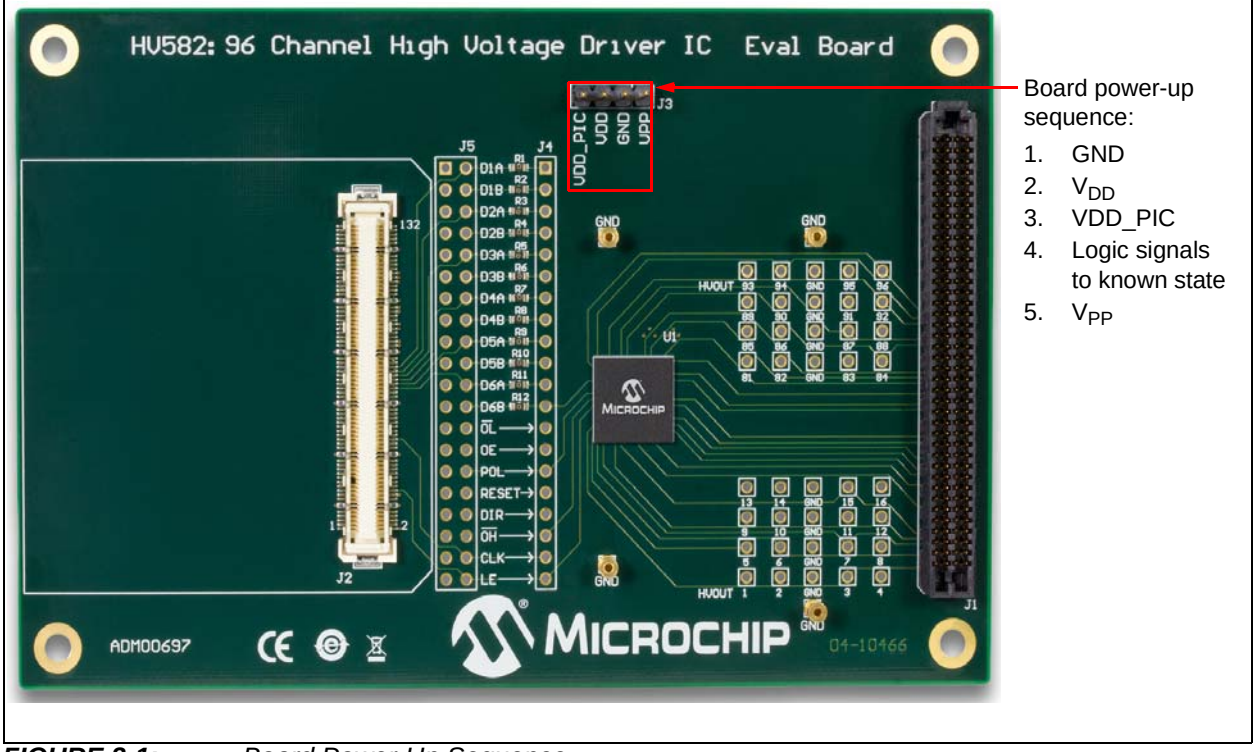


FIGURE 2-1: *Board Power-Up Sequence.*

Step 2. Apply the voltage settings by following the sequence shown in Table 2-1.

TABLE 2-1: VOLTAGE SETTINGS

Sequence Number	Terminal Name	Description
1	GND	Ground
2	V_{DD}	5.0V, logic power supply for HV582
3	VDD_PIC	5.0V, power supply for PIC32 Starter Kit (1)
4	V_{PP}	+10V to +80V, high-voltage power supply for all $HV_{OUT}n$

Note 1: If the PIC32 Starter Kit is used and not connected to the USB debug cable.

After following the power-up sequence and applying the voltage settings correctly, the evaluation board is ready to operate.

The HV582 96-Channel High-Voltage Driver IC Evaluation Board can be driven by a generic signal pattern generator or by the suggested PIC32 Starter Kit (DM320001). Section 2.3 "Using The Evaluation Board with a Generic Signal Pattern Generator" and Section 2.4 "Using The Evaluation Board with the PIC32 Starter Kit" elaborate on the operation and evaluation process in detail.

2.3 USING THE EVALUATION BOARD WITH A GENERIC SIGNAL PATTERN GENERATOR

2.3.1 Introduction

The HV582 96-Channel High-Voltage Driver IC Evaluation Board can be operated by a generic logic signal pattern generator, or by any signal driver, via the J5 pin header connector (pin header not installed).

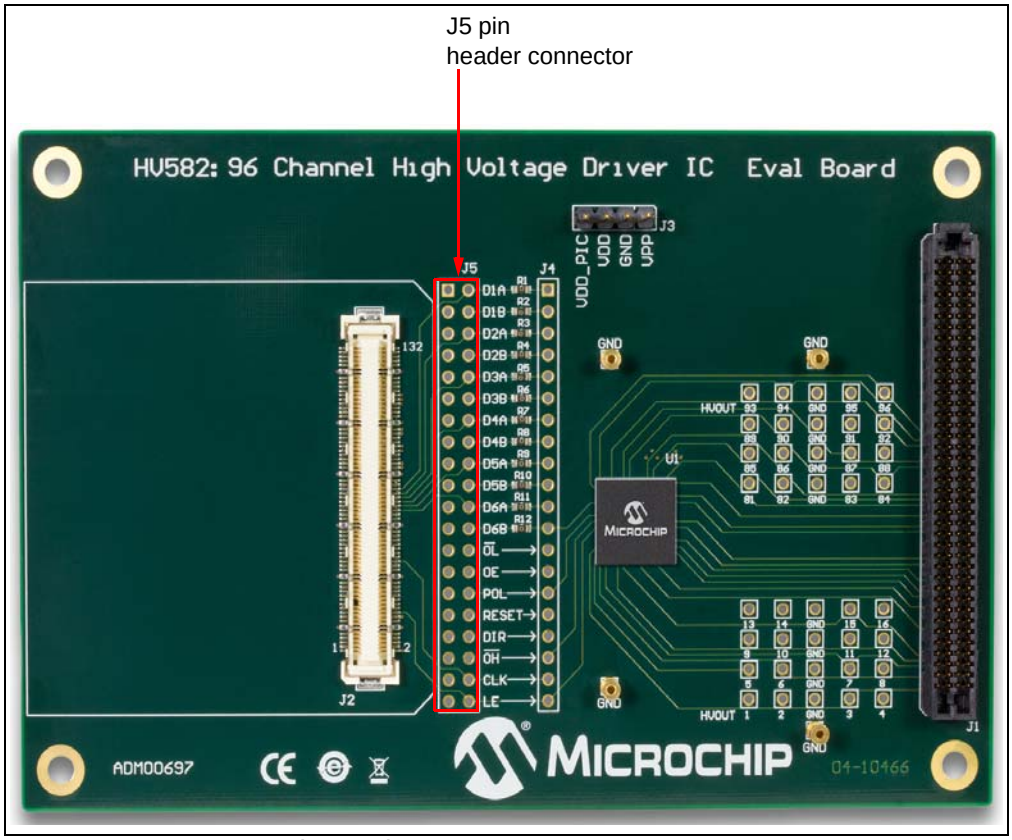


FIGURE 2-2: *J5 Pin Header Connector.*

The user must ensure the minimum DC and AC electrical parameters are achieved by the signal pattern generator. For more information, refer to the HV582 data sheet (DS20005455).

2.3.2 Operating the Evaluation Board

When operating the evaluation board with a generic logic signal pattern generator, the VDD_PIC pin should not be powered on. The VDD_PIC pin is an optional power pin used only for the PIC32 Starter Kit (DM320001).

In case a read operation is required for the signal driver, it is recommended to operate the pattern generator at the same voltage potential as the evaluation board (V_{DD}). If, for instance, the signal pattern generator is operating at a lower voltage (e.g. 3.3V) than that of the evaluation board (5.0V), this will cause the ESD protection diodes of the signal generator to forward bias and possibly damage the board.

WARNING
If a read-back operation is required by the generic signal pattern generator (or by any signal driver), the operational voltage level of the logic signals must be equal to the voltage potential of the evaluation board (V_{DD}).

Figure 2-3 presents the logic diagram of a sample data transmission for testing and understanding the functionality of the HV582.

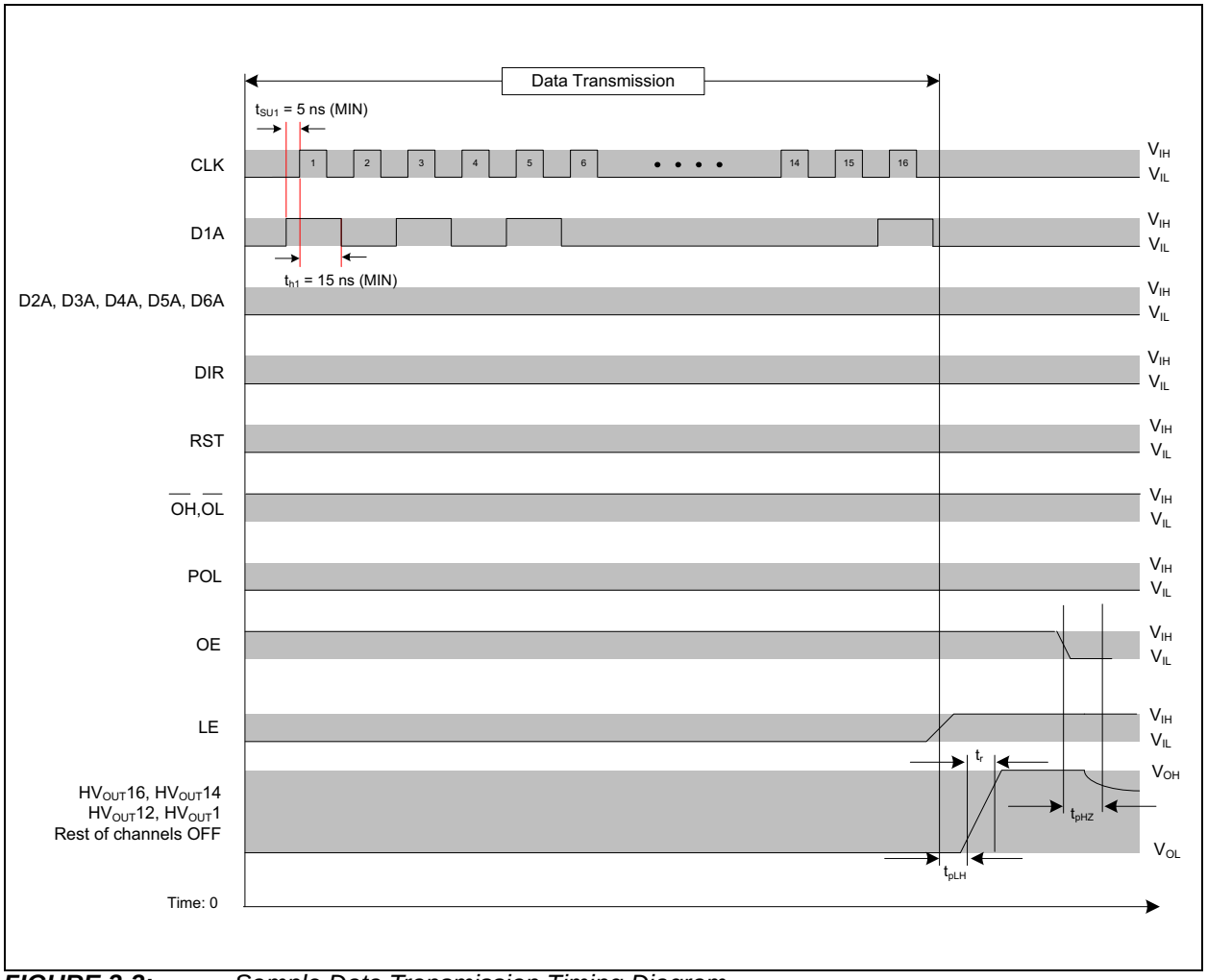


FIGURE 2-3: *Sample Data Transmission Timing Diagram.*

2.4 USING THE EVALUATION BOARD WITH THE PIC32 STARTER KIT

2.4.1 Introduction

The HV582 96-Channel High-Voltage Driver IC Evaluation Board can be operated by the Microchip PIC32 Starter Kit (DM320001) via the FX10A-120S/12-SV(71) connector, J2 (see Figure 2-4).

Note: Several PIC32 Starter kits might be compatible with the HV582 Evaluation Board, but only the DM320001 is supported with code.

2.4.2 Software Requirements

In order to operate the PIC32 Starter Kit, the MPLAB X IDE software and the MPLAB XC32 Compiler must be installed in the user's system. Software and compilers are available for download on the Microchip website at www.microchip.com.

For detailed information regarding the installation and usage of MPLAB X IDE software, refer to *MPLAB X IDE User's Guide* (DS50002027).

2.4.3 Connecting the PIC32 Starter Kit to the HV582 Evaluation Board

Mount the PIC32 Starter Kit (DM320001) onto the J2 connector before powering up the board. Follow the power-up sequence and apply the voltage settings indicated in Section 2.2 "Setup Procedure".

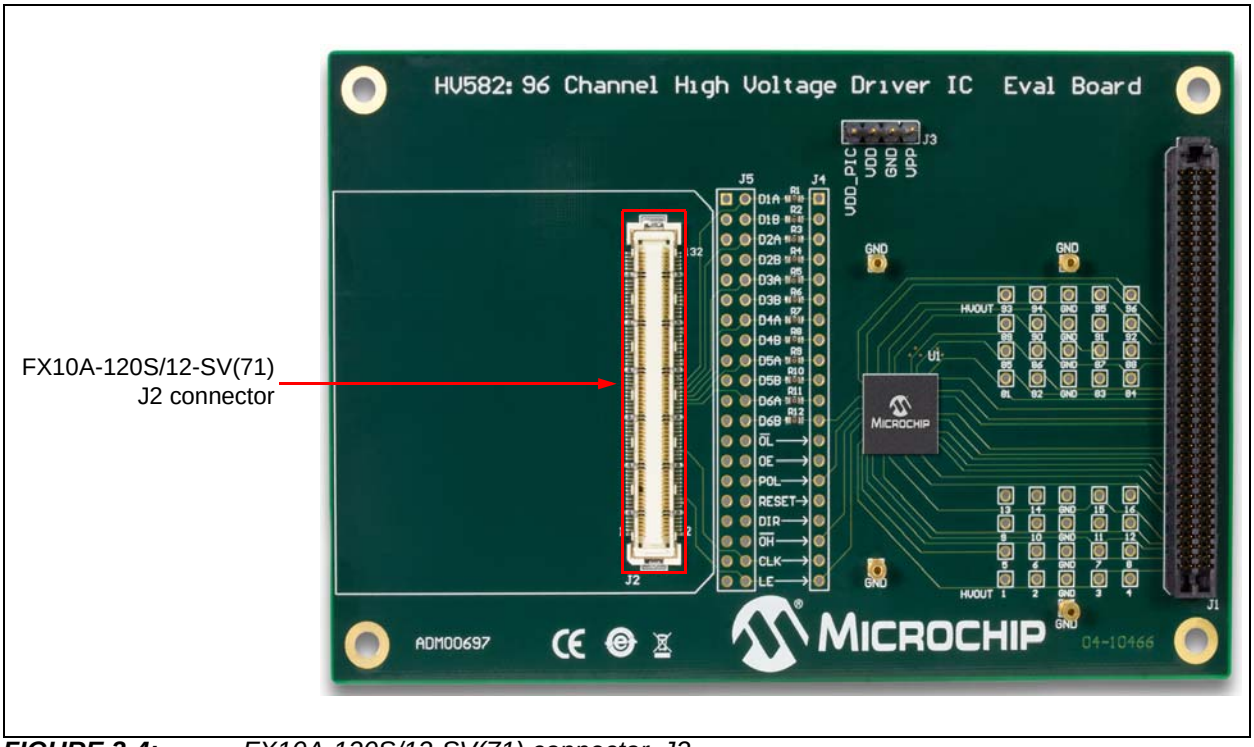


FIGURE 2-4: *FX10A-120S/12-SV(71) connector, J2.*

2.4.4 PIC32 Starter Kit Software Code

The source code for driving the HV582 96-Channel High-Voltage Driver IC Evaluation Board, `PIC32_HV582.X`, is available for download on the Microchip web site. The objective of the code is to provide a starting platform for using the HV582 evaluation board.

The code flowchart is presented in Figure 2-5.

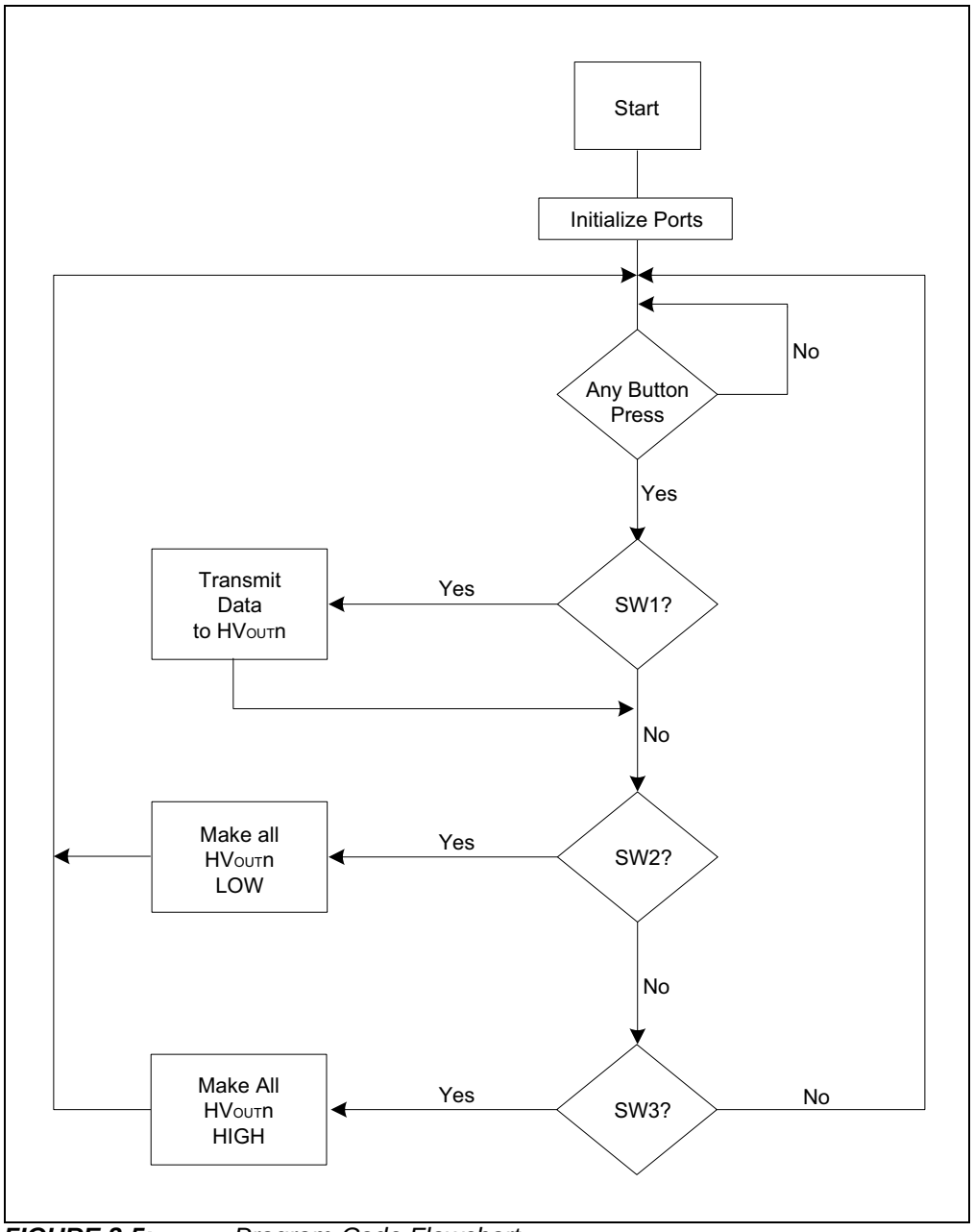


FIGURE 2-5: *Program-Code Flowchart.*

Table 2-2 provides a summary of the software code operation.

TABLE 2-2: SOFTWARE CODE OPERATION (1)

Switch	Description
SW1	LED 1 turns on, data is sent to input registers D1A, D2A, D3A, D4A, D5A and D6A
SW2	LED 2 turns on, makes all ($HV_{OUT}n$) High-Voltage channels low (GND)
SW3	LED 3 turns on, makes all ($HV_{OUT}n$) High-Voltage channels high (VPP)

Note 1: Push-button switches are located on the PIC32 Starter Kit, see Figure 2-6 and Section 2.4.6 “Modifying the Control Signals and Data in the Code”.

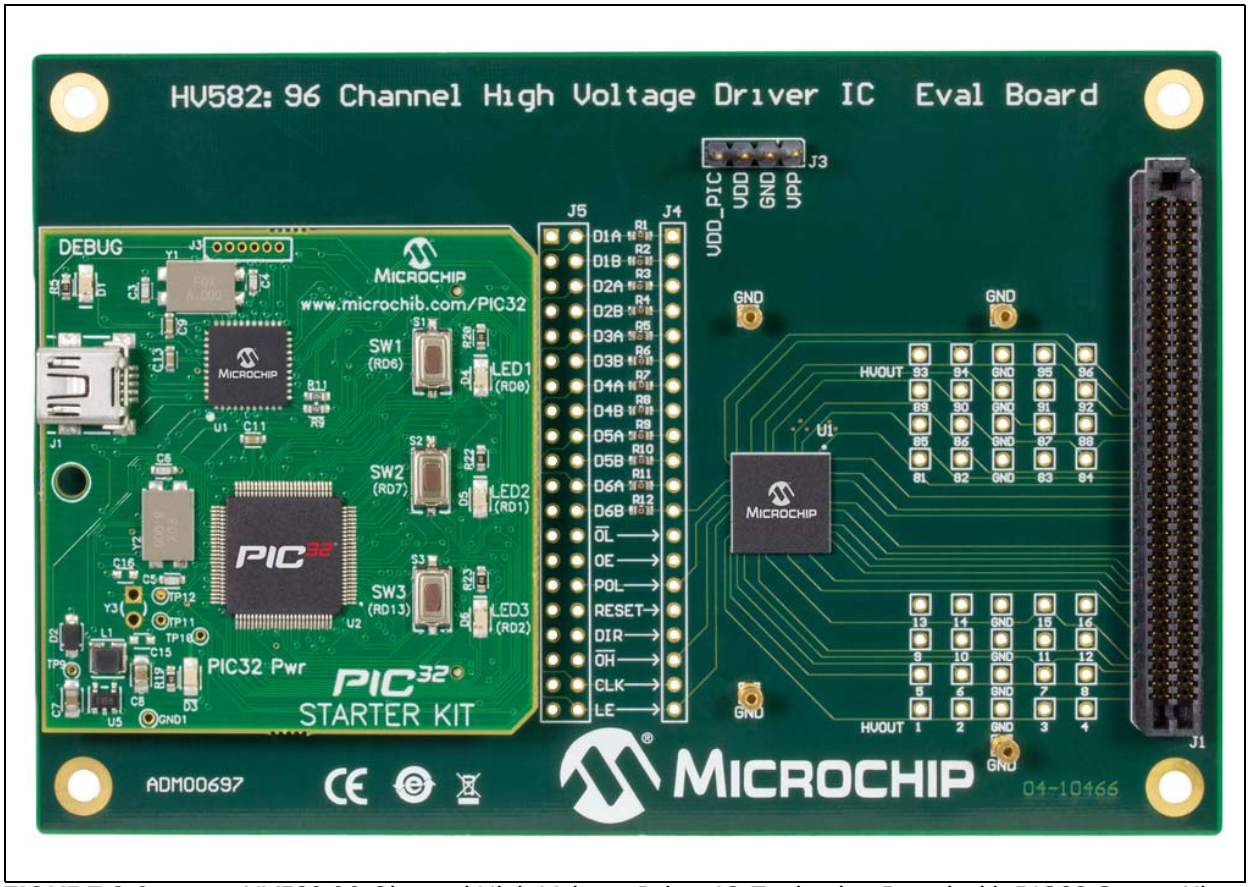


FIGURE 2-6: *HV582 96-Channel High-Voltage Driver IC Evaluation Board with PIC32 Starter Kit (DM320001) connected on J2 – Top View.*

2.4.5 Programming the PIC32 Starter Kit

This section assumes that MPLAB X IDE software and the MPLAB XC32 Compiler are installed on the user’s system and the PIC32 Starter Kit is connected to the PC via the USB debug cable.

To load the `PIC32_HV582.X` code, follow these steps:

1. Open MPLAB X IDE and then `PIC32_HV582.X` program code.
2. Select **Clean and Build Project** icon ( ). Ignore warning messages.
3. Select **Make and Program Device** icon ( ).

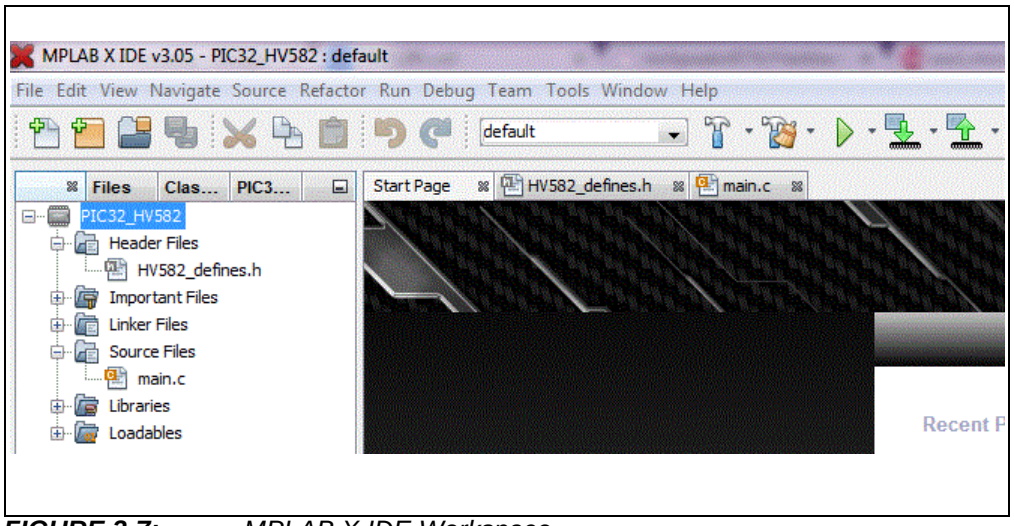


FIGURE 2-7: *MPLAB X IDE Workspace.*

2.4.6 Modifying the Control Signals and Data in the Code

To modify the control signals and the data to be sent to the HV582 evaluation board, open the `HV582_defines.h` file located under the Header Files folder.

To change the control signals, LE, OE, DIR, RST, $\overline{OH}$, $\overline{OL}$ and POL, locate the section in the file labeled `INPUT CONTROL SIGNALS` (see Figure 2-8) and modify accordingly. Data transmission is controlled by pressing the push-button switches on the PIC32 Starter Kit (see Figure 2-6). The user can select one of three available cases:

- `HV582_CASE_1_ENABLE` and `HV582_CASE_1_DISABLE` control the data transmission; selectable by pressing SW1
- `HV582_CASE_2_ENABLE` makes all HV_{OUT}n channels low; selectable by pressing SW2
- `HV582_CASE_3_ENABLE` makes all HV_{OUT}n channels high; selectable by pressing SW3

```
//============================================================//
//------------------------------------------------------------//
//-----------      INPUT CONTROL SIGNALS          ------------//
//------------------------------------------------------------//
//============================================================//
#define HV582_CASE_1_ENABLE     0b0000000110010 //(LSB)LE = LOW,  OE = HIGH, DIR  = LOW, RST = LOW, OH = HIGH, OL = HIGH, POL = LOW
#define HV582_CASE_1_DISABLE    0b0000000110011 //(LSB)LE = HIGH, OE = HIGH, DIR  = LOW, RST = LOW, OH = HIGH, OL = HIGH, POL = LOW
#define HV582_CASE_2_ENABLE     0b0000000010010 //(LSB)LE = LOW,  OE = HIGH, DIR  = LOW, RST = LOW, OH = HIGH, OL = LOW,  POL = LOW
#define HV582_CASE_3_ENABLE     0b0000000100010 //(LSB)LE = LOW,  OE = HIGH, DIR  = LOW, RST = LOW, OH = LOW,  OL = HIGH, POL = LOW
```

FIGURE 2-8: *Input Control Signals: LE, OE, DIR, RST, $\overline{OH}$, $\overline{OL}$ and POL.*

To change the data to be sent to the input registers of the HV582, scroll at the bottom of the file and locate the section `DATA TO SEND TO REGISTERS` and change the values as desired. See Figure 2-9.

The HV582 consists of six 16-bit shift registers. The distribution of the input/output pins to registers is detailed in Table 2-3.

TABLE 2-3: PIN-TO-REGISTER ASSIGNMENT

16-bit shift registers	Corresponding input/output pins
first register	D1A/D1B
second register	D2A/D2B
third register	D3A/D3B
fourth register	D4A/D4B
fifth register	D5A/D5B
sixth register	D6A/D6B

In the code, DATA_1 corresponds to the first bit of data to be send to all of the six registers. The Least Significant Bit (LSB) corresponds first to D1A/D1B, second to D2A/D2B, third to D3A/D3B, fourth to D4A/D4B, fifth to D5A/D5B, and sixth to D6A/D6B.

```
//=================================================================//
//-----------------------------------------------------------------//
//-----------        DATA TO SEND TO REGISTERS     ----------------//
//-----------------------------------------------------------------//
//=================================================================//
#define DATA_1  0b000001 //(LSB)D1A/B-> 1, D2A/B-> 0, D3A/B-> 0, D4A/B-> 0, D5A/B-> 0, D6A/B-> 0
#define DATA_2  0b100000
#define DATA_3  0b011111
#define DATA_4  0b111110
#define DATA_5  0b011110
#define DATA_6  0b111110
#define DATA_7  0b011110
#define DATA_8  0b111110
#define DATA_9  0b011110
#define DATA_10 0b111110
#define DATA_11 0b011110
#define DATA_12 0b101110
#define DATA_13 0b011110
#define DATA_14 0b111110
#define DATA_15 0b000000
#define DATA_16 0b101010
```

FIGURE 2-9: *Data to Send to the HV582 Input Registers.*

The code flow is specified in the `main.c` file (see Figure 2-7) located under the Source Files folder.

NOTICE

By default, the code provided sends data into the DnA input registers of the HV582.

WARNING

The PIC32 Starter Kit (DM320001) cannot be used to read data back from the HV582 Evaluation Board because of the difference in logic voltage level: 3.3V (PIC32) vs 5.0V (V_{DD}). If used, this will cause the ESD protection diodes in the PIC32 board to forward bias and possibly damage the board.

NOTES:

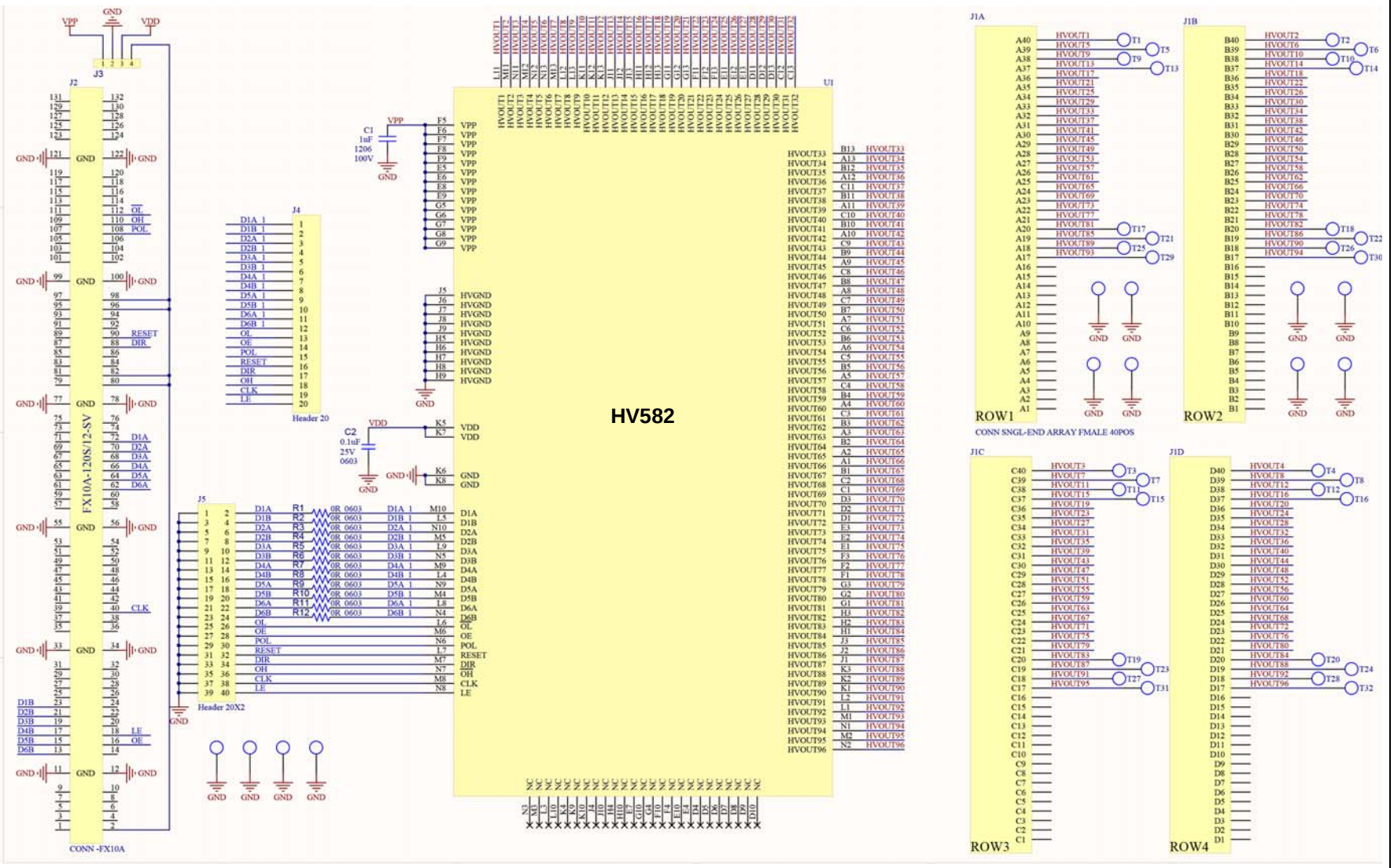
Appendix A. Schematic and Layouts

A.1 INTRODUCTION

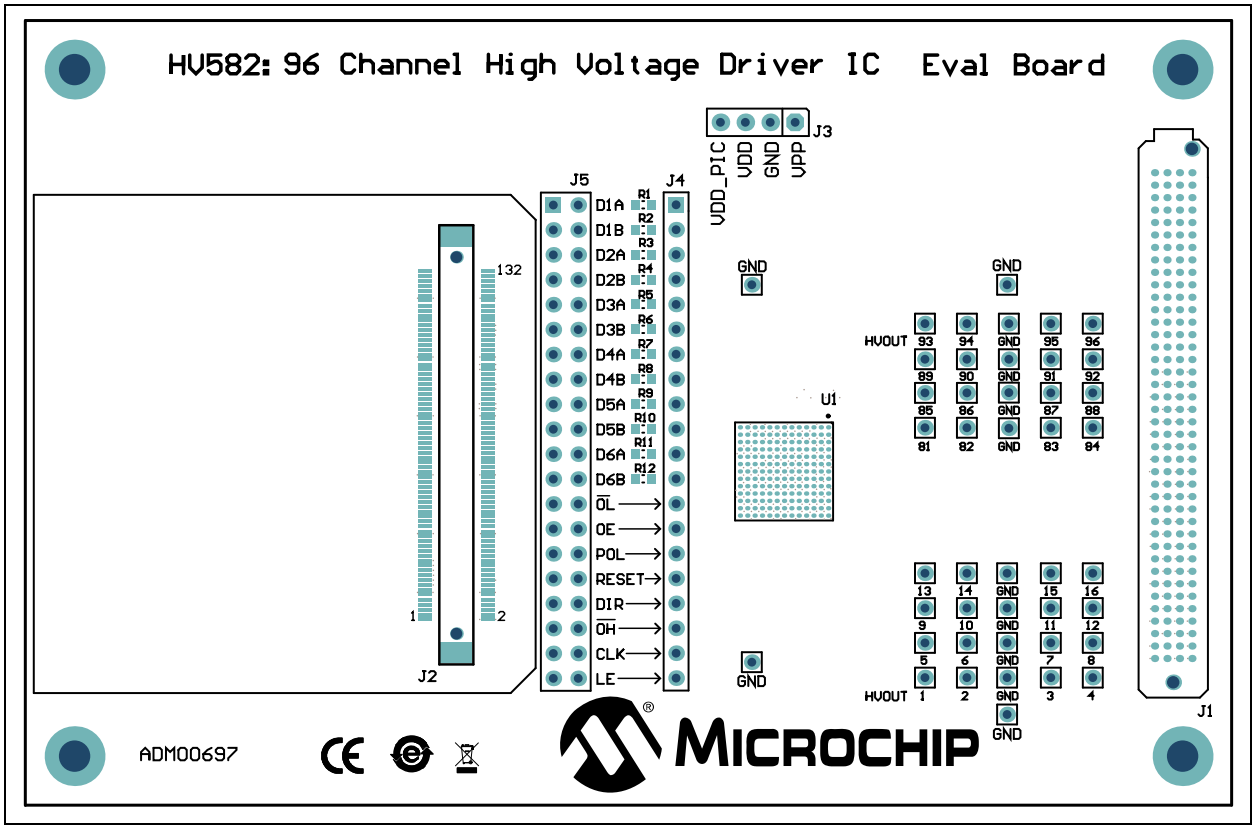
This appendix contains the following schematics and layouts for the HV582 96-Channel High-Voltage Drive IC Evaluation Board.

- Evaluation Board – Schematic
- Evaluation Board – Top Silk
- Evaluation Board – Top Copper and Silk
- Evaluation Board – Top Copper
- Evaluation Board – Ground Plane
- Evaluation Board – Mid Layer 1
- Evaluation Board – Mid Layer 2
- Evaluation Board – Power Plane
- Evaluation Board – Bottom Copper
- Evaluation Board – Bottom Copper and Silk
- Evaluation Board – Bottom Silk

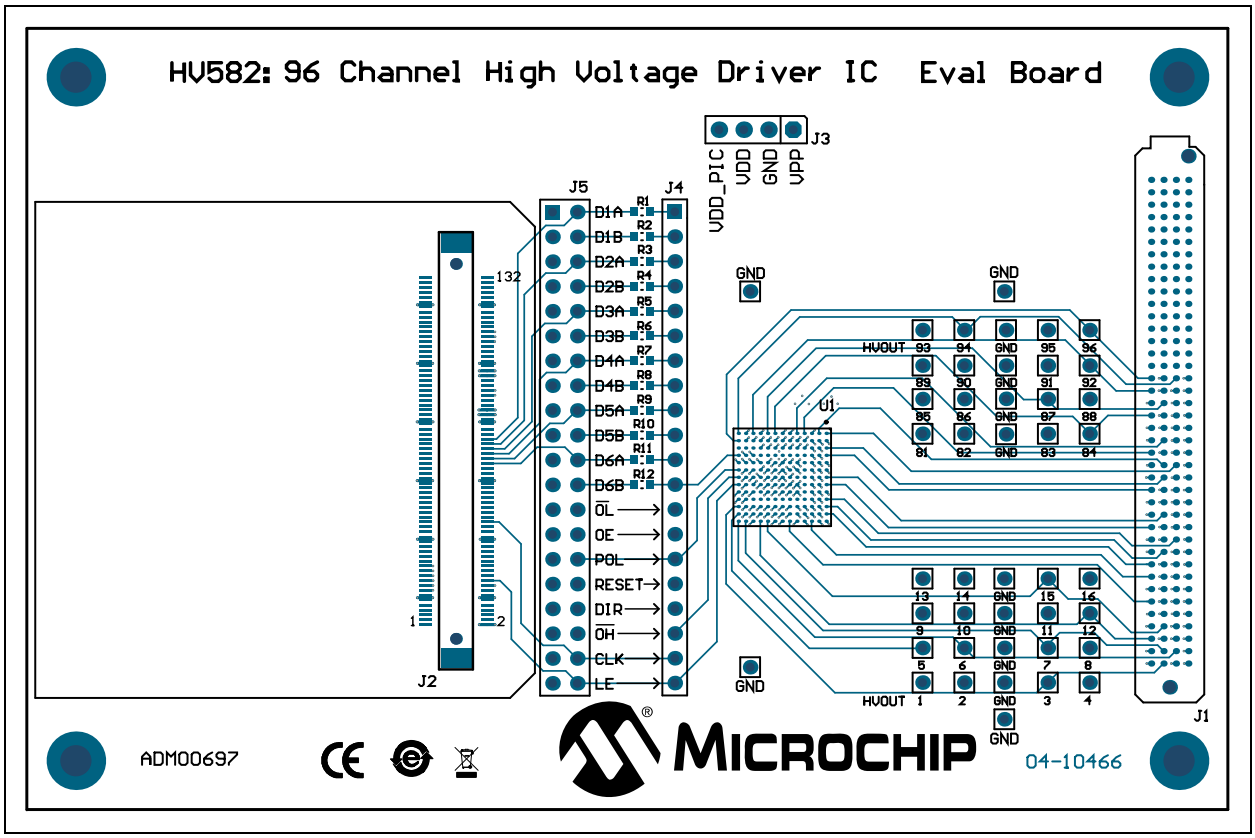
A.2 EVALUATION BOARD – SCHEMATIC



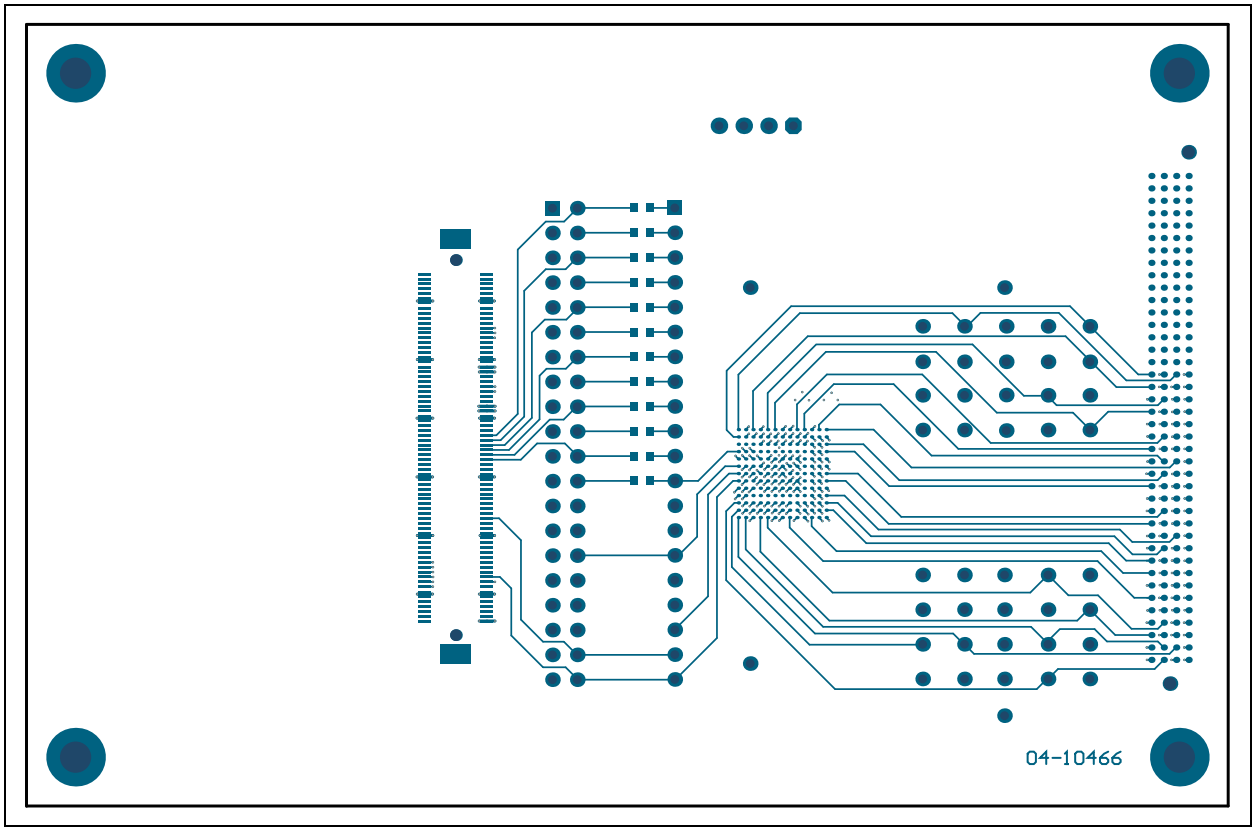
A.3 EVALUATION BOARD – TOP SILK



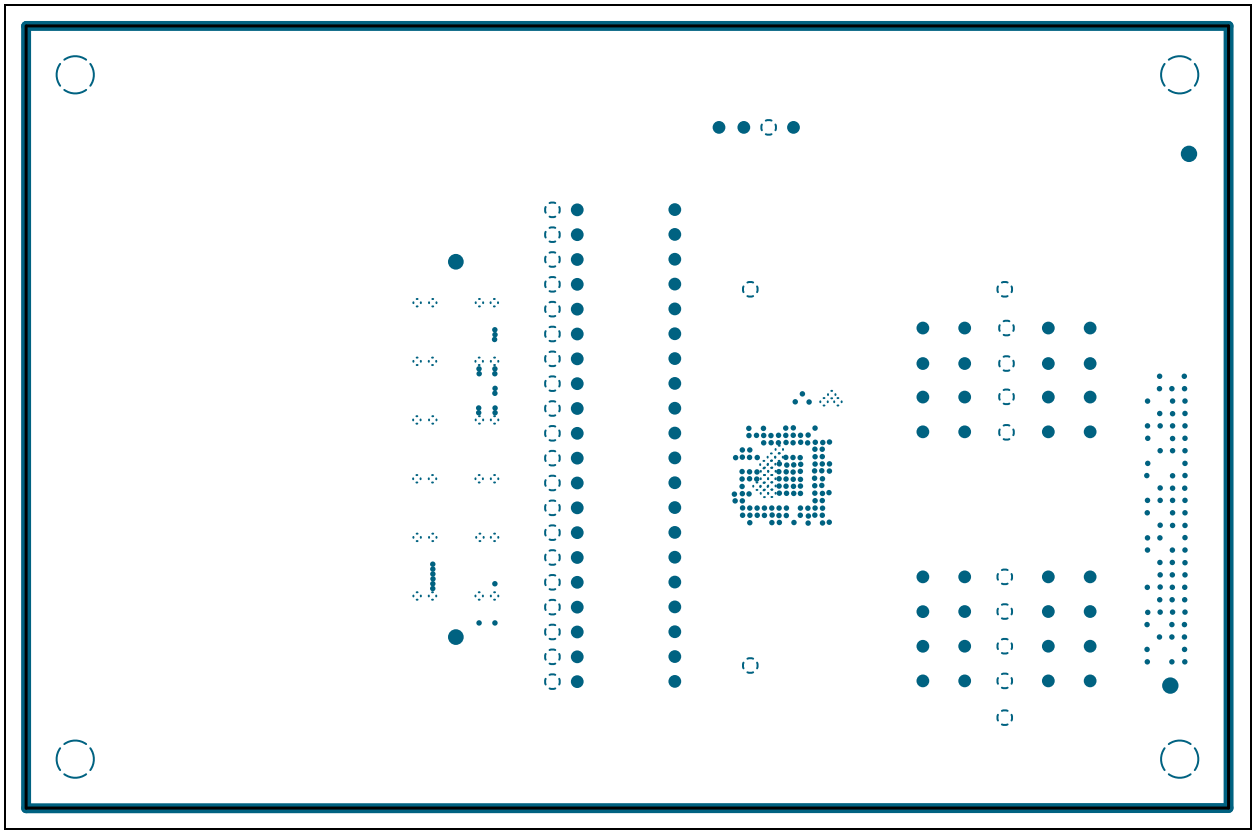
A.4 EVALUATION BOARD – TOP COPPER AND SILK



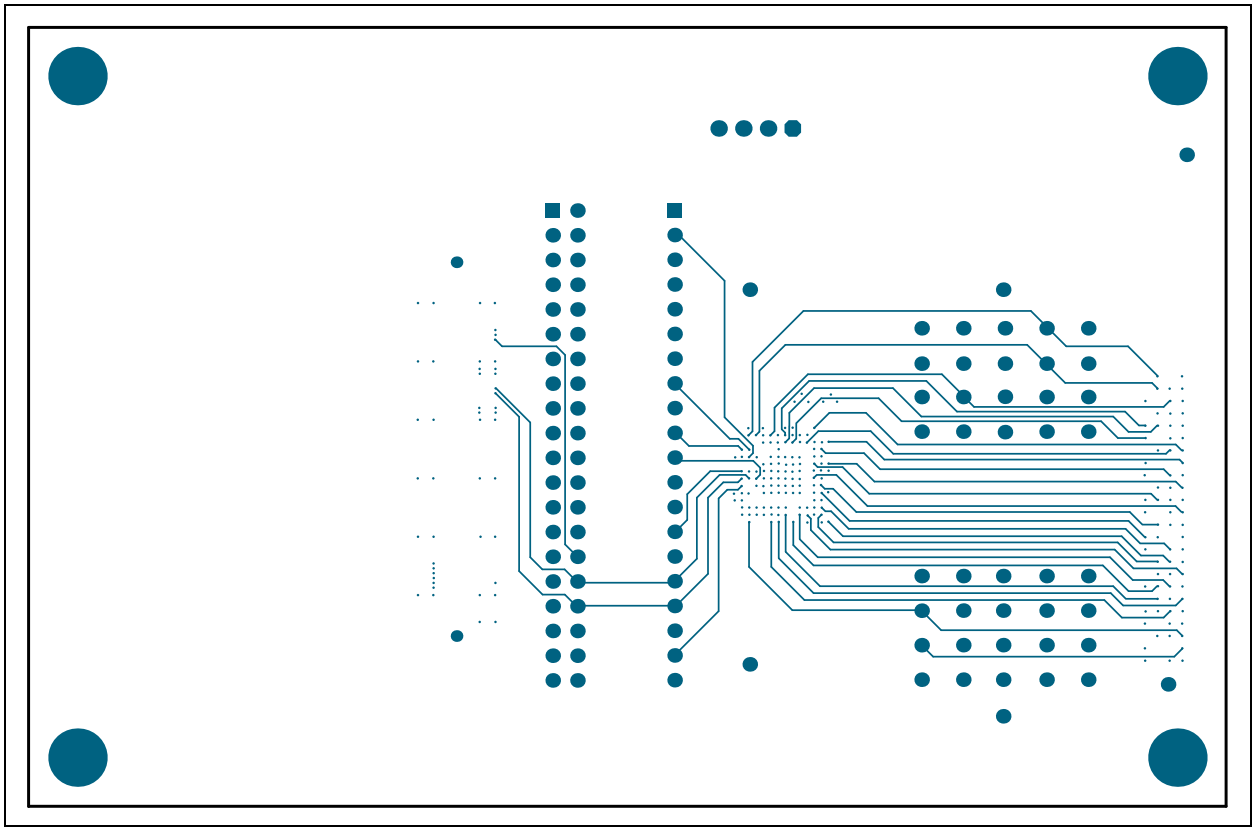
A.5 EVALUATION BOARD – TOP COPPER



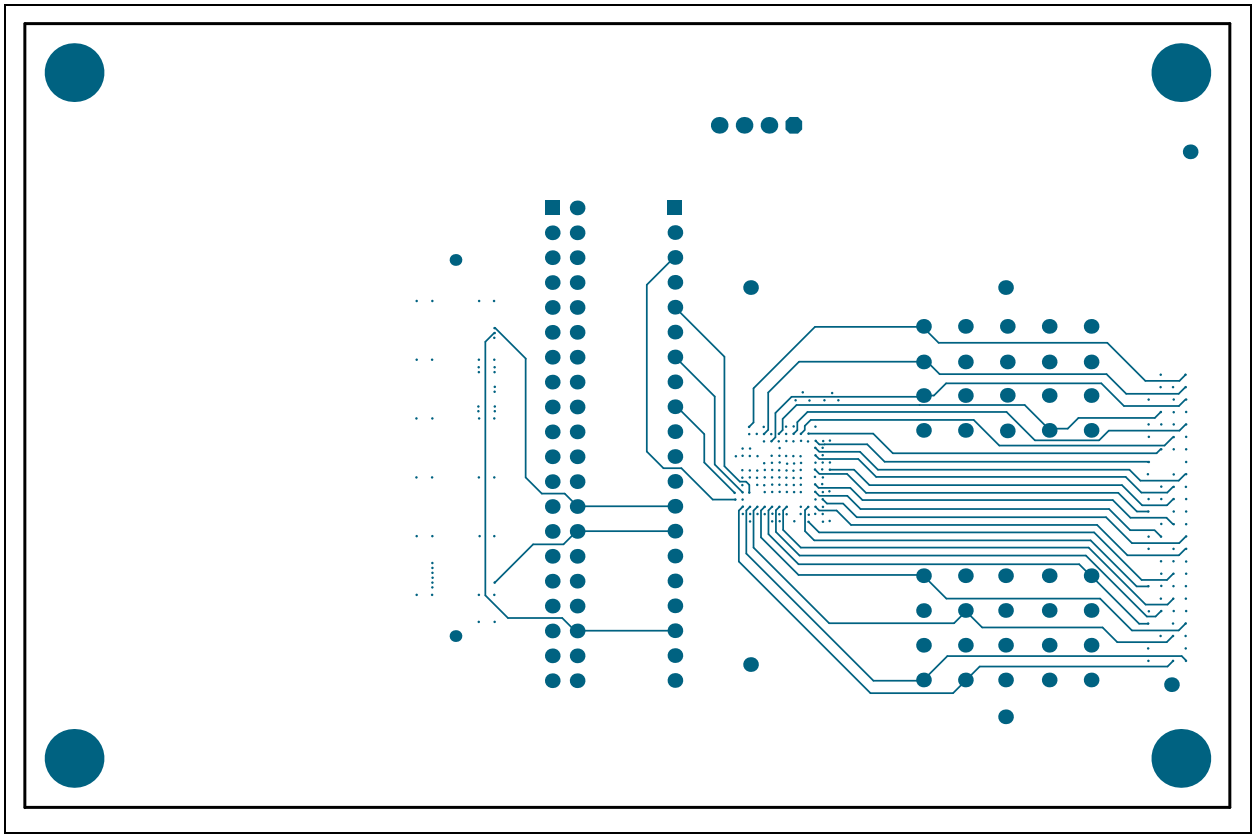
A.6 EVALUATION BOARD – GROUND PLANE



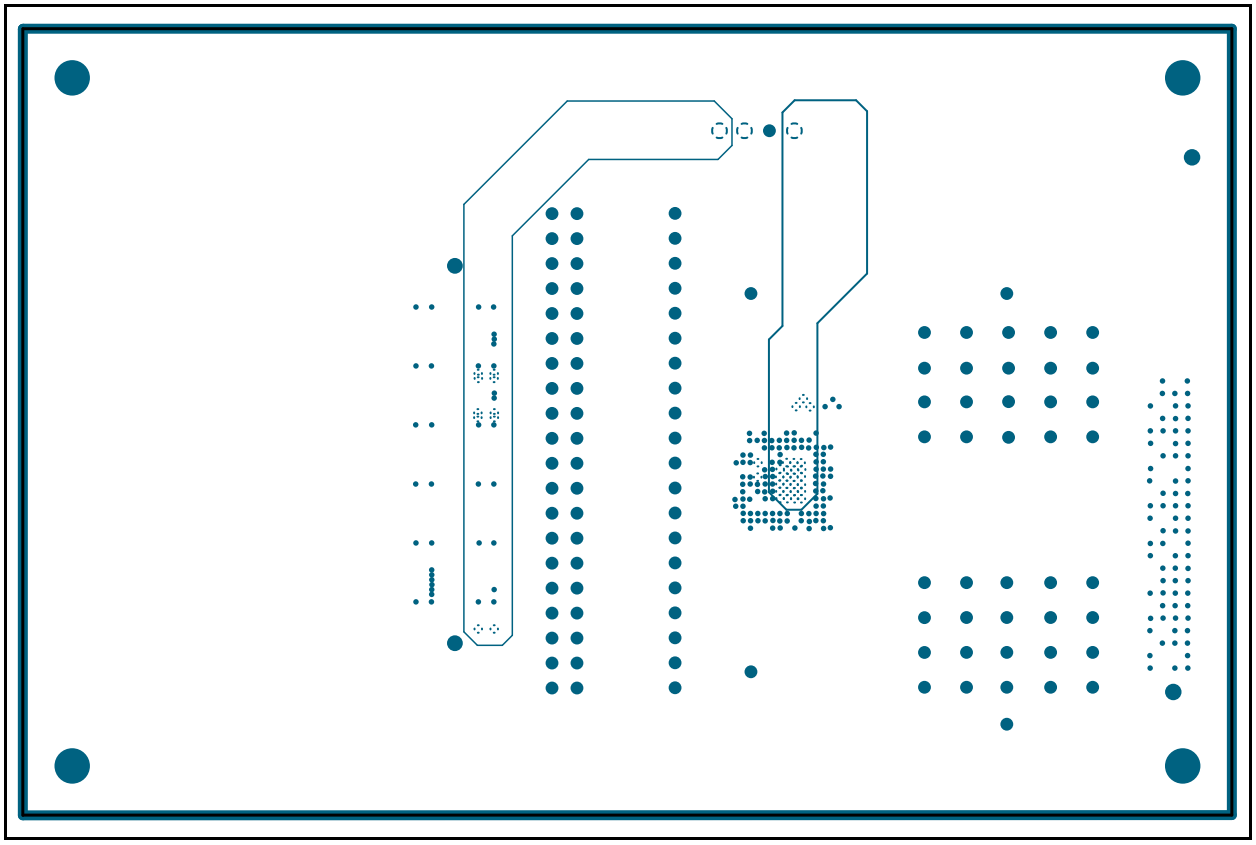
A.7 EVALUATION BOARD – MID LAYER 1



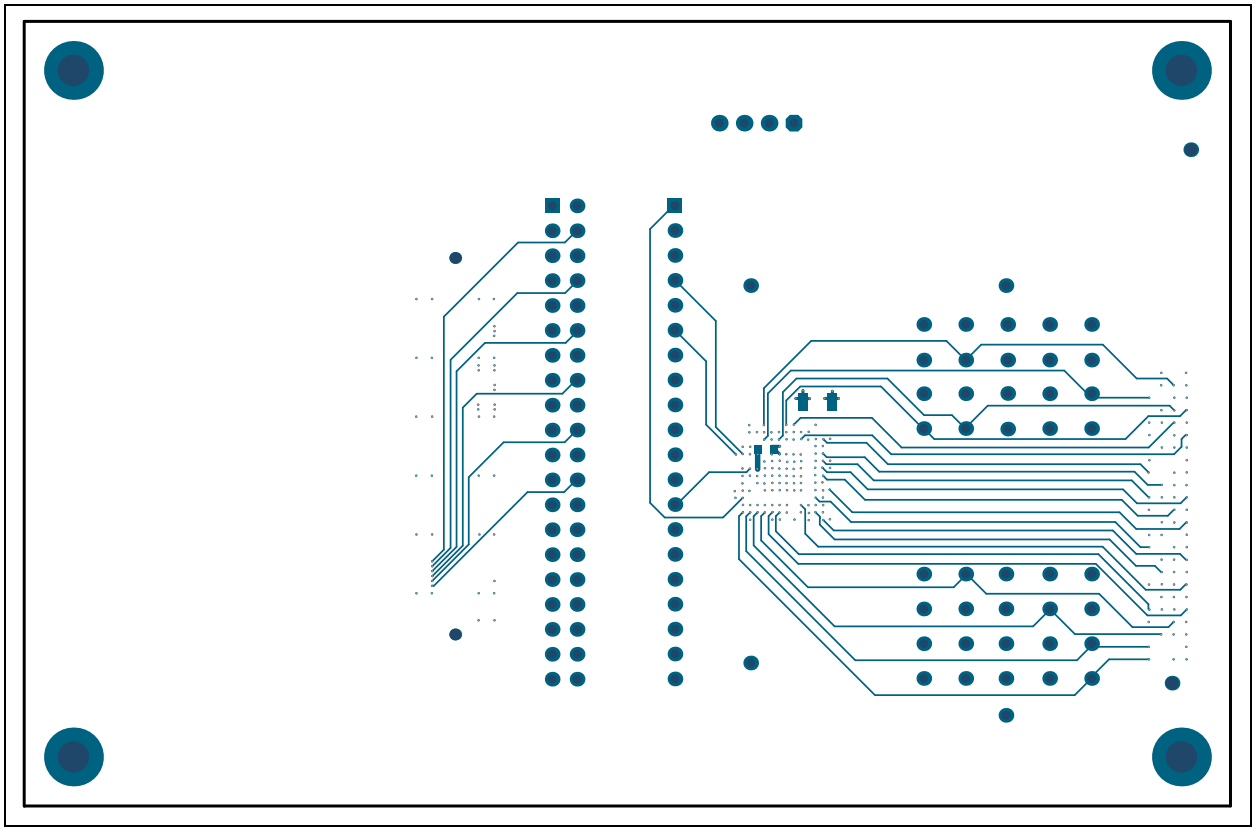
A.8 EVALUATION BOARD – MID LAYER 2



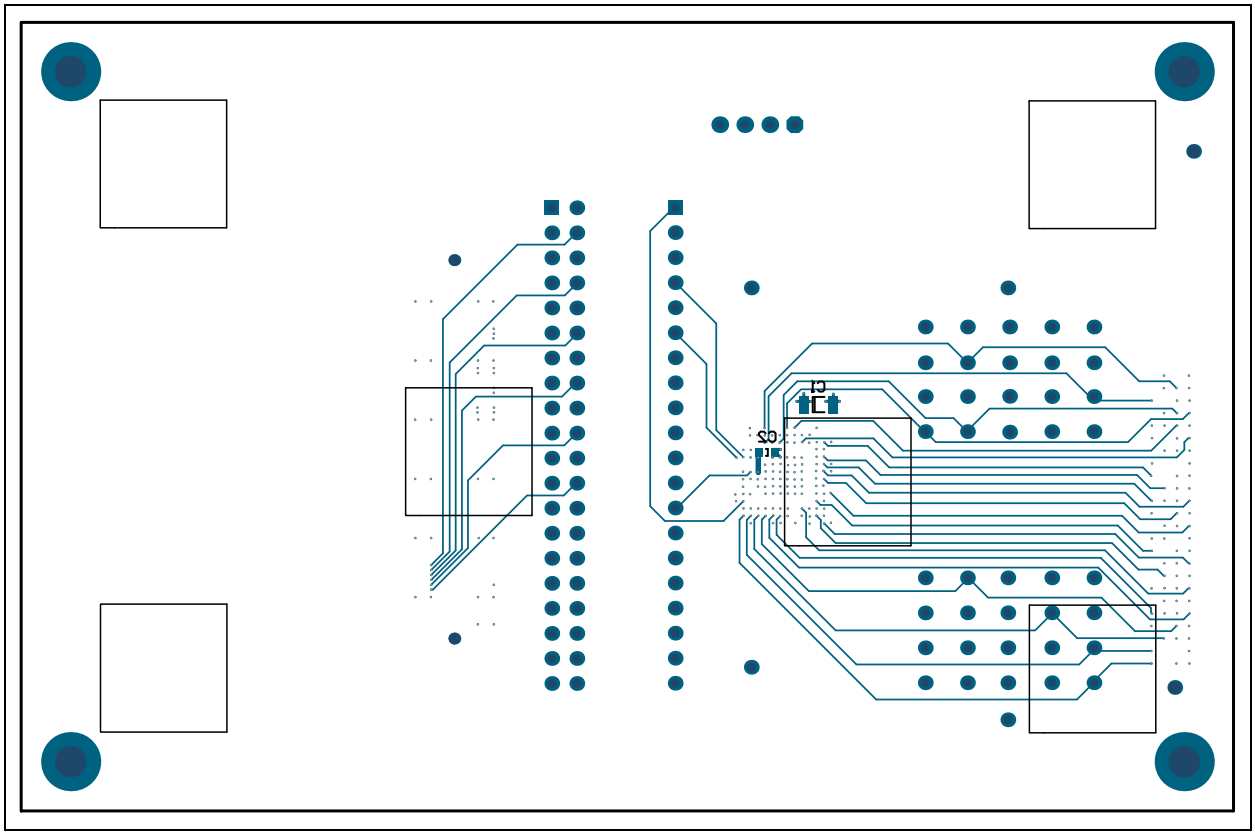
A.9 EVALUATION BOARD – POWER PLANE



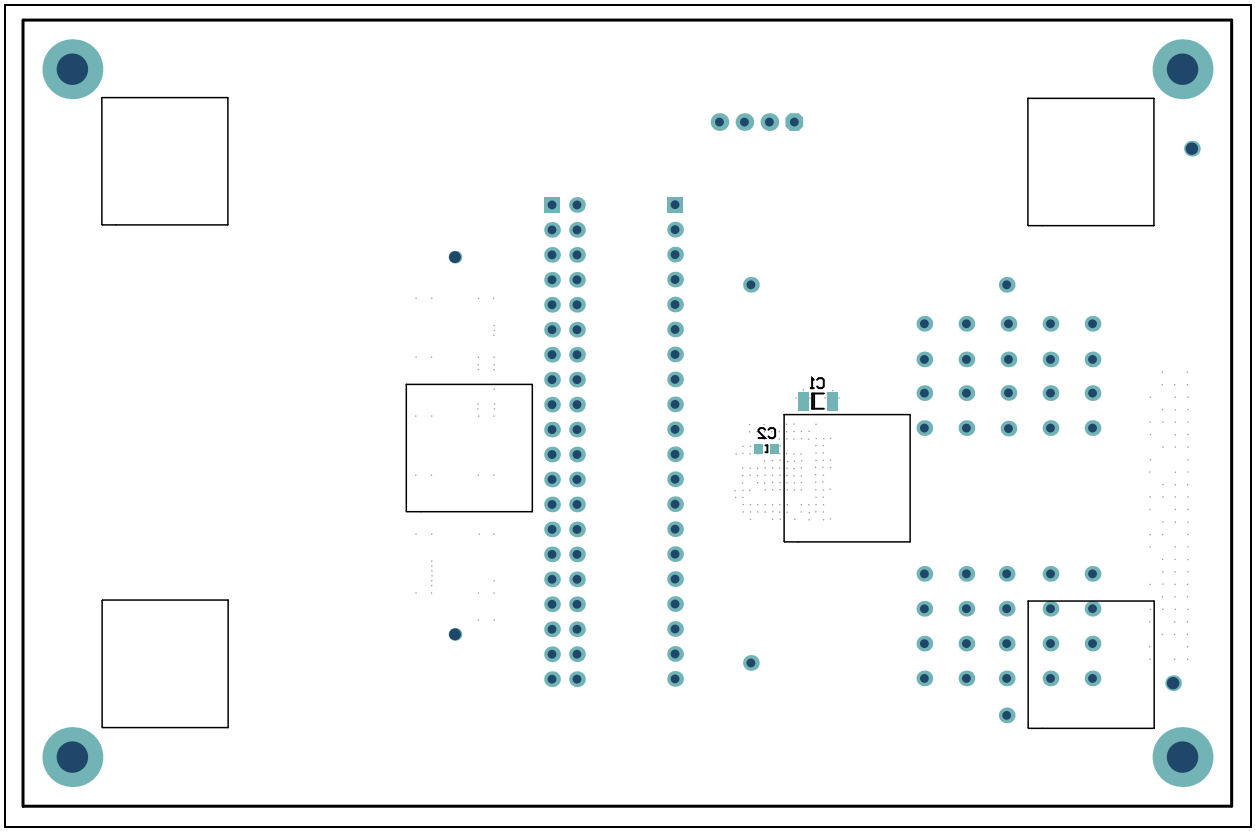
A.10 EVALUATION BOARD – BOTTOM COPPER



A.11 EVALUATION BOARD – BOTTOM COPPER AND SILK



A.12 EVALUATION BOARD – BOTTOM SILK



NOTES:

Appendix B. Bill of Materials (BOM)

B.1 BILL OF MATERIALS (BOM)

Qty.	Reference	Description	Manufacturer	Part Number
1	C1	Ceramic capacitor, 1 µF 100V	AVX Corporation	12061C105KAT2A
1	C2	Ceramic capacitor, 0.1 µF 25V	AVX Corporation	06033C104JAT2A
1	J1	160 position high-density array connector, female	Molex, LLC	0465574145
1	J2	Board-to-board high-speed connector, 120 positions, w/post, SMD	Hirose Electric Co. Ltd.	FX10A-120S/12-SV(71)
1	J3	Connector header, 4 positions, 100" SGL, gold	Samtec Inc.	TSW-104-07-G-S
4	GND	Connector PC pin, circular, 0.030 diameter, gold	Mill-Max Manufacturing Corp.	3132-0-00-15-00-00-08-0
6	N/A	Rubber bumper square, 0.5"L x 0.5"W, black	3M	SJ-5518 (BLACK)
1	PCB	HV582 96-Channel High Voltage Driver IC Evaluation Board – Printed Circuit Board	—	04-10466
6	R1, R3, R5, R7, R9, R11	Resistor SMD 0.0 Ohm jumper 1/10W	Stackpole Electronics Inc.	RMCF0603ZT0R00
6	R2, R4, R6, R8, R10, R12	**DO NOT POPULATE**	—	—
1	U1	HV582, unipolar 96-channel low-voltage serial to high-voltage parallel converter with push-pull outputs	Microchip Technology Inc.	**HV582GA-G**

Note: The components listed in this Bill of Materials are representative of the PCB assembly. The released BOM used in manufacturing uses all RoHS-compliant components.

NOTES:

Appendix C. HV582 Typical Waveforms

C.1 INTRODUCTION

Waveforms presented in this section correspond to two consecutive data transmissions: Transmission 1 and Transmission 2. No load is connected to the output channels.

C.1.1 Transmission 1

Turns on **HV_{OUT}16, 14,** and **1**. Turns off the rest of the other HV_{OUT} channels.

The control signals not shown in the waveforms have the states listed in Table C-1:

TABLE C-1: TRANSMISSION 1: CONTROL SIGNAL STATES

Signal	State
DIR	Low
RST	Low
$\overline{OH}$	High
$\overline{OL}$	High
POL	Low
D2A	Low
D3A	Low
D4A	Low
D5A	Low
D6A	Low

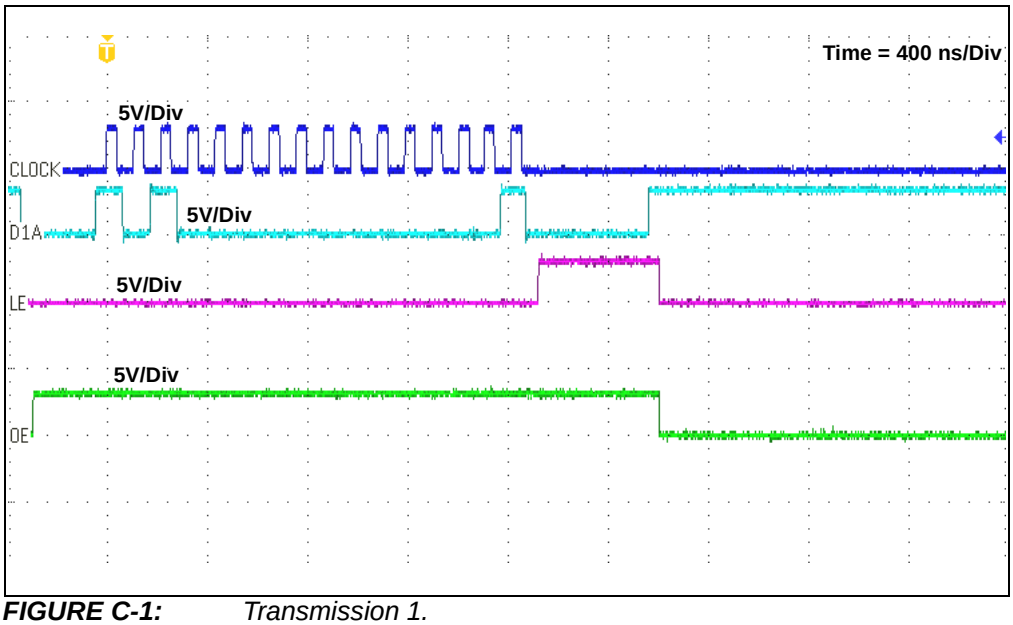


FIGURE C-1: *Transmission 1.*

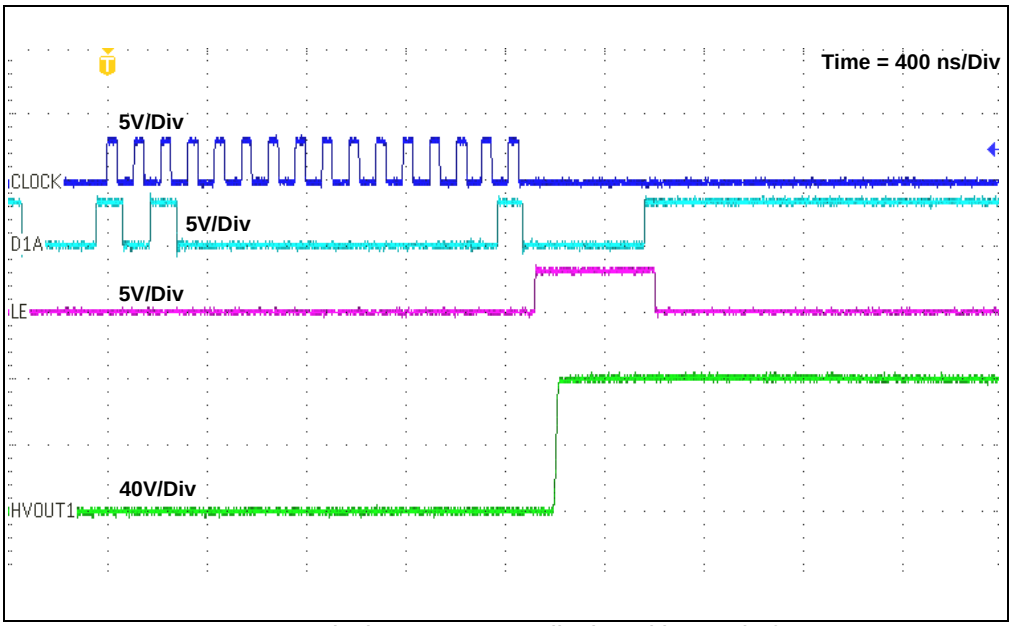


FIGURE C-2: *Transmission 1: $HV_{OUT}1$ displayed instead of OE.*

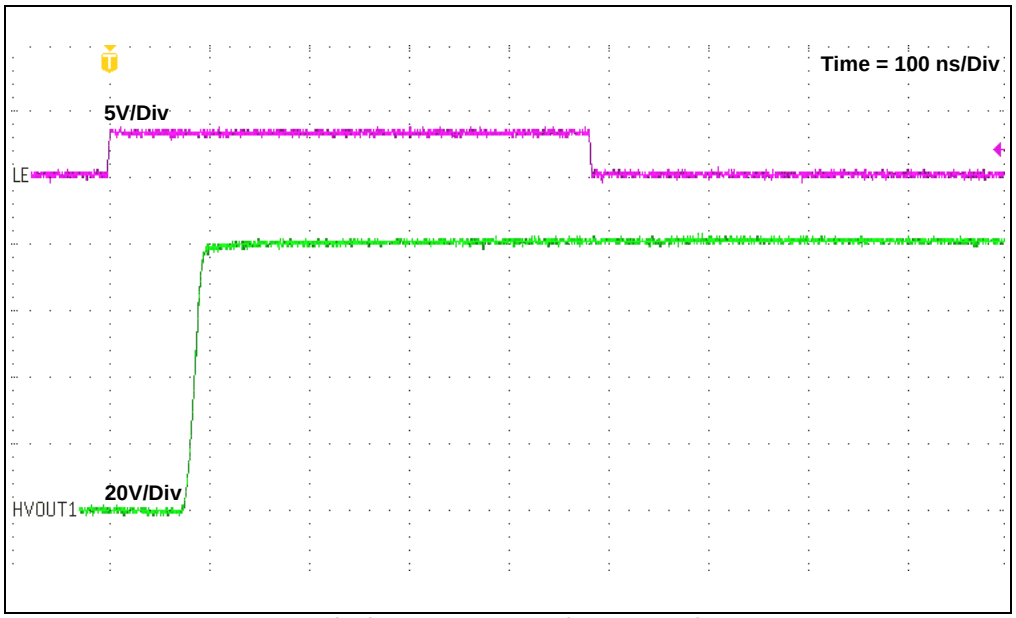


FIGURE C-3: *Transmission 1: Zoom version, LE and $HV_{OUT}1$.*

C.1.2 Transmission 2

Turns on **HV_{OUT}16** and **HV_{OUT}14**. Turns off **HV_{OUT}1** and the rest of the HV_{OUT} channels. This transmission illustrates the turn-off transition for HV_{OUT}1, which is at high level (V_{PP}) due to Transmission 1.

The control signals not shown in the waveforms have the states listed in Table C-2:

TABLE C-2: TRANSMISSION 2: CONTROL SIGNAL STATES

Signal	State
DIR	Low
RST	Low
$\overline{OH}$	High
$\overline{OL}$	High
POL	Low
D2A	Low
D3A	Low
D4A	Low
D5A	Low
D6A	Low

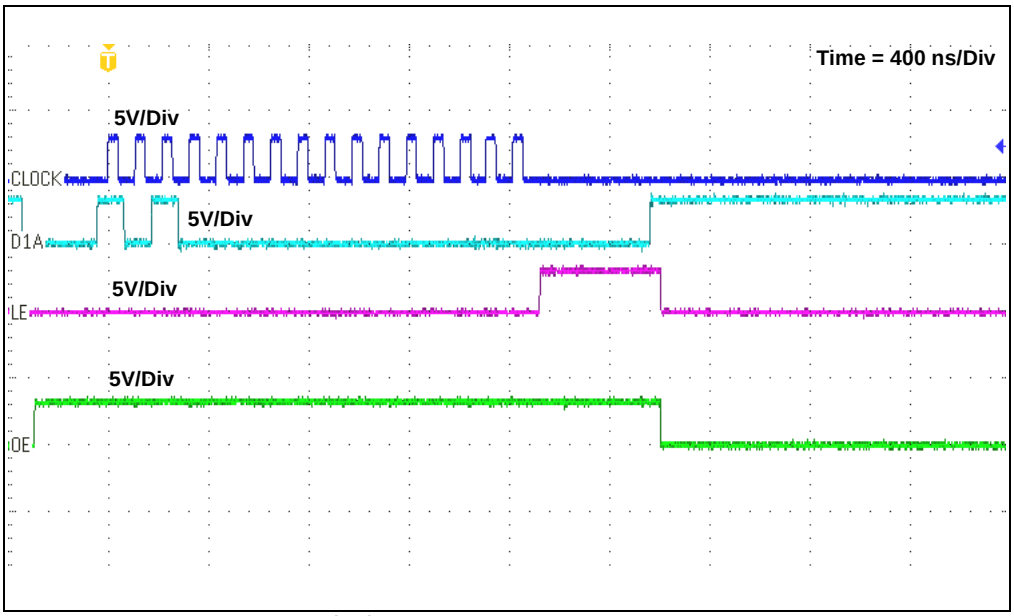


FIGURE C-4: *Transmission 2.*

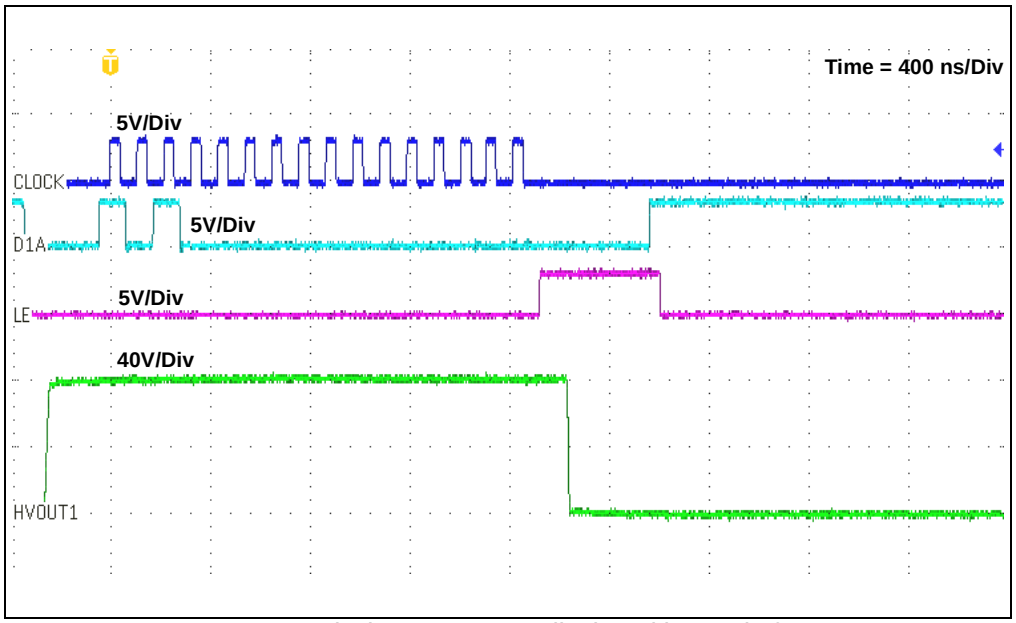


FIGURE C-5: *Transmission 2: $HV_{OUT}1$ displayed instead of OE.*

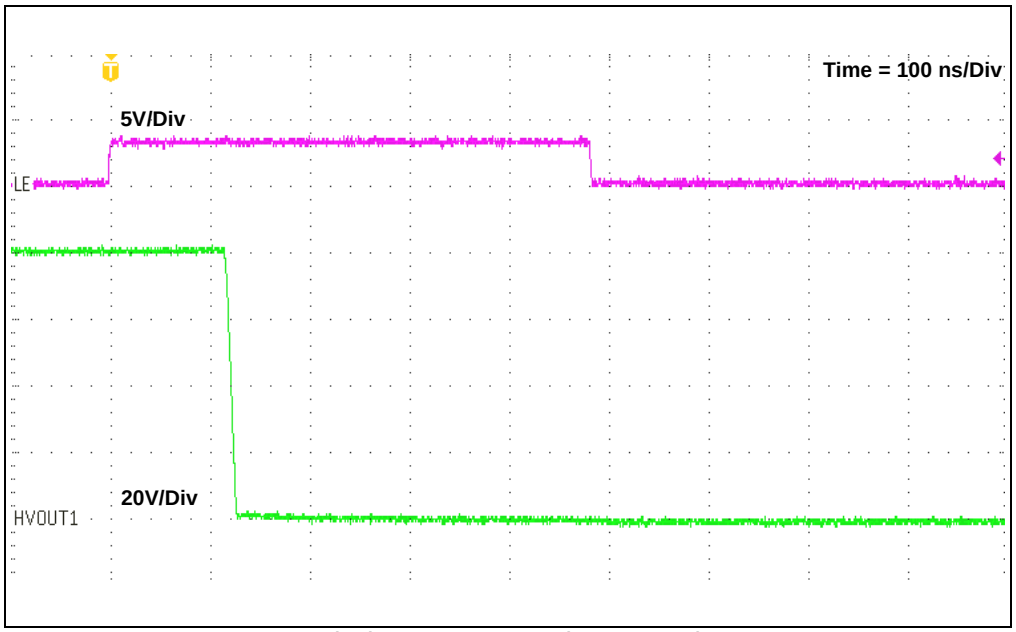


FIGURE C-6: *Transmission 2: Zoom version, LE and $HV_{OUT}1$.*

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