

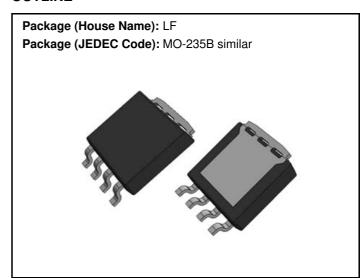
P70LF4QLK

Power MOSFETs 40V, 70A, N-channel

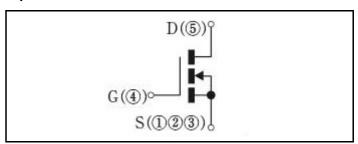
Feature

- N-channel
- Small SMD
- Large Current
- Low Ron
- 4.5V Gate Drive
- Low Capacitance
- Based on AEC-Q101
- · Halogen free
- · Pb free terminal
- RoHS:Yes

OUTLINE



Equivalent circuit



Absolute Maximum Ratings (unless otherwise specified : Tc=25°C)

Item	Symbol	Conditions	Ratings	Unit
Storage temperature	Tstg		-55 to 175	°C
Channel tempertature	Tch		-55 to 175	°C
Drain-source voltage	V_{DSS}		40	V
Gate-source voltage	V_{GSS}		±20	V
Continuous drain current(DC)	I _D		70	Α
Continuous drain current(Peak)	I _{DP}	Pulse width 10µs, duty=1/100	210	Α
Total power dissipation	P _T		123	W
Single avalanche current	I _{AS}	Starting Tch=25°C Tch≦150°C	26	Α
Single avalanche energy	E _{AS}	Starting Tch=25°C Tch≦150°C	76	mJ

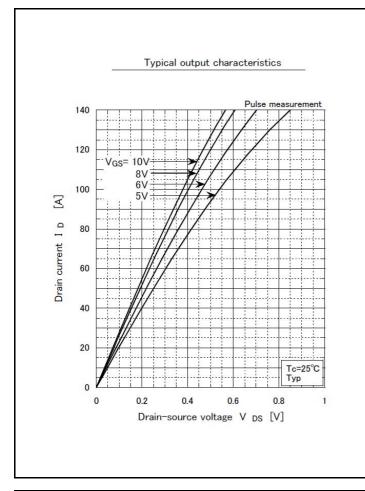
^{* :} See the original Specifications

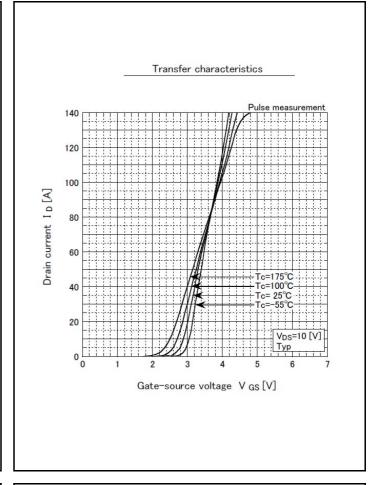
Electrical Characteristics (unless otherwise specified : Tc=25°C)

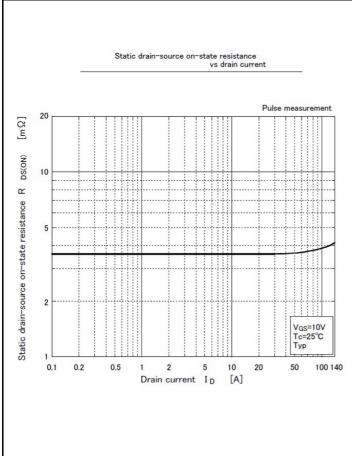
Item	Symbol	Conditions		Ratings		
			MIN	TYP	MAX	Unit
Drain-Source breakdown voltage	$V_{(BR)DSS}$	ID=1mA, VGS=0V	40			V
Zero gate voltage drain current	I _{DSS}	VDS=40V, VGS=0V			1	μΑ
Gate-source leakage current	I _{GSS}	VGS=±20V, VDS=0V			±0.1	μA
Forward transconductance	9fs	ID=35A, VDS=10V	15			S
Static drain-source on-state resistance	R _{DS(ON)}	ID=35A, VGS=10V		0.0036	0.0045	Ω
Static drain-source on-state resistance	R _{DS(ON)}	ID=35A, VGS=4.5V		0.0056	0.007	Ω
Gate threshold voltage	Vth	ID=1mA, VDS=10V	1.5	2	2.5	٧
Source-drain diode forward voltage	V_{SD}	IS=70A, VGS=0V			1.5	V
Thermal resistance	Rth(j-c)	Junction to case, with heatsink			1.21	°C/W
Total gate charge	Qg	VDD=32V, VGS=10V, ID=70A		49		nC
Gate to source charge	Qgs	VDD=32V, VGS=10V, ID=70A		10		nC
Gate to drain charge	Qgd	VDD=32V, VGS=10V, ID=70A		12		nC
Input capacitance	Ciss	VDS=25V, VGS=0V, f=1MHz		2360		pF
Reverce transfer capacitnce	Crss	VDS=25V, VGS=0V, f=1MHz		150		pF
Output capacitance	Coss	VDS=25V, VGS=0V, f=1MHz		290		pF
Turn-on delay time	td(on)	ID=35A, RL=0.55Ω, VDD=20V, Rg=0Ω, VGS(+)=10V, VGS(-)=0V		5		ns
Rise time	tr	ID=35A, RL=0.55Ω, VDD=20V, Rg=0Ω, VGS(+)=10V, VGS(-)=0V		7.5		ns
Turn-off delay time	td(off)	ID=35A, RL=0.55Ω, VDD=20V, Rg=0Ω, VGS(+)=10V, VGS(-)=0V		26		ns
Fall time	tf	ID=35A, RL=0.55Ω, VDD=20V, Rg=0Ω, VGS(+)=10V, VGS(-)=0V		6.5		ns
Diode reverse recovery time	trr	IF=70A, VGS=0V, di/dt=100A/μs		35		ns
Diode reverse recovery charge	Qrr	IF=70A, VGS=0V, di/dt=100A/μs		32		nC

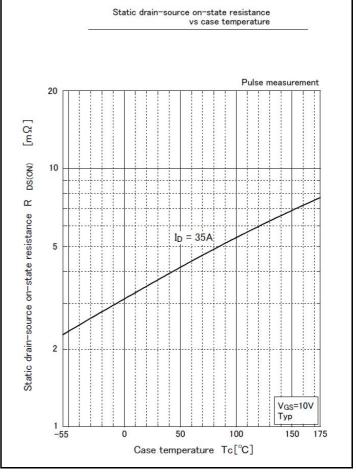
st :See the original Specifications

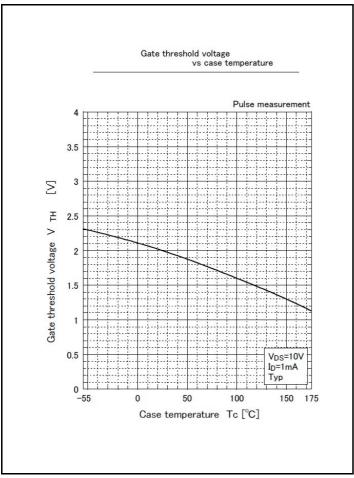
CHARACTERISTIC DIAGRAMS

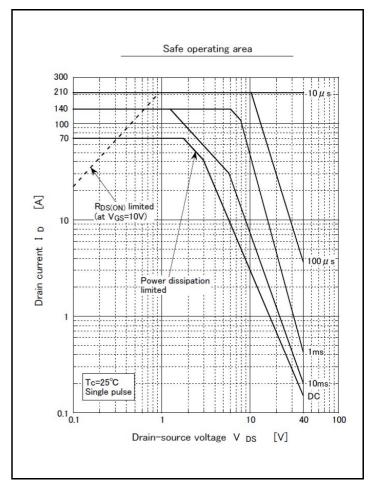


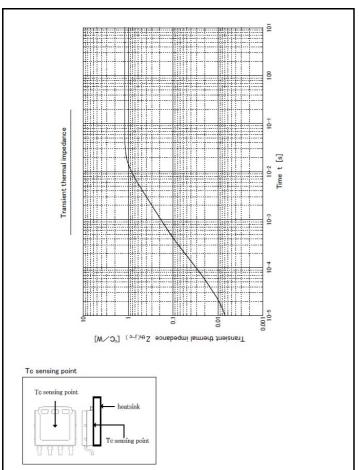


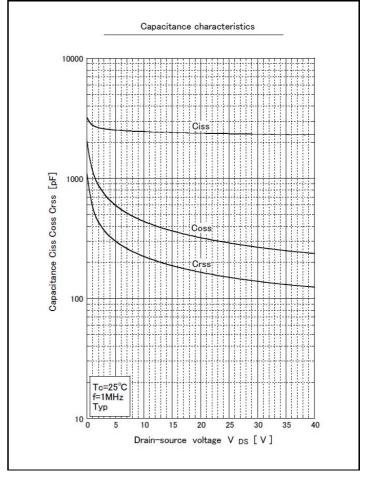


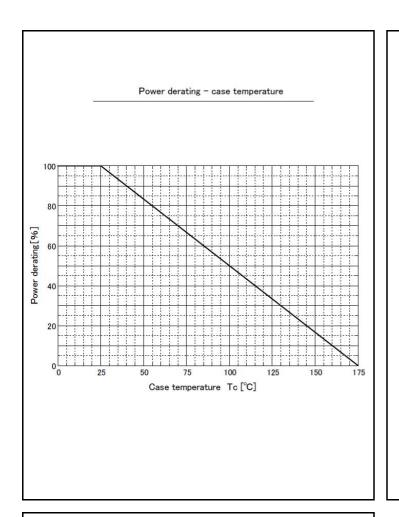


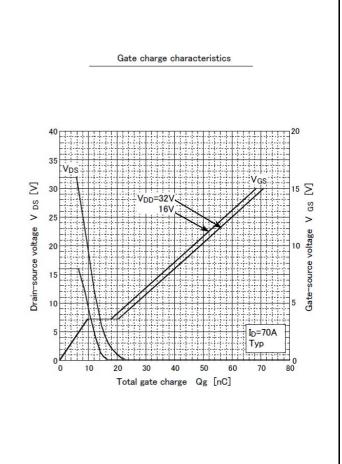


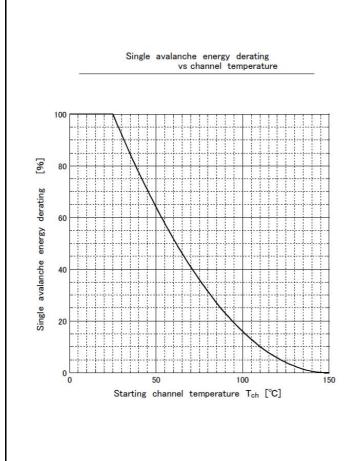






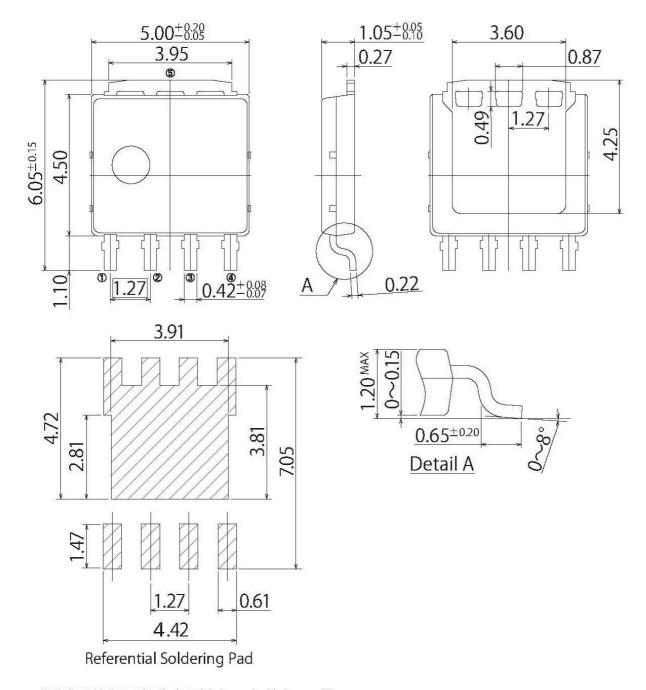






G7

JEDEC Code	MO-235B similar		
JEITA Code	-		
House Name	LF		



[•] Optimize soldering pad to the board design and soldering condition.

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