

P6B52HP2

Power MOSFETs

525V, 6A, N-channel

Feature

- N-channel
- SMD
- High Voltage
- Low Capacitance
- High Avalanche Durability, High di/dt Durability
- Pb free terminal
- RoHS:Yes

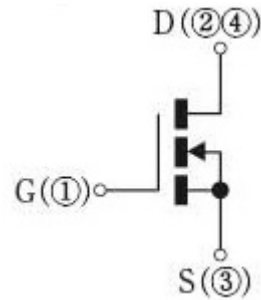
OUTLINE

Package (House Name): FB

Package (JEDEC Code): TO-252AA



Equivalent circuit



Absolute Maximum Ratings (unless otherwise specified : Tc=25°C)

Item	Symbol	Conditions	Ratings	Unit
Storage temperature	T _{stg}		-55 to 150	°C
Channel temperature	T _{ch}		150	°C
Drain-source voltage	V _{DSS}		525	V
Gate-source voltage	V _{GSS}		±30	V
Continuous drain current(DC)	I _D		6	A
Continuous drain current(Peak)	I _{DP}	Pulse width 10μs, duty=1/100	24	A
Continuous source current(DC)	I _S		6	A
Total power dissipation	P _T		70	W
Repetitive avalanche current	I _{AR}	Starting T _{ch} =25°C T _{ch} ≤150°C	6	A
Single avalanche energy	E _{AS}	Starting T _{ch} =25°C T _{ch} ≤150°C	50	mJ
Repetitive avalanche energy	E _{AR}	Starting T _{ch} =25°C T _{ch} ≤150°C	5	mJ
Drain-source diode di/dt strength	di/dt	I _S =6A, T _c =25°C	350	A/μs

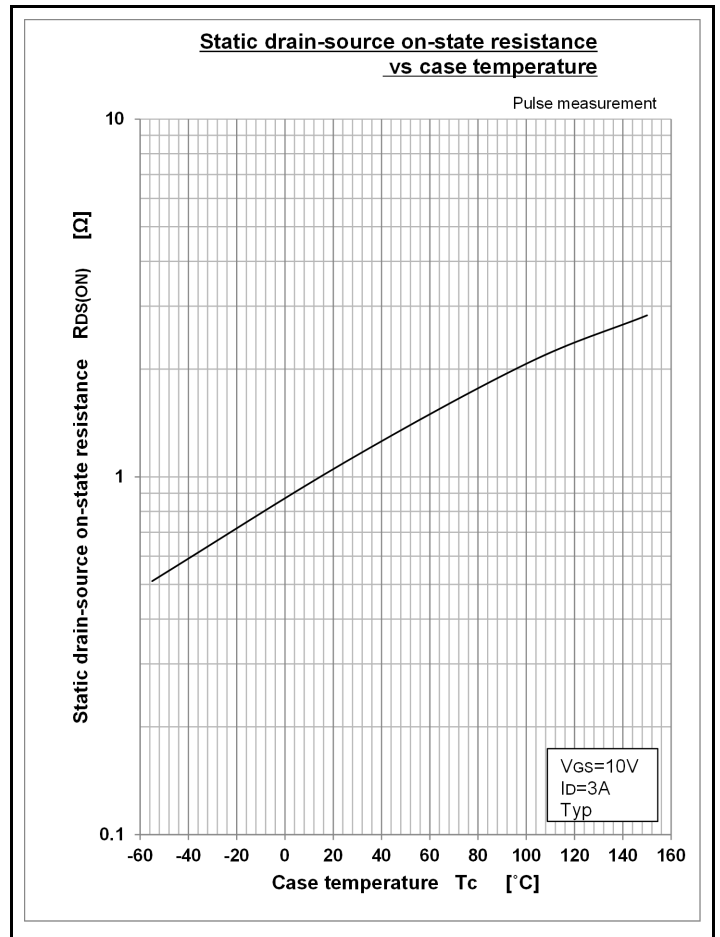
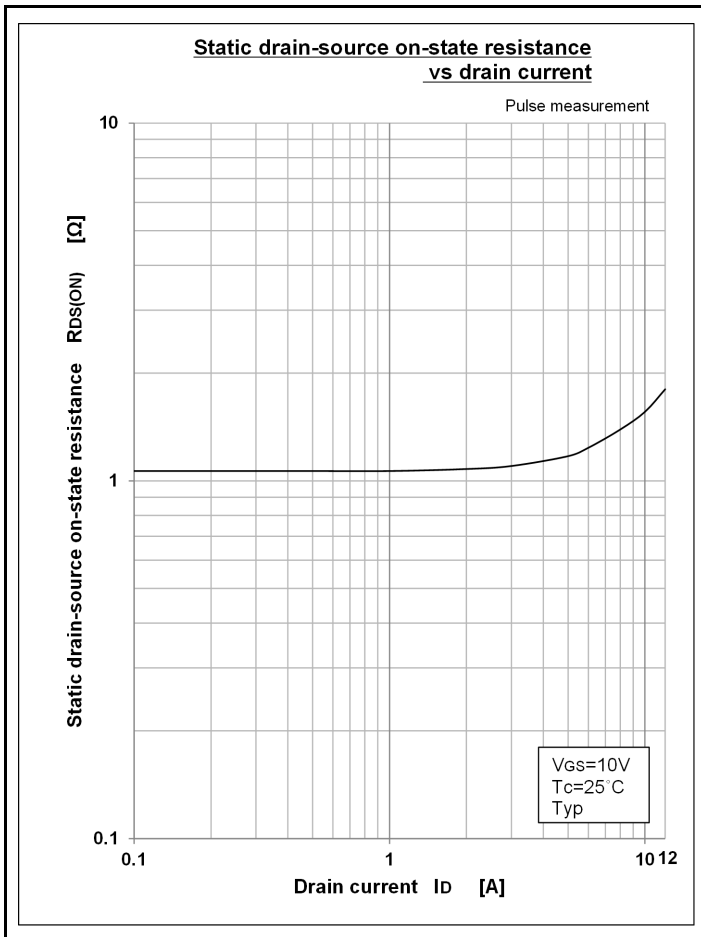
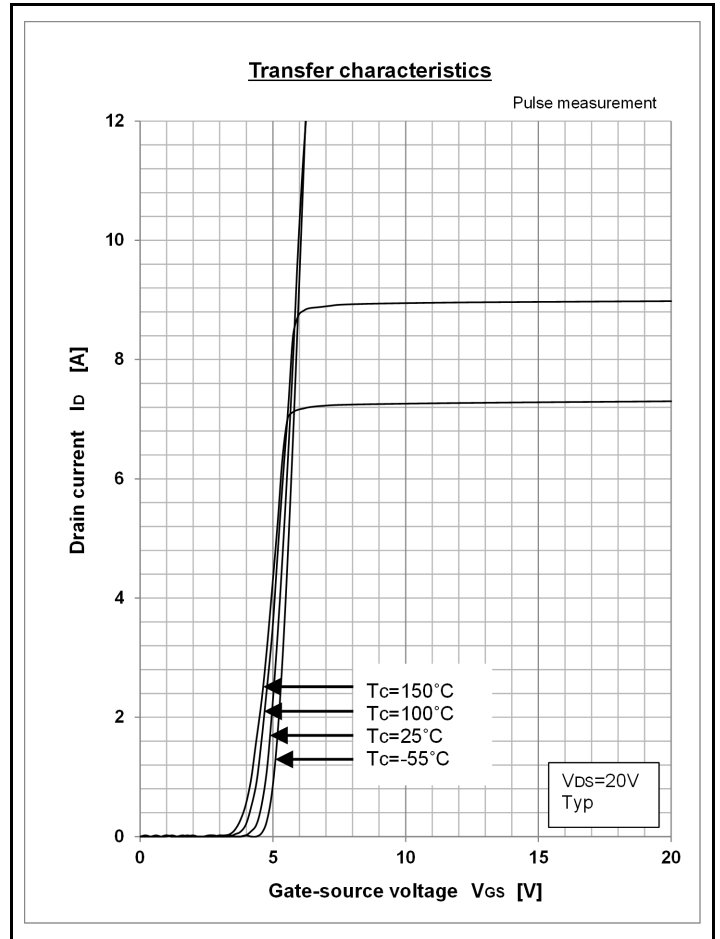
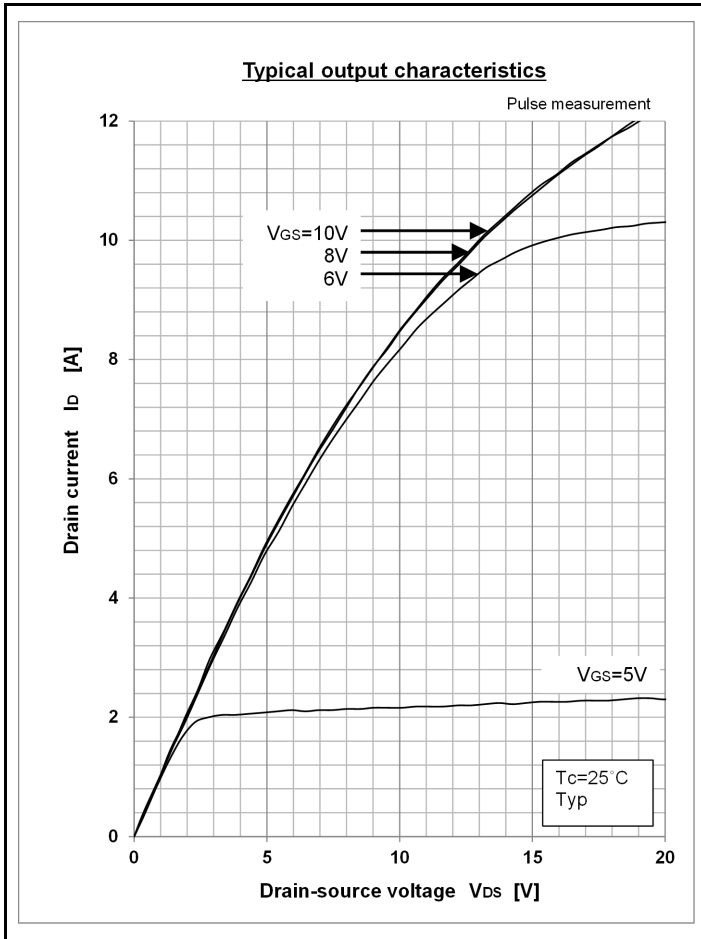
* :See the original Specifications

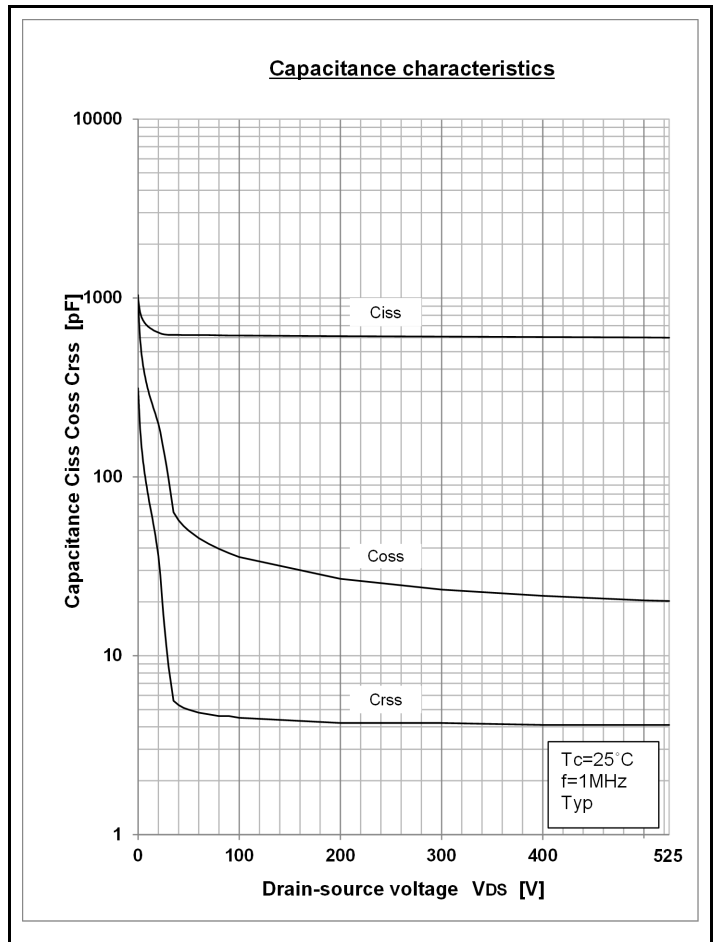
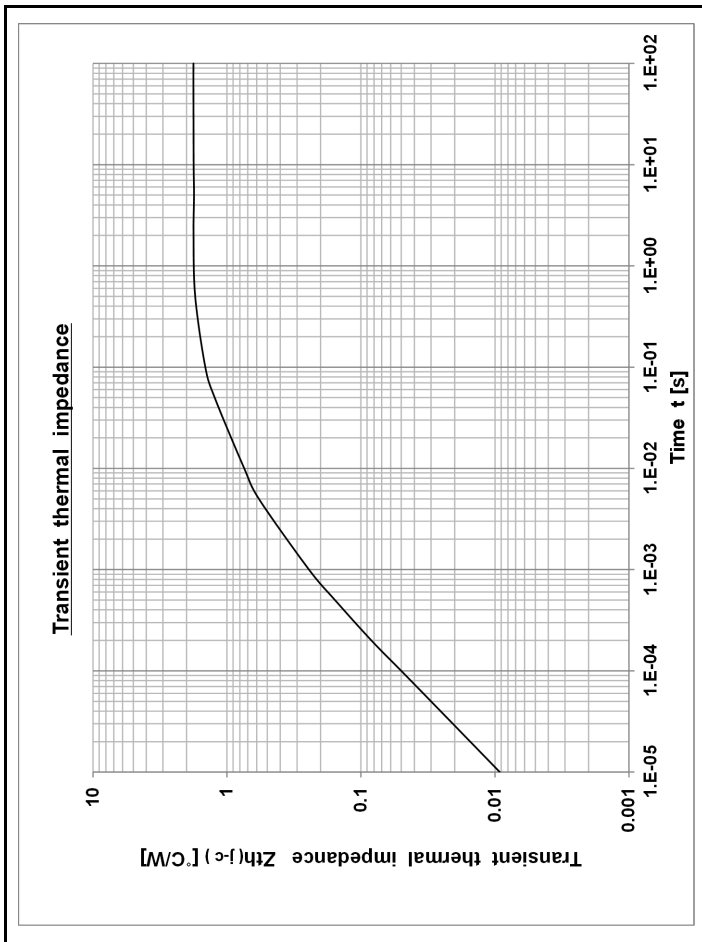
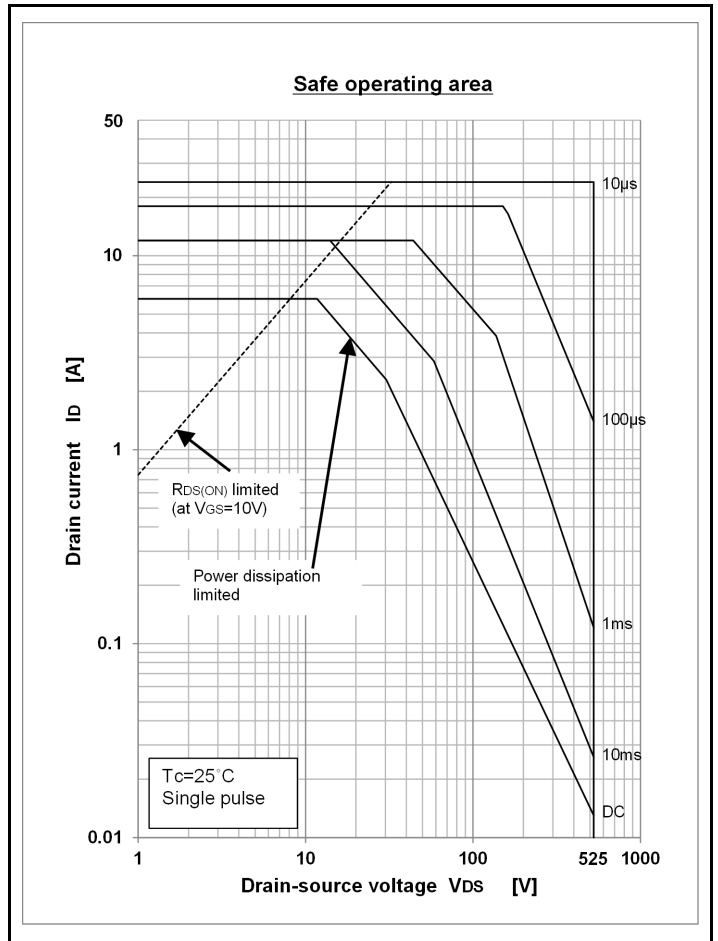
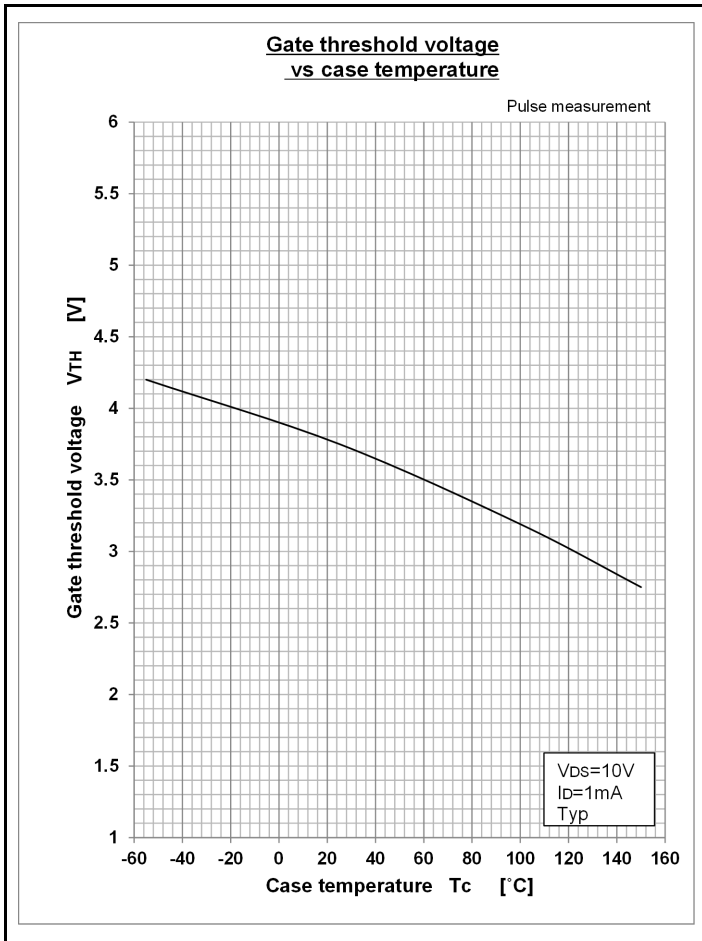
Electrical Characteristics (unless otherwise specified : Tc=25°C)

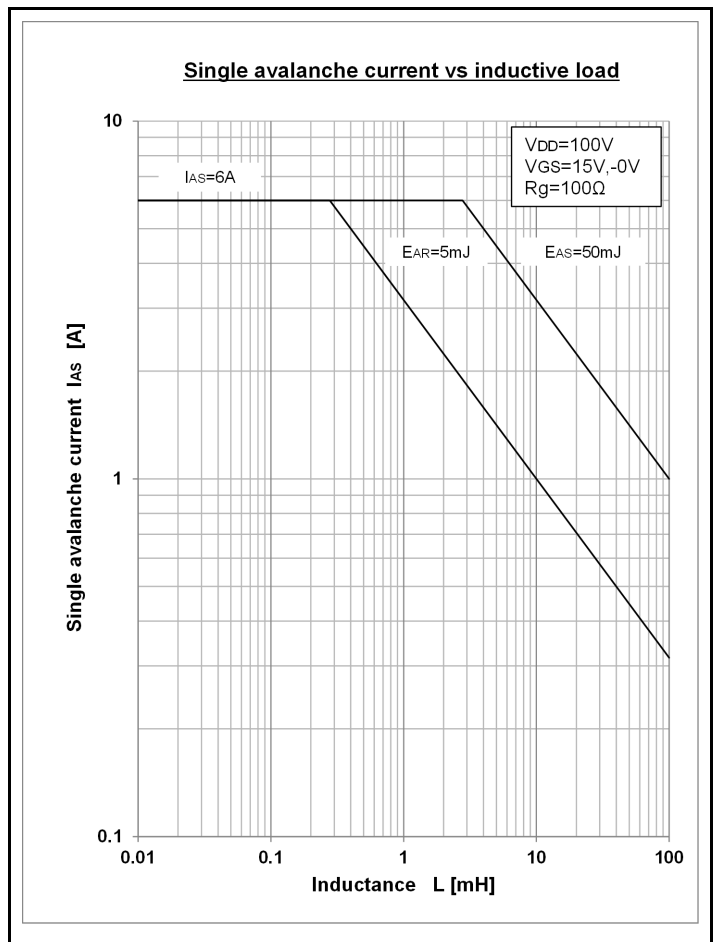
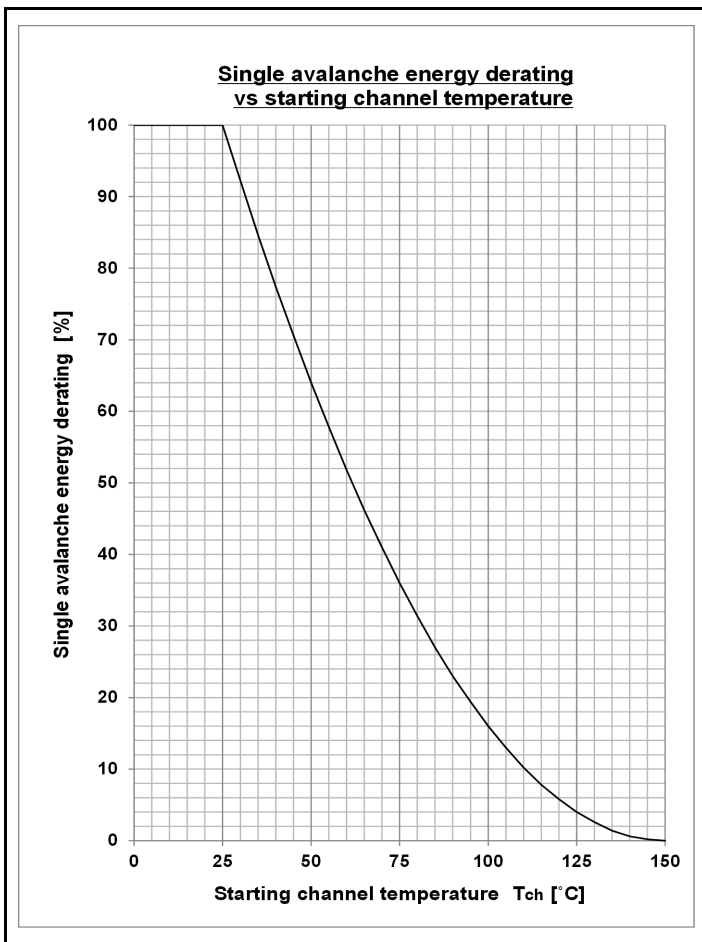
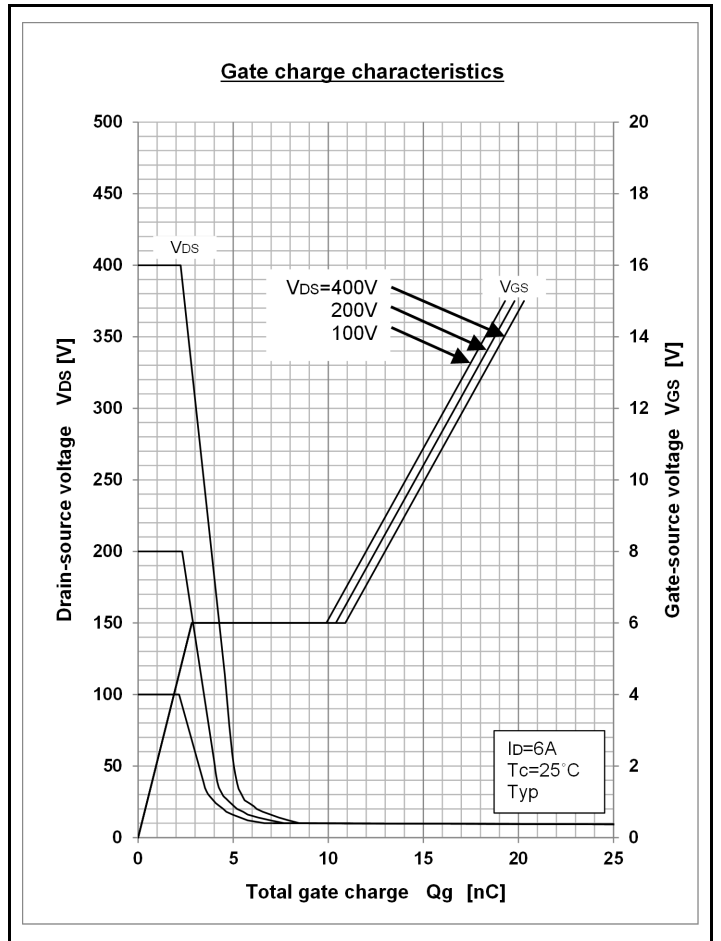
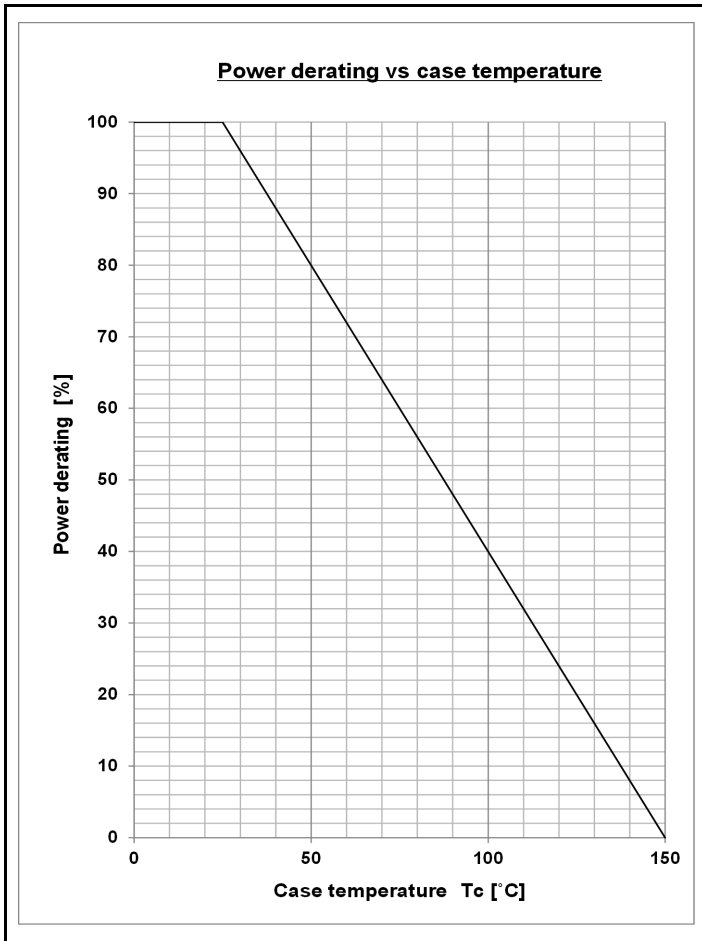
Item	Symbol	Conditions	Ratings			Unit
			MIN	TYP	MAX	
Drain-Source breakdown voltage	$V_{(BR)DSS}$	ID=1mA, VGS=0V	525			V
Zero gate voltage drain current	I_{DSS}	VDS=525V, VGS=0V			100	μ A
Gate-source leakage current	I_{GSS}	VGS= \pm 25V, VDS=0V			\pm 10	μ A
Forward transconductance	g_{fs}	ID=3A, VDS=10V	2	6		S
Static drain-source on-state resistance	$R_{DS(ON)}$	ID=3A, VGS=10V		1.1	1.35	Ω
Gate threshold voltage	V_{th}	ID=1mA, VDS=10V	3	3.75	4.5	V
Source-drain diode forward voltage	V_{SD}	IS=3A, VGS=0V			1.5	V
Thermal resistance	$R_{th(j-c)}$	Junction to case			1.78	$^{\circ}$ C/W
Total gate charge	Q_g	VDD=400V, VGS=10V, ID=6A		15		nC
Input capacitance	C_{iss}	VDS=50V, VGS=0V, f=1MHz		520		pF
Reverse transfer capacitance	C_{rss}	VDS=50V, VGS=0V, f=1MHz		5.5		pF
Output capacitance	C_{oss}	VDS=50V, VGS=0V, f=1MHz		58		pF
Turn-on delay time	$t_{d(on)}$	ID=3A, RL=50 Ω , VDD=150V, Rg=50 Ω , VGS(+)=10V, VGS(-)=0V		15		ns
Rise time	t_r	ID=3A, RL=50 Ω , VDD=150V, Rg=50 Ω , VGS(+)=10V, VGS(-)=0V		20		ns
Turn-off delay time	$t_{d(off)}$	ID=3A, RL=50 Ω , VDD=150V, Rg=50 Ω , VGS(+)=10V, VGS(-)=0V		40		ns
Fall time	t_f	ID=3A, RL=50 Ω , VDD=150V, Rg=50 Ω , VGS(+)=10V, VGS(-)=0V		25		ns

* : See the original Specifications

CHARACTERISTIC DIAGRAMS

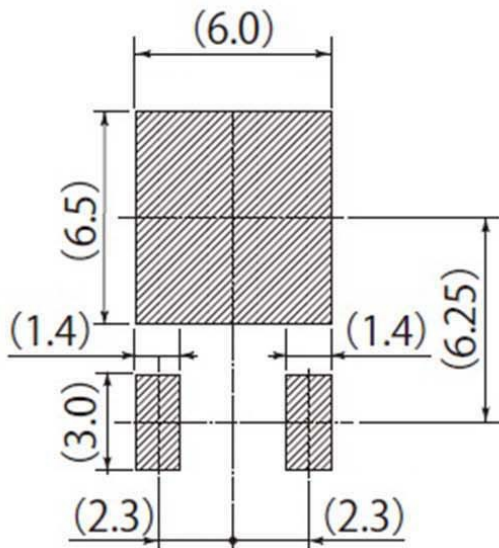
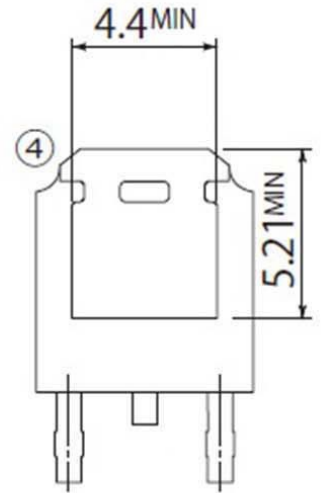
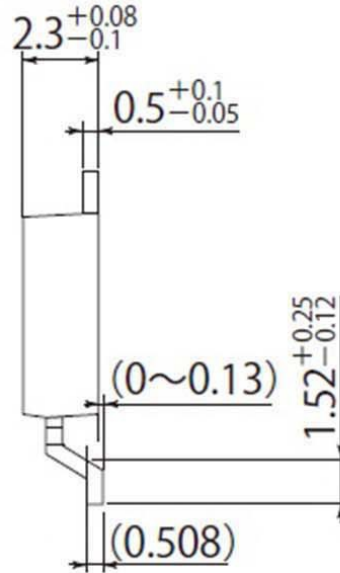
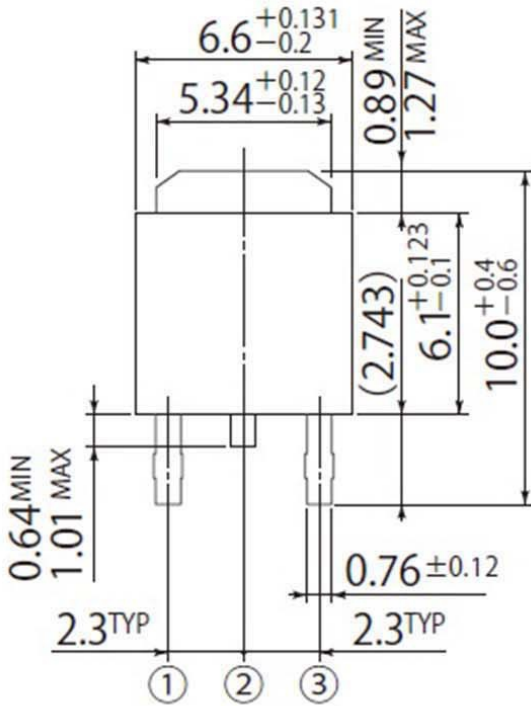






G2

JEDEC Code	TO-252AA
JEITA Code	-
House Name	FB



Referential Soldering Pad

• Optimize soldering pad to the board design and soldering condition.

Notes

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