

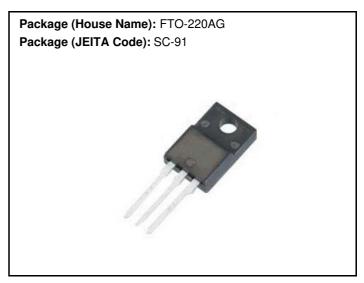
KD8SF60

TRIACs 600V, 8A

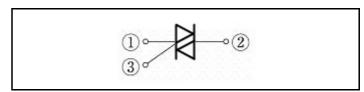
Feature

- Full molded
- · High voltage
- Tj=150°C
- · Stable surge-on current capability
- Pb free terminal
- RoHS:Yes

OUTLINE



Equivalent circuit



$\textbf{Absolute Maximum Ratings} \quad \text{(unless otherwise specified : } Tc=25\,^{\circ}C)$

Item	Symbol	Conditions	Ratings	Unit
Storage temperrature	Tstg		-55 to 150	°C
Junction temperature	Tj		-40 to 150	°C
Repetitive peak off-state voltage	V_{DRM}		600	V
Non-repetitive peak off-state voltage	V_{DSM}	*	720	V
R.M.S. on-state current	I _{T(RMS)}	Tc=110°C, commercial frequency, sine wave, θ =360°C	8	А
Surge on-state current	I _{TSM}	Tj=25°C, 60Hz sine wave, Non-repetive 1 cycle peak	80	А
Current squared time	l ² t	Tj=25°C, t=8.33ms, Non-repetitive	26	A ² S
Critical rate of rise of on-state current	di/dt		50	A/μs
Peak gate dissipation	P_{GM}	f=60Hz, Duty≦10%	5	W
Average gate dissipation	P _G (AV)		0.5	W
Peak gate current	I_{GM}	f=60Hz, Duty≦10%	2	Α
Peak gate voltage	V_{GM}		10	V
Dielectric strength	Vdis	Terminals to case, AC 1 minute	2	kV
Mounting Torque	TOR	(Recommended torque:0.3N·m)	0.5	N∙m

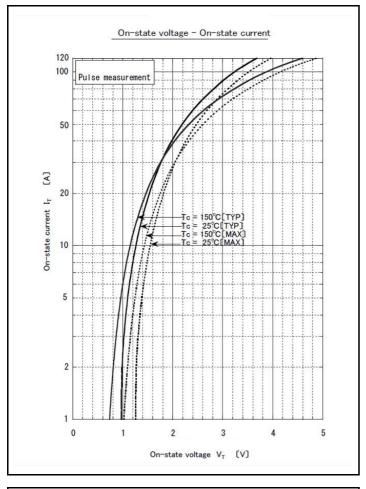
^{*} :See the original Specifications

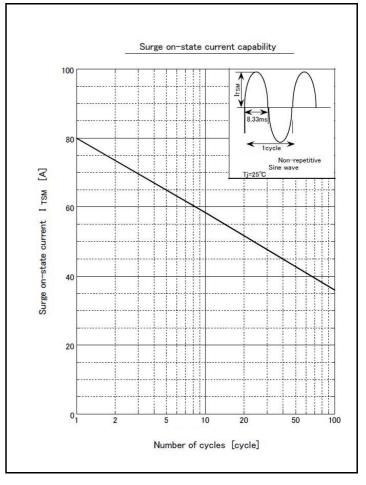
Electrical Characteristics (unless otherwise specified : Tc=25°C)

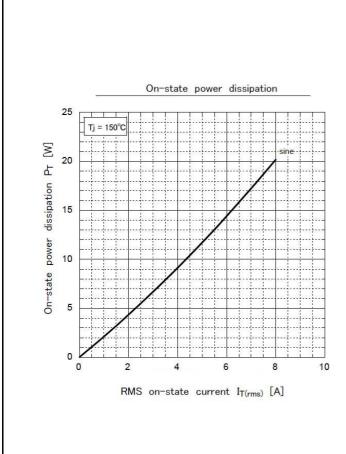
Item	Symbol	Conditions		Ratings		
			MIN	TYP	MAX	Unit
Off-state current	I _{DRM}	VD=600V, Pulse measurement			10	μA
On-state voltage	V_{TM}	ITM=12A, Pulse measurement			1.6	V
Gate trigger voltage	V_{GTI}	VD=6V, RL=10Ω, T1-, T2+, G+			1.5	V
Gate trigger voltage	V _{GTII}	VD=6V, RL=10Ω, T1-, T2+, G-			1.5	V
Gate trigger voltage	V _{GTIII}	VD=6V, RL=10Ω, T1+, T2-, G-			1.5	V
Gate trigger voltage	V_{GTIV}	VD=6V, RL=10Ω, T1+, T2-, G+			- *	V
Gate non-trigger voltage	V_{GD}	Tj=150°C, VD=1/2VDRM	0.1			V
Gate trigger current	I _{GTI}	VD=6V, RL=10Ω, T1-, T2+, G+			30	mA
Gate trigger current	I _{GTII}	VD=6V, RL=10Ω, T1-, T2+, G-			30	mA
Gate trigger current	I _{GTIII}	VD=6V, RL=10Ω, T1+, T2-, G-			30	mA
Gate trigger current	I _{GTIV}	VD=6V, RL=10Ω, T1+, T2-, G+			- *	mA
Latching current	ILI	IG=0.1A, T1-, T2+, G+			100	mA
Latching current	I _{LII}	IG=0.1A, T1-, T2+, G-			100	mA
Latching current	I _{LIII}	IG=0.1A, T1+, T2-, G-			100	mA
Latching current	I _{LIV}	IG=0.1A, T1+, T2-, G+			- *	mA
Holding current	lΗ	ITM=1A			100	mA
Critical rate of rise of off-state voltage	dv/dt	Tj=150°C,VD=2/3VDRM	100			V/µs
Critical rate of rise of commutating voltage	(dv/dt)c	Tj=150°C, VD=2/3VDRM, (di/dt)c=-4A/ms	1			V/µs
Thermal resistance	Rth(j-c)	Junction to case with heatsink			1.95	°C/W

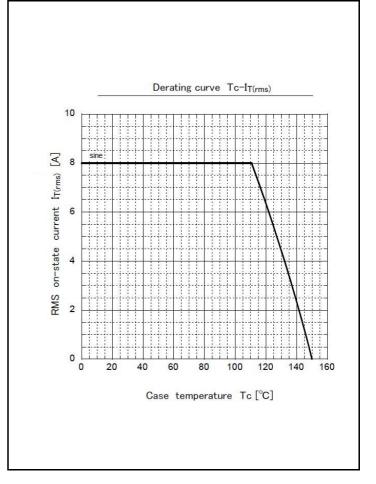
st :See the original Specifications

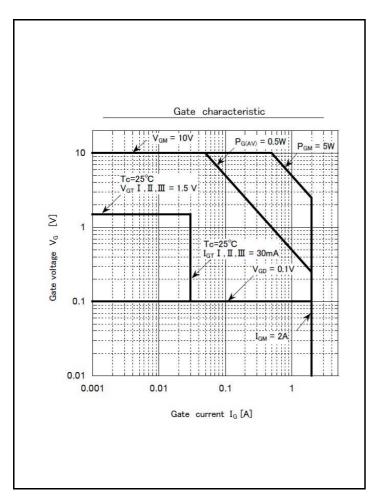
CHARACTERISTIC DIAGRAMS

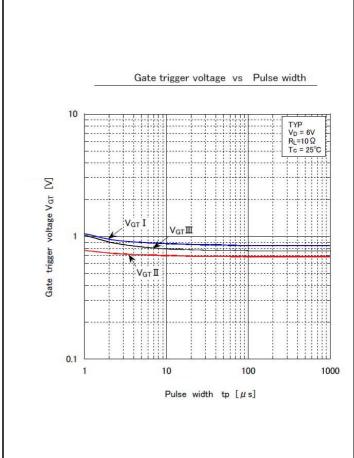


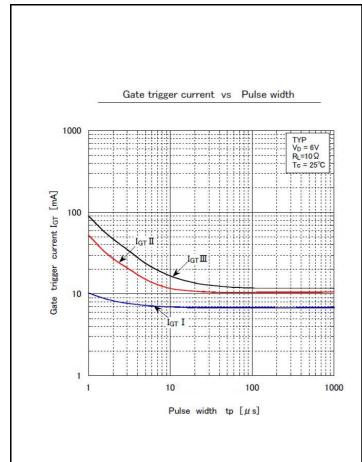


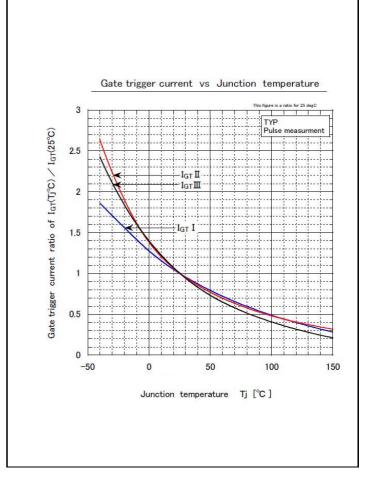


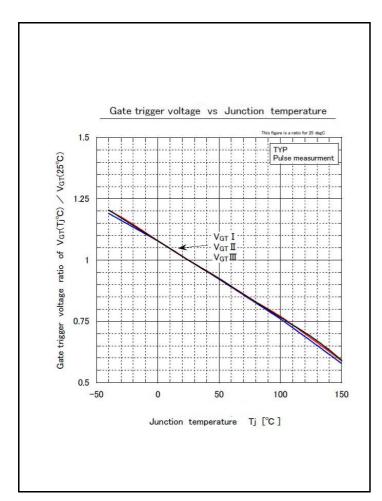


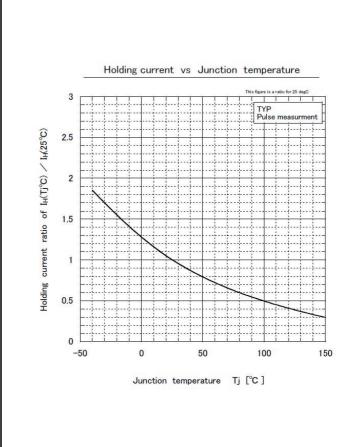


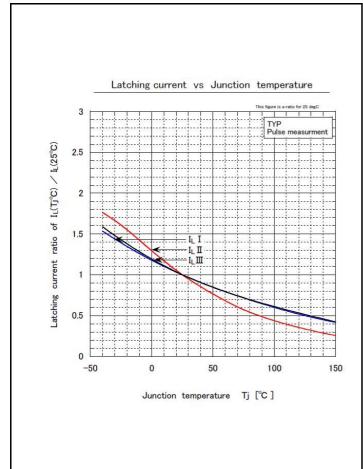


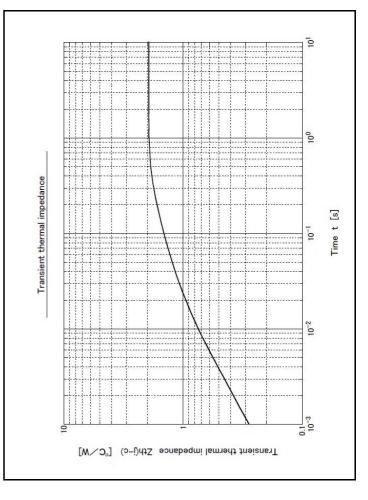








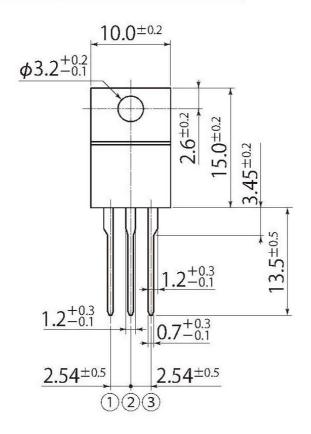


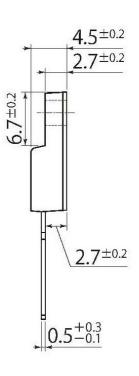


unit:mm

J8

JEDEC Code	_		
JEITA Code	SC-91		
House Name	FTO-220AG(3pin)		





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